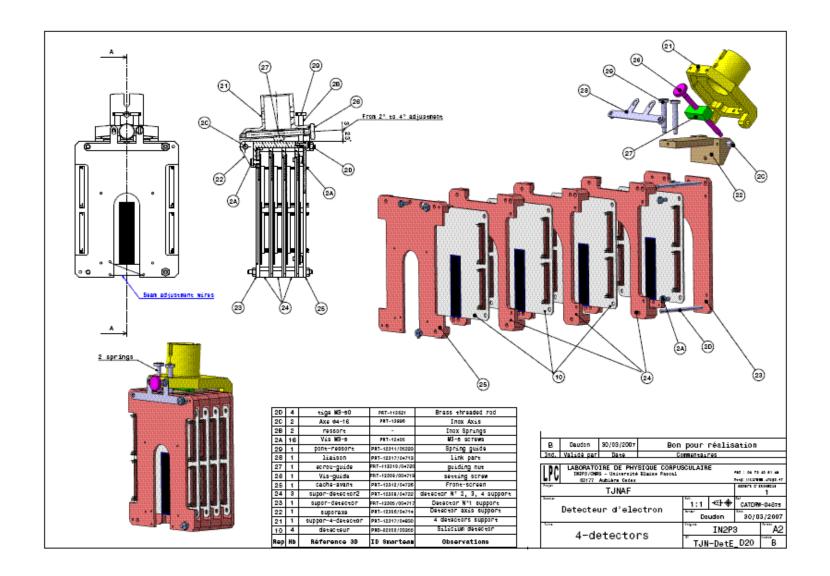
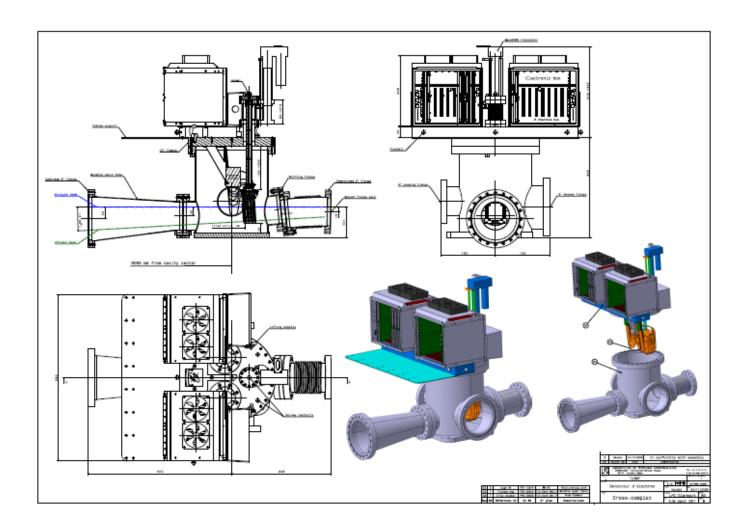
# Hall A Compton Electron status and plan

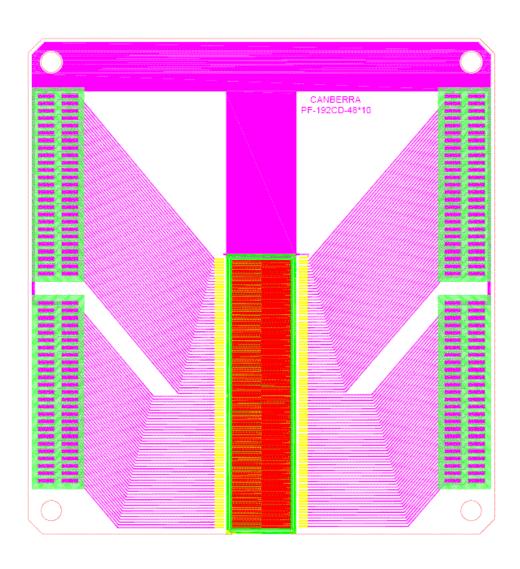
Alexandre Camsonne August 18<sup>th</sup> 2017

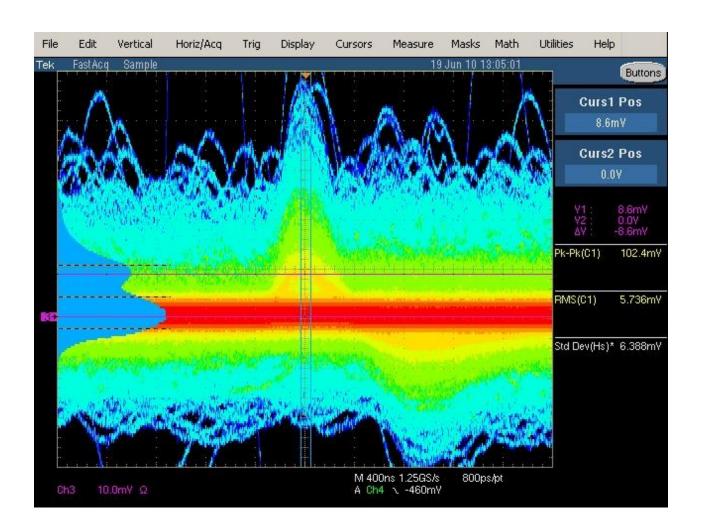
## Electron detector schematic

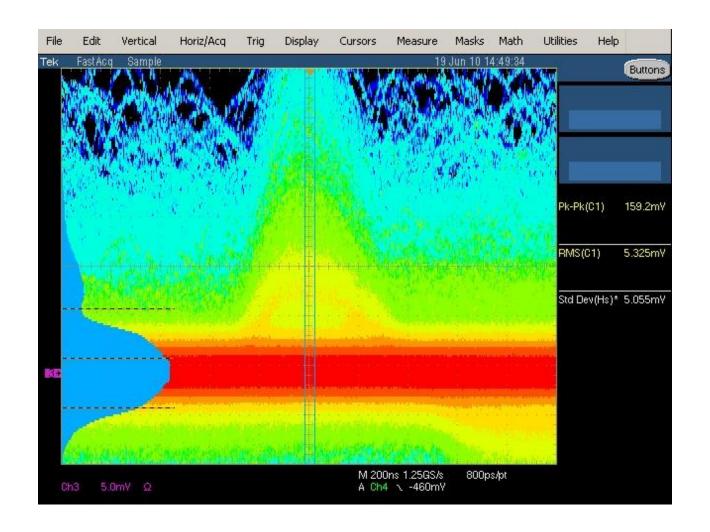


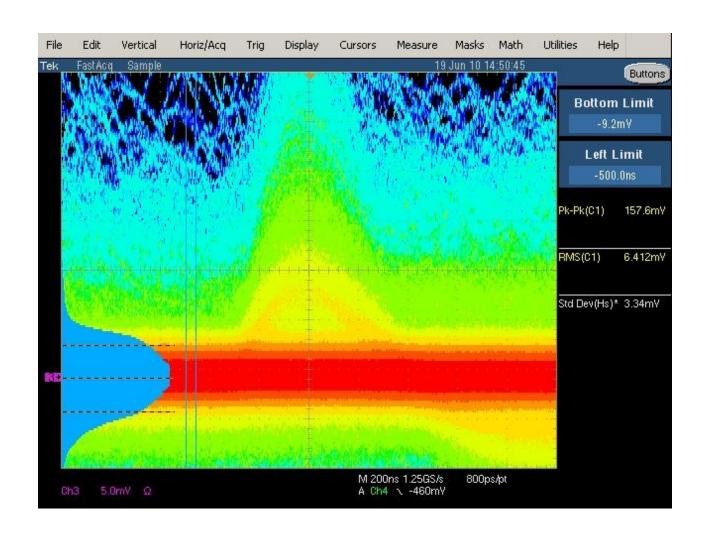


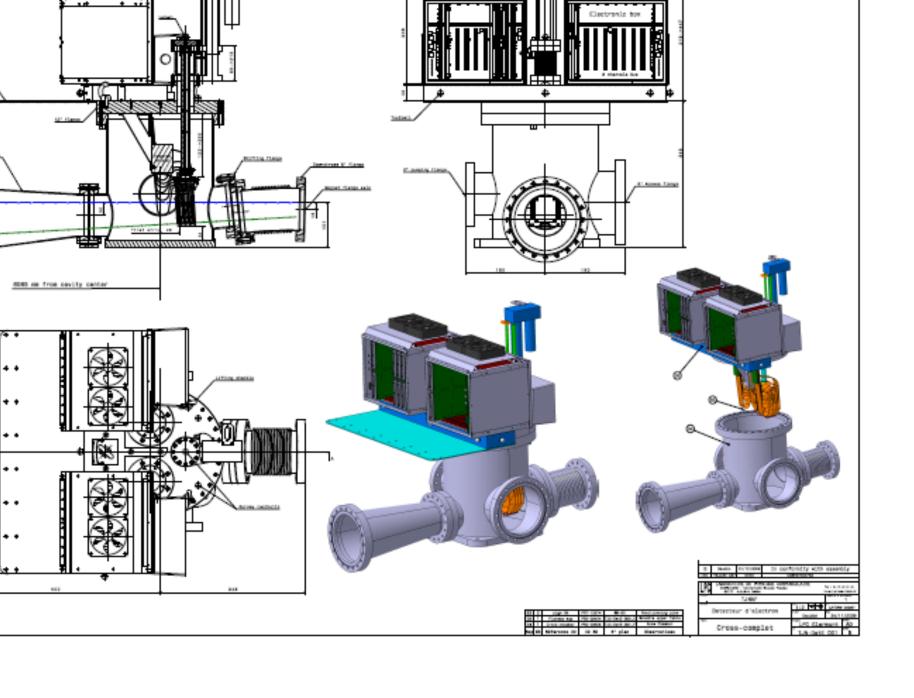
## Electron detector schematic











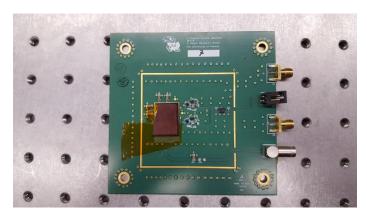
## Possible solutions

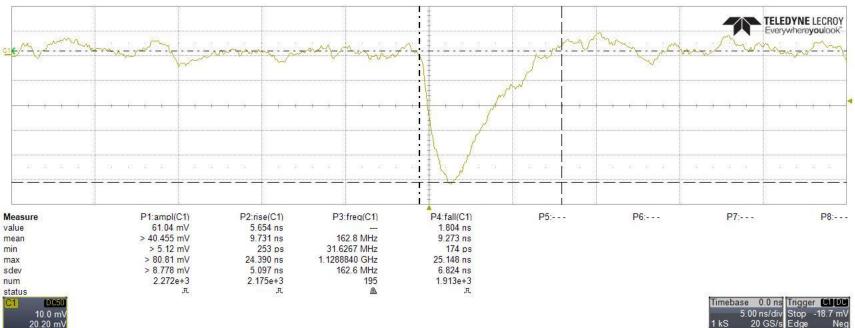
Preamp on detector

Redo scattering chamber to reduce capacitance

Thicker detector

## Kansas University preamp





X1= -600 ps  $\Delta$ X= 8.55 ns X2= 7.95 ns 1/ $\Delta$ X= 117.0 MHz

## Preamp option

- Discussed with Fernando , Chris and Nicola Minafra
- ASIC needs ressources, need other project to develop and several years
- Discrete option possible but 96 channels only 192 channels per plan most likely wont fit

## **Electronics**

- Older French electronics
- VETROC
  - 1 VXS crate
  - 6 VETROC for 768 channels
- Borrowing 2<sup>nd</sup> HCAL VTP for triggering

Hall D chamber ASIC : 8 channel amplifier discriminator

## Additionnal electronics

- Trigger readout
  - VTP 7K\$
  - Adapter from 50 pins to 26 pins to plug detector (1K \$ + design )

- Front end
  - Might switch to Hall D A/D if better signal to noise ratio / crosstalk

## Plan

- Move detector back as is after tritium
- Might upgrade to thick plane if funding available

- Run with VETROC readout
- Longer term: redo scattering chamber to be similar to Hall D design and have cooling and low voltage to test detector preamp option, HVMAPS option being looked by UManitoba