

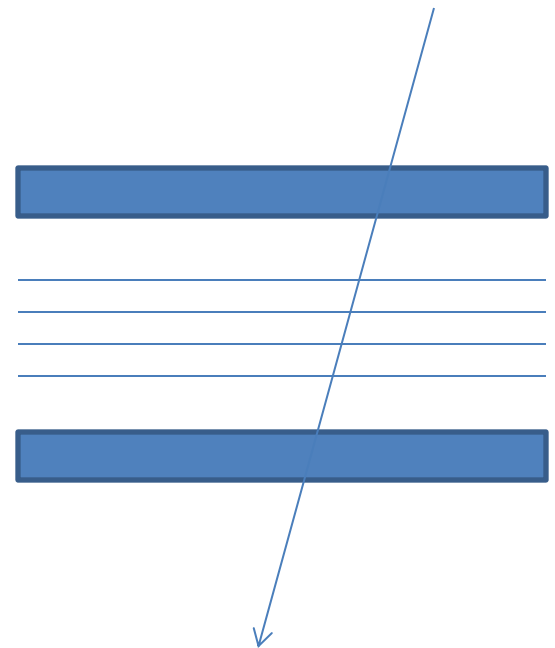
Compton Edet DAQ

VETROC

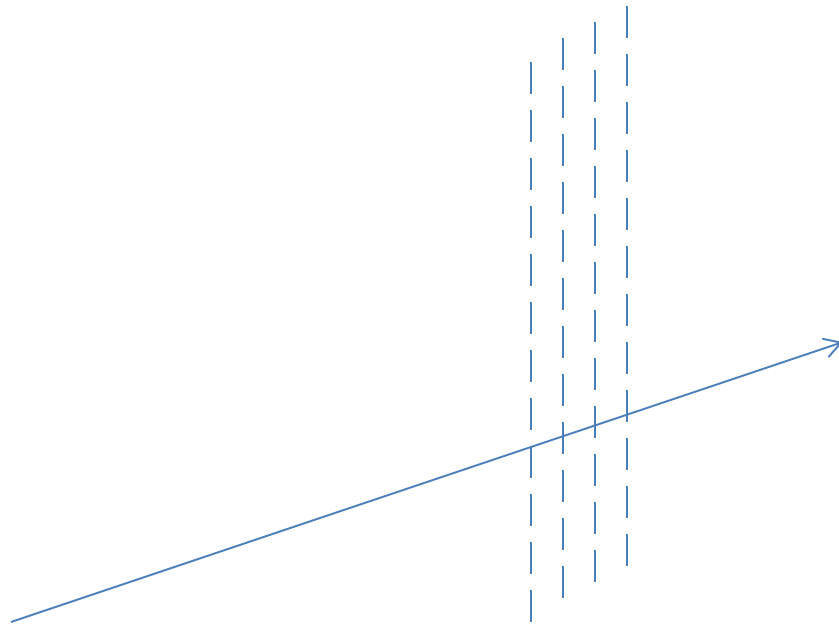
- Issues with French trigger (empty events)
- Will use VETROC as TDC
- 3 VETROC = 3 * 192
- VME64X crate : needs fan
- Intel CPU from DVCS
- Old TI

Cosmics test

- Test detector efficiency
- Thick plane
- New Hall D front end
- External scintillator trigger
- standard VETROC firmware



Compton Edet trigger



A B C D

Coincidence between 2,3
or 4 planes

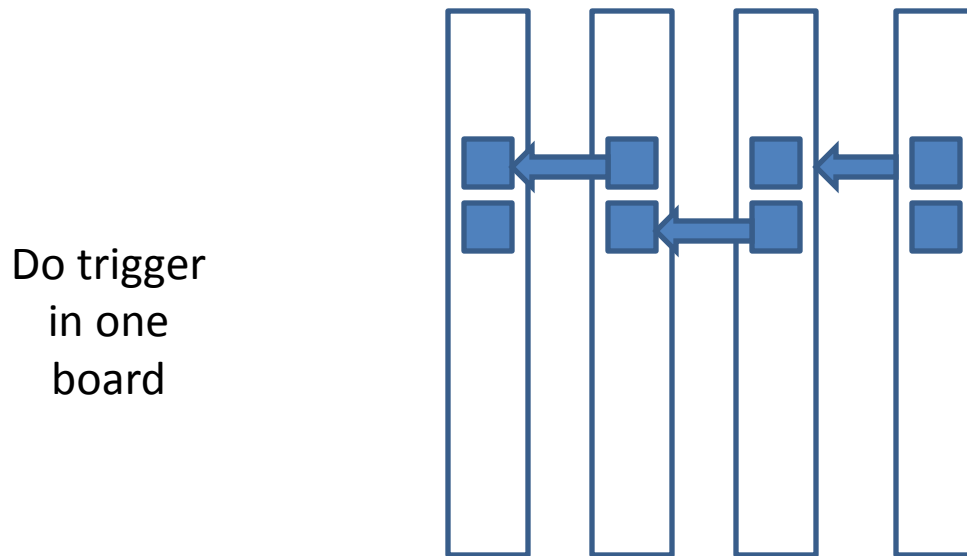
matching $A_i B_i$

larger Region

A_i and B_{i+1}

Poor man's trigger

- VETROC only



- Pro
 - work in cheap VME crate
- Cons
 - less ressource than VTP
 - less flexibility than VTP
 - more development