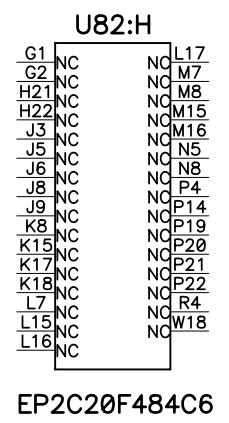


This Header is used for programming the EP2C20



<b>TITLE</b>			
<b>HIGH RESOLUTION 18-BIT ADC</b>			
<b>Hall A HAPPEX</b>			
FJ Barbosa, E. Jastrzembki, J. Wilson			
<b>FPGA PROGRAMING</b>			
<b>SIZE</b>	<b>CAD I.D. NO.</b>	<b>DRAWING NO.</b>	<b>REV.</b>
<b>B</b>		<b>18-BIT_ADC_V2_1.SCH</b>	
<b>SCALE</b>	<b>SHOWN ON</b>	<b>SHEET OF</b>	<b>REV.</b>
		<b>16</b>	<b>17</b>

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