

SBS DAQ

- **HCAL**
 - FADC based DAQ
 - Digital trigger
 - 288 channels : 2 VXS crates , Crate processor
 - High resolution timing for Gen, GMn
 - Discussion with Chris about CTP , FADC procurement
 - Hall B Trigger module specifications seems to meet specification
 - Advise to go with Wiener so we are standard with lab and have easy access to spare
- **ECAL**
 - Fastbus ADC
 - Analog sum trigger : ~1800 channels
- **BigBite**
 - Fastbus ADC
 - 243 shower channels, analog sum trigger
 - Scintillator 180 channels
 - Cerenkov using NINO : 550 channels
- **Cdet**
 - 3700 channels timing only , NINO + Fastbus

SBS DAQ

- GEM
 - INFN MPD
 - On site
 - Working integration with intel CPU and CODA
 - One SULI student Jessie Twigger will help
- Fastbus :
 - ADC,TDC tested : seems to have enough
 - Weldment in Test Lab : start using ?
 - Shipment to SMU still on hold will check with new property management