
Minutes: SBS Meeting October 7, 2015

Agenda: Simona - Update on ECAL Readout and Equipment Inventory
Scott - Reports on VETROC Tests

Attendees: Brian Quinn, Gregg Franklin, John Annand, Rachel Montgomery,
Mark Jones, Mitra Shabestari, Dasuni Adikaram, Scott Barcus,
Simona Malace, Bob Michaels, Gordon Cates, Vina Punjabi,
Alexandre Camsonne, Seamus Riordan, Andrew Puckett,
Nilanga Liyanage, Bogdan Wojtsekhowski.

Simona - ECAL Readout and Equipments

- general overview of the ECAL
- some blocks are on-site and plan is to bring the rest within a month
- discusses the blocks simulations and layout progress
- reviews requirement/status of front-end electronics, cables, high voltage, and DAQ

- addressing Brian's question regarding signals from the modules, explains that they split signal using the pins on the back of modules
- Andrew points out that there were a logical mistake in their simulation and the cluster size in slide 6 should be 5X5
- John expresses his concern about the age of the front-end electronics and Mark mentions that the budget has been set based on using old electronics
- Gordon suggest to test the existing electronics under realistic conditions

Scott - VTROC Tests report

- presents the results of various measurements and tests on VTROC board (trigger test, speed/dead-time measurements)
- explaining TDC data and sensitivity/resolution check
- On-going projects:
 - create more complex GTP triggers with FPGA
 - test with many channels on W&M prototype detector
 - further development of analysis software

- Gregg asks about changing time resolution to picosecond. Alex comments that they could try to check the resolution rate.