
Minutes: SBS Meeting April 27, 2016

Agenda: Brian - HCal electronics
Francesco - CDet module commissioning & DAQ preparation

Attendees: Brian Quinn, John Annand, Francesco Tortorici, Gregg Franklin,
Mark Jones, Mitra Shabestari, Dasuni Adikaram, Andrew Puckett,
Tommy Miller, Peter Monaghan, Simona Malace, Alexandre Camsonne.

Brian - HCal Electronics, with First-level ECal trigger

- Explaining:
 - Logic signal scheme sent to ECal
 - Cabling scheme for HCal
- Suggesting module-naming convention
- Making suggestion for cabling pattern
- Discussing UVA summing modules
- Discussing test results of summing module
- To Do / open questions
 - Continue discussion to see whether modest custom module/board is practical
 - Consider construction of patch-panel/asymmetric splitter
 - Understand 16-input summing circuits
 - Decide whether amplifiers are needed
 - Consider options for housing/power for NINO cards

Francesco - CDet module commissioning update

- New hardware: power supply distributor for NINO cards
- Results of studying duration of pulse vs amplitude
- Results of testing of ADC channels
- New software tool: number of photoelectrons
- Showing the results obtained using new software.