

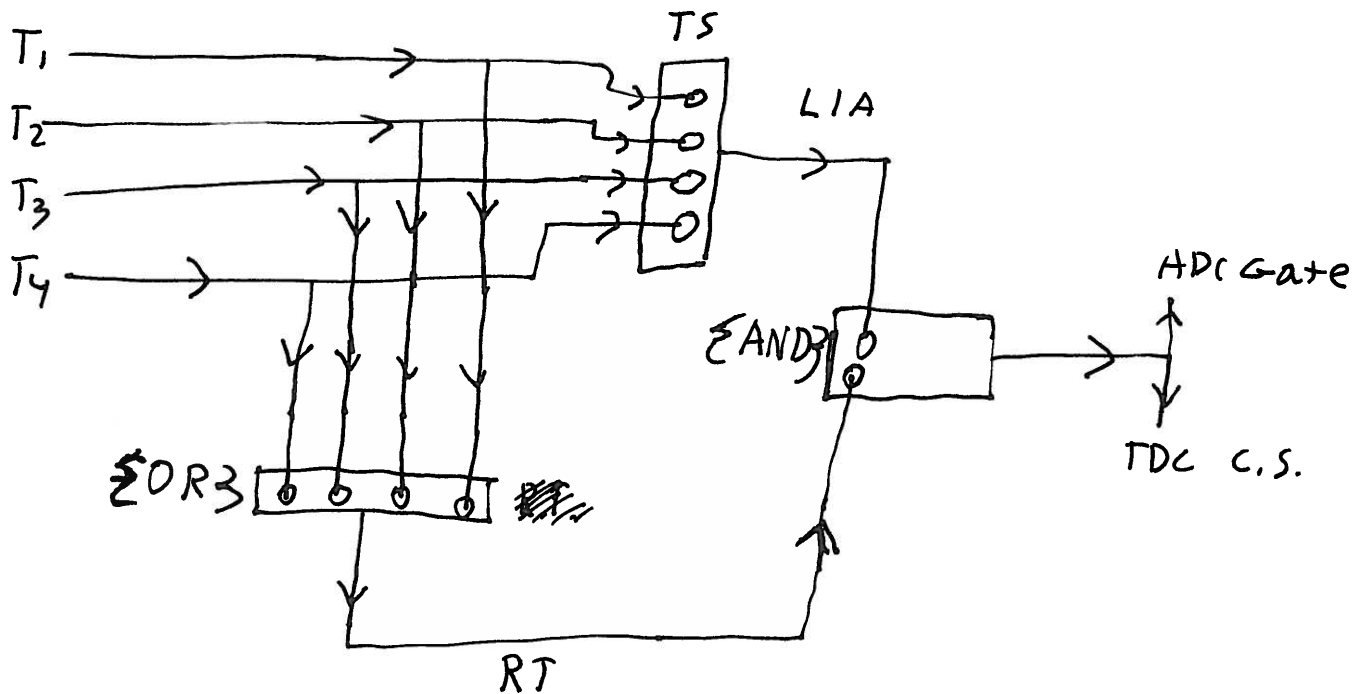
Trigger Timing/Logic

T1: S ϕ and S2

T2: S ϕ and GC

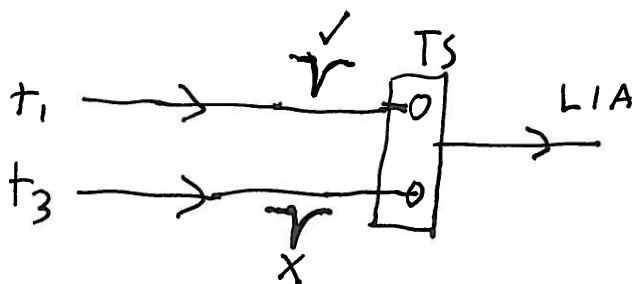
T3: ~~S ϕ and GC~~ S2 and GC

T4: S ϕ and cal.



RT signal should arrive at {AND} ~~before~~ after LIA

consider cases where both t₁ and t₂ have a ~~pulse~~ signal, but prescales are larger than 1



IS LIA signal delayed when trigger supervisor decides whether to reject t₃ pulse? If so, LIA will ~~be~~ arrive to {AND} late, and will carry the time.