CORRESPONDENCE TABLE FOR LEFT-ARM PION-REJECTOR

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The left-arm pion rejector is consisted of two layers of lead glass blocks, 34 blocks for each layer. On each layer, 17 blocks on the left side and 17 blocks on the right side. As one look following a particles direction, block PiRL001 is at the lower momentum left-side, while PiRL0017 is at the higher momentum side left-side, PiRR001 is at the lower momentum right-side, while PiRR0017 is at the higher momentum right-side. For the second layer, PiRL0018 is on lower momentum left-side, while PiRL0034 is on high momentum left-side, PiRR0018 is on lower momentum right-side, PiRR0034 is on high momentum right-side.

Block Label	HV Channel	HV Setting	Amplifier Channel	Patch Panel	ADC Channel	Espace Variable	Notes
PiRL001	3.0	-1633	1-A1	A1	23:0	PRL1.ADC1	
PiRL002	3.1	-1587	1-A2	A2	23:1	PRL1.ADC2	
PiRL003	3.2	-1624	1-A3	A3	23:2	PRL1.ADC3	
PiRL004	3.3	-1449	1-A4	A4	23:3	PRL1.ADC4	
PiRL005	3.4	-1757	1-A5	A5	23:4	PRL1.ADC5	
PiRL006	3.5	-1355	1-A6	A6	23:5	PRL1.ADC6	
PiRL007	3.6	-1858	1-A7	A7	23:6	PRL1.ADC7	
PiRL008	3.7	-1532	1-B1	A8	23:7	PRL1.ADC8	
PiRL009	3.8	-1612	1-B2	A9	23:8	PRL1.ADC9	
PiRL010	3.9	-1422	1-B3	A10	23:9	PRL1.ADC10	
PiRL011	3.10	-1755	1-B4	A11	23:10	PRL1.ADC11	
PiRL012	3.11	-1384	1-B5	A12	23:11	PRL1.ADC12	
PiRL013	4.0	-1862	1-B6	A13	23:12	PRL1.ADC13	
PiRL014	4.1	-1433	1-B7	A14	23:13	PRL1.ADC14	
PiRL015	4.2	-1901	2-A1	A15	23:14	PRL1.ADC15	
PiRL016	4.3	-1600	2-A2	A16	23:15	PRL1.ADC16	
PiRL017	4.4	-1910	2-A3	B1	23:16	PRL1.ADC17	
PiRR001	4.5	-1568	2-A4	B2	23:17	PRL1.ADC18	
PiRR002	4.6	-1979	6-A1	B3	23:18	PRL1.ADC19	
PiRR003	4.7	-1542	2-A6	B4	23:19	PRL1.ADC20	
PiRR004	10.8	-1964	2-A7	B5	23:20	PRL1.ADC21	special HV cable
PiRR005	4.9	-1533	2-B1	B6	22:36	PRL1.ADC22	
PiRR006	4.10	-1868	6-A2	B7	23:22	PRL1.ADC23	
PiRR007	4.11	-1512	3-A6	B8	23:23	PRL1.ADC24	
PiRR008	5.0	-1920	2-B4	B9	23:24	PRL1.ADC25	
PiRR009	5.1	-1411	2-B5	B10	23:25	PRL1.ADC26	
PiRR010	5.2	-1673	2-B6	B11	23:26	PRL1.ADC27	
PiRR011	5.3	-1412	2-B7	B12	23:27	PRL1.ADC28	
PiRR012	5.4	-1711	3-A1	B13	23:28	PRL1.ADC29	
PiRR013	5.5	-1513	3-A2	B14	23:29	PRL1.ADC30	
PiRR014	5.6	-1810	3-A3	B15	23:30	PRL1.ADC31	
PiRR015	5.7	-1601	3-A4	B16	23:31	PRL1.ADC32	
PiRR016	5.8	-1850	3-A5	C1	23:32	PRL1.ADC33	
PiRR017	5.9	-1708	6-A3	C2	23:33	PRL1.ADC34	

Block Label	HV Channel	HV Setting	Amplifier Channel	Patch Panel	ADC Channel	Espace Variable	Notes
PiRL018	5.10	-1523	3-B2	C3	23:34	PRL2.ADC1	
PiRL019	5.11	-1687	3-B3	C4	23:35	PRL2.ADC2	
PiRL020	6.0	-1539	3-B4	C5	23:36	PRL2.ADC3	
PiRL021	6.1	-1782	3-B5	C6	23:37	PRL2.ADC4	
PiRL022	6.2	-1531	3-B6	C7	23:38	PRL2.ADC5	
PiRL023	6.3	-1601	3-B7	C8	23:39	PRL2.ADC6	
PiRL024	6.4	-1459	4-A1	C9	23:40	PRL2.ADC7	
PiRL025	6.5	-1890	4-A2	C10	23:41	PRL2.ADC8	
PiRL026	6.6	-1490	4-A3	C11	23:42	PRL2.ADC9	
PiRL027	6.7	-1765	4-A4	C12	23:43	PRL2.ADC10	
PiRL028	6.8	-1451	4-A5	C13	23:44	PRL2.ADC11	
PiRL029	6.9	-1856	4-A6	C14	23:45	PRL2.ADC12	
PiRL030	6.10	-1561	4-A7	C15	23:46	PRL2.ADC13	
PiRL031	6.11	-2002	4-B1	C16	23:47	PRL2.ADC14	
PiRL032	9.0	-1609	4-B2	D1	23:48	PRL2.ADC15	
PiRL033	9.1	-1967	4-B3	D2	23:49	PRL2.ADC16	
PiRL034	9.2	-2147	4-B4	D3	23:50	PRL2.ADC17	
PiRR018	9.3	-1493	4-B5	D4	23:51	PRL2.ADC18	
PiRR019	9.4	-1467	4-B6	D5	23:52	PRL2.ADC19	
PiRR020	9.5	-1713	4-B7	D6	23:53	PRL2.ADC20	
PiRR021	9.6	-1474	5-A1	D7	23:54	PRL2.ADC21	
PiRR022	9.7	-1891	5-A2	D8	23:55	PRL2.ADC22	
PiRR023	9.8	-1572	5-A3	D9	23:56	PRL2.ADC23	
PiRR024	9.9	-1730	5-A4	D10	23:57	PRL2.ADC24	
PiRR025	9.10	-1586	5-A5	D11	23:58	PRL2.ADC25	
PiRR026	9.11	-1804	5-A6	D12	23:59	PRL2.ADC26	
PiRR027	10.0	-1634	5-A7	D13	23:60	PRL2.ADC27	
PiRR028	10.1	-1810	5-B1	D14	23:61	PRL2.ADC28	
PiRR029	10.2	-1494	5-B2	D15	23:62	PRL2.ADC29	
PiRR030	10.3	-2009	5-B3	D16	23:63	PRL2.ADC30	
PiRR031	10.4	-1731	5-B4	E1	22:32	PRL2.ADC31	
PiRR032	10.5	-1722	5-B5	E2	22:33	PRL2.ADC32	
PiRR033	10.6	-1446	5-B6	E3	22:34	PRL2.ADC33	
PiRR034	10.7	-1686	5-B7	E4	22:35	PRL2.ADC34	