Scaler Inputs

LHRS:

For both scalers, free running (5th scaler) and normalization (2nd scaler), inputs also to TDC, usage of custom made NIM-ECL module at right rack)

- 1-3: LHRS Single arm triggers
- 4-5: Empty or Single arm triggers from MLU as crosscheck
- 6: L1A Remote Signal (from RHRS)
- 7: Clock RHRS
- 8: Clock LHRS
- 9: empty
- 10: (S0 || S2)_{LHRS}
- 11: LHRS S2_L Signal
- 12: LHRS S2_R Signal
- 13: (S0 || S2)_{RHRS} (Retiming Signal from RHRS)
- 14: ADC gate LHRS
- 15: L1A LHRS (important for independent spectrometers)
- 16: empty

BCM signals only to the normalization scaler (channel 17-23, 2nd scaler) Rest of the signals (S2m, S0, GC) in same scalers

RHRS Single arm mode:

For both scalers, free running (2nd scaler) and normalization (4th scaler, exchanged by SIS Scaler from LHRS), inputs also to TDC, usage of custom made NIM-ECL module at right rack

- 1-3: LHRS Single arm triggers
- 4-6: RHRS Single arm triggers
- 7: Clock RHRS
- 8: Clock LHRS
- 9: L1A remote signal (to LHRS)

- 10: Clock RHRS
- 11: RHRS S2_L Signal
- 12: RHRS S2_R Signal
- 13: (S0 || S2)_{RHRS}
- 14: ADC gate RHRS
- 15: L1A RHRS
- 16: FastBus Remote (from LHRS)

BCM signals only to the normalization scaler (channel 17-23, 4th scaler) S2, S0 and GC same as before

RHRS Coincidence mode:

For both scalers, free running (2nd scaler) and normalization (4th scaler, exchanged by SIS Scaler from LHRS), inputs also to TDC, usage of custom made NIM-ECL module at right rack

- 1-3: LHRS Single arm triggers
- 4-6: Coincidence triggers
- 7: (S0&S2) RHRS
- 8: Clock LHRS
- 9: L1A remote signal (to LHRS)
- 10: Clock RHRS
- 11: RHRS S2_L Signal
- 12: RHRS S2_R Signal
- 13: (S0 || S2)_{RHRS}
- 14: ADC gate RHRS
- 15: L1A RHRS
- 16: FastBus Remote (from LHRS)

BCM signals only to the normalization scaler (channel 17-23, 4th scaler) S2, S0 and GC same as before