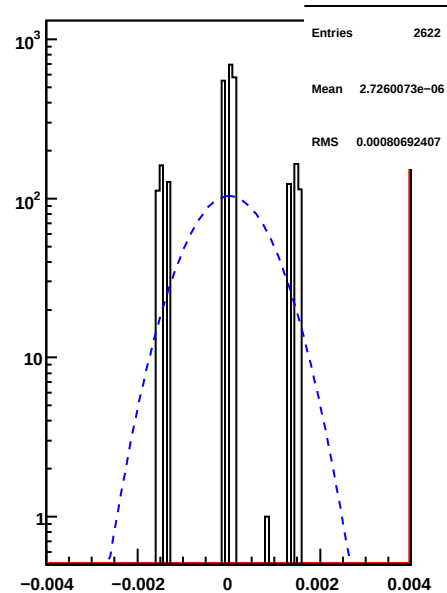


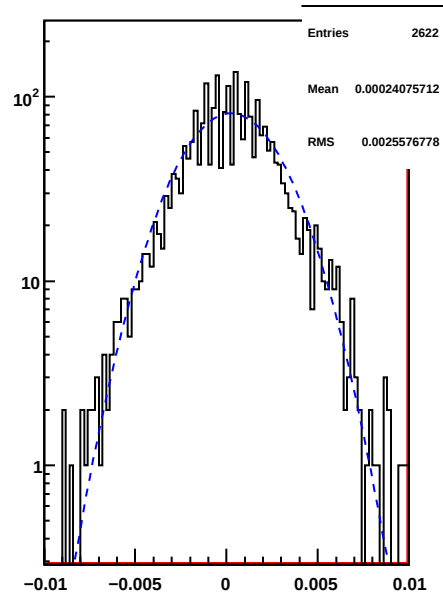
Elec_n asym

h1



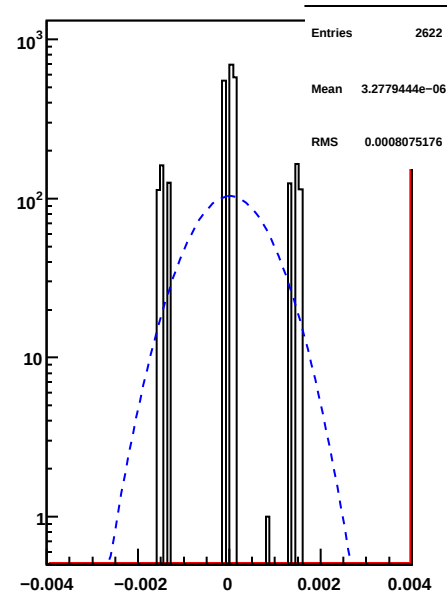
Pion_n asym

h2



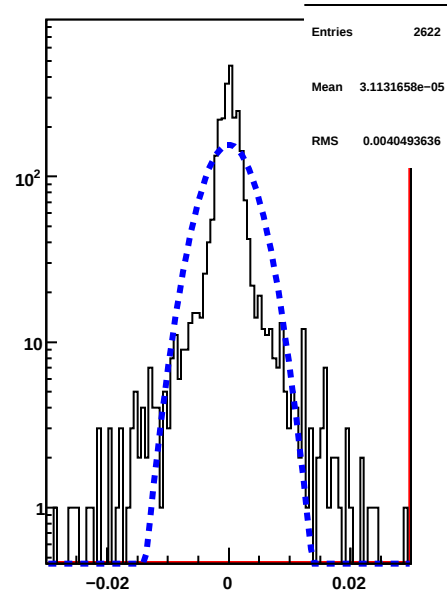
Elec_w asym

h3



Scint asym

h4



Clock asym

h5

