

DAQ Status Update

10/12/11

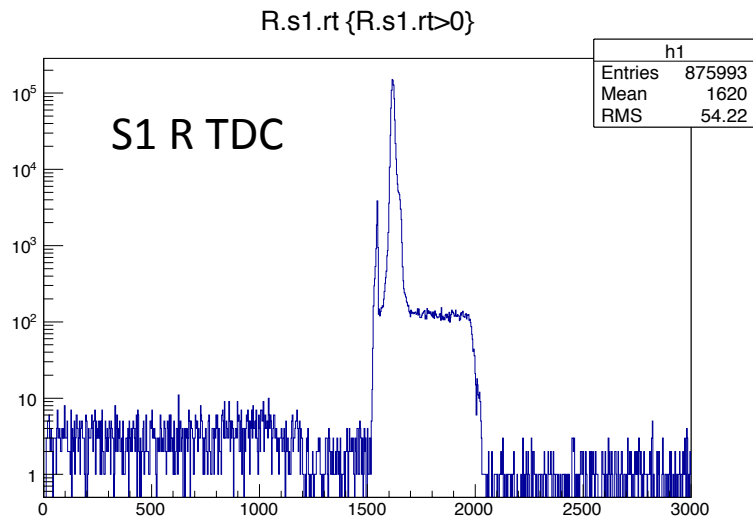
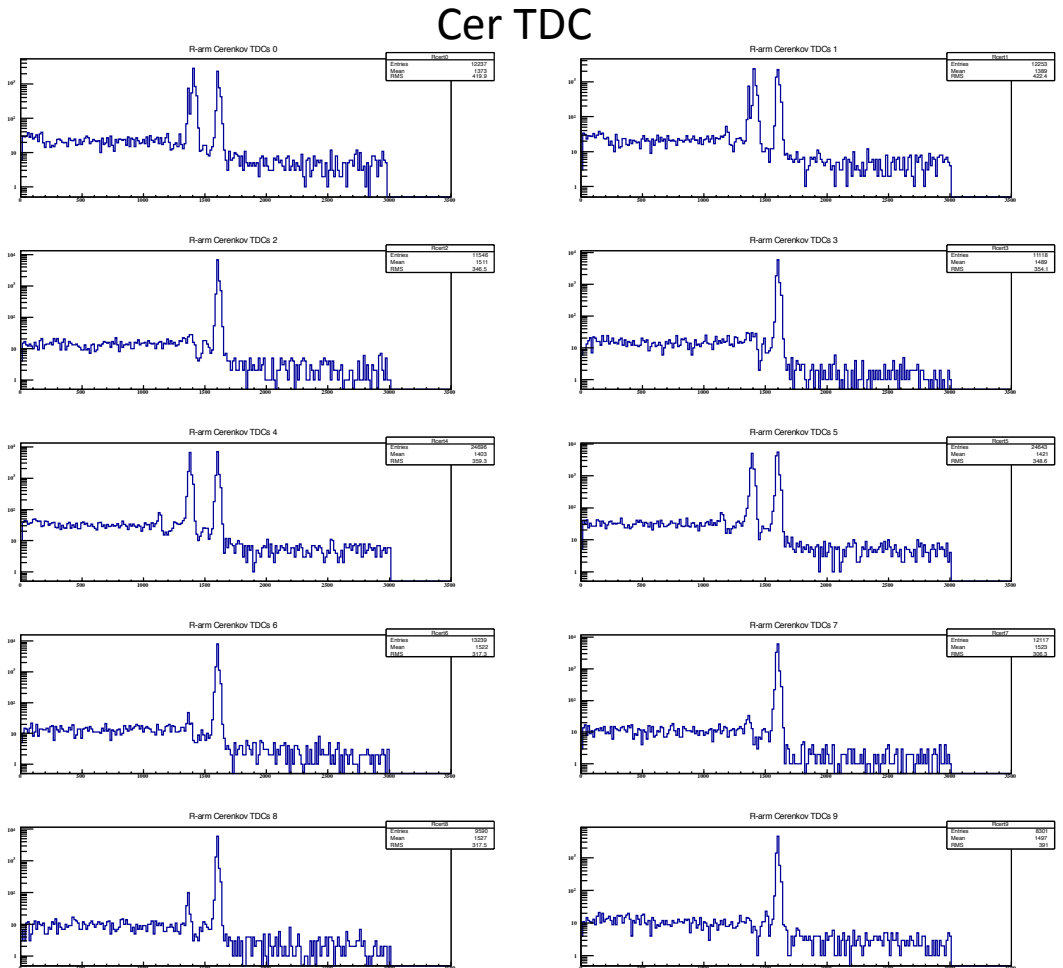
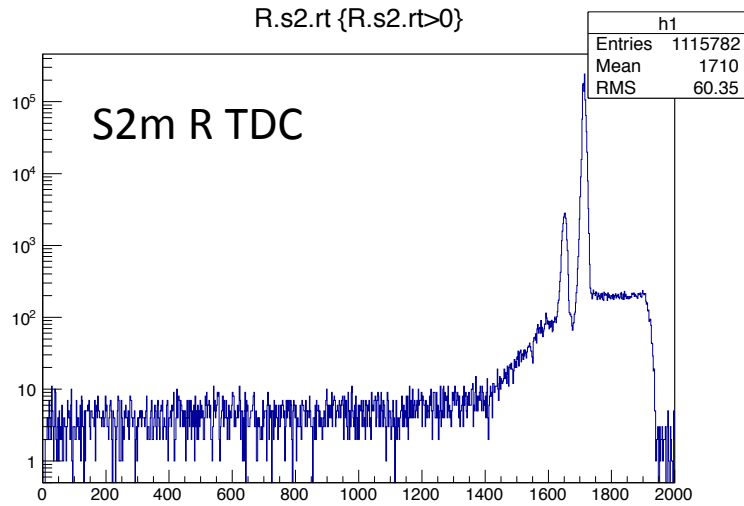
LHRS

- Replacing FIVE S1 PMTs sometime this week
 - 5L, 6L, 2R, 5R, 6R
 - Hopefully can take cosmic run over weekend with new PMTs
- Jack start install 3rd FASTBUS crate next week
- Melissa has Cerenkov analysis

RHRS

- Finished detector check-out of cosmic run
 - No ADC signals on Shower 75 and PreShower 18R
 - All S1 TDC spectrum show two peaks
 - Some S2m/Cer TDC spectrum show two peaks

TDC Spectrum



Intel CPU

- Intel CPU installed in ROC1 on RHRS
- Uses “RightIntel” configuration in CODA
- Currently set up to read-out 1877 TDC
 - Library does not exist for 1881 ADC
- Successfully writes to data stream for a block of 5 TDCs
- Next step is add second block of TDCs to output
 - After that need to add 1881 ADC