

LHRS Scaler Setup

Physical Slot #	Module (Total Channels)	Signal	Channels
4	Lecroy 1151 (16)	empty	-
5	SIS 36/38 SX (32)	Triggers/BCM (helicity gated)	Next slide
6	SIS 36/38 SX (32)	Triggers/BCM	Next slide
7	SIS 36/38 SX (32)	S1	S1L:Ch.1-6 S1R:Ch.9-14 Ch.7,8,15,16 empty
8	SIS 36/38 SX (32)	S2m	S2mL:Ch.1-16 S2mR:Ch.17-32
9	SIS 36/38 SX (32)	Cherenkov	Ch.1-10 cer_asum Ch.11
10	NIM (32)	104kHz Fast clock	Ch.1

LHRS Trigger Scalers

Channel	Signal	Status
0	T1 (S1+S2 from RHRS)	No signal
1	T2 (S1+Cer or S2+Cer from RHRS)	No signal
2	T3 (S1+S2 from LHRS)	working
3	T4 (S1+Cer or S2+Cer from LHRS)	working
4	T5	empty
5	T6	empty
6	T7 (Fast clock)	working
7	T8-clock	working
8	R_strobe	empty
9	Fast clock	working
10	EDTM	empty
11	ADC_gate	empty
12	L1A	empty
13	L_strobe	working
14	S1	working
15	S2	working

RHRS Scaler Setup

Physical Slot #	Module (Total Channels)	Signal	Channels
4	SIS 36/38 SX (32)	Triggers/BCM (helicity gated, added)	Next slide
5	SIS 36/38 SX (32)	Triggers/BCM (added)	Next slide
6	empty	-	-
7	empty	-	-
8	empty	-	-
9	Lecroy 1151E (16)	S1 (moved from slot 11)	S1L:Ch.1-6 S1R:Ch.9-14 Ch.7,8,15,16 empty
10	Lecroy 1151E (16)	S2mR	Ch.1-16
11	Lecroy 1151E (16)	S2mL (moved from slot 5)	Ch.1-16
12	V560E (16)	Cer	Ch.1-10 cer_asum Ch.11
13	V560E (16)	-	-

RHRS Trigger Scalers

Channel	Signal	Status
0	T1 (S1+S2 from RHRS)	Working
1	T2 (S1+Cer or S2+Cer from RHRS)	No signal
2	T3 (S1+S2 from LHRS)	No signal
3	T4 (S1+Cer or S2+Cer from LHRS)	No signal
4	T5	empty
5	T6	empty
6	T7	No signal
7	T8-clock	working
8	R_strobe	working
9	Fast clock	working
10	EDTM	empty
11	ADC_gate	empty
12	L1A	empty
13	L_strobe	empty
14	S1	working
15	S2	working

- Lecroy 1151's on RHRS need logical gate to output data. Currently using NIM-bar as gate.
- T2 (S1+Cer or S2+Cer for RHRS) needs to be set up
- BCM signals for scalers or exclusively in HAPPEX crate.