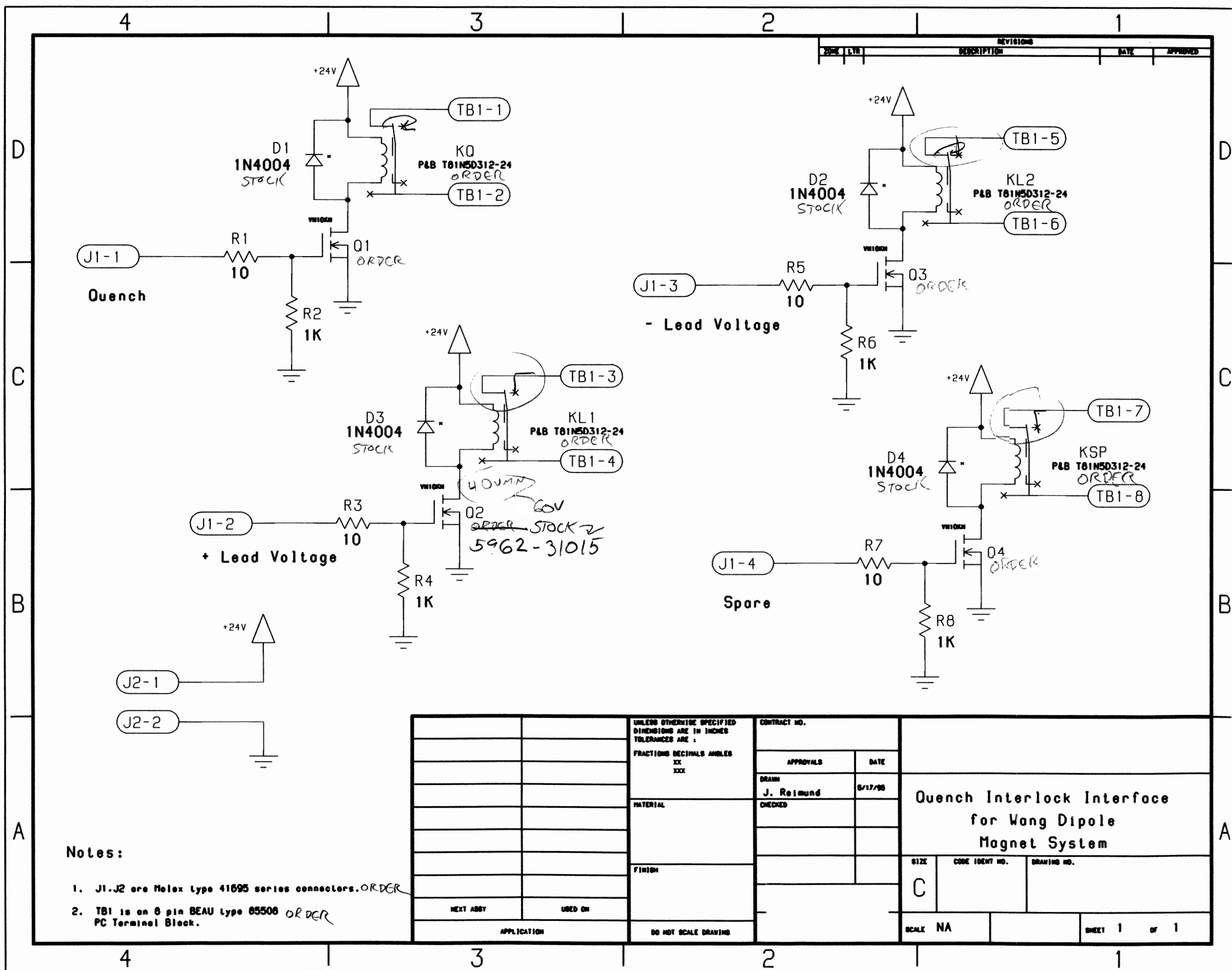


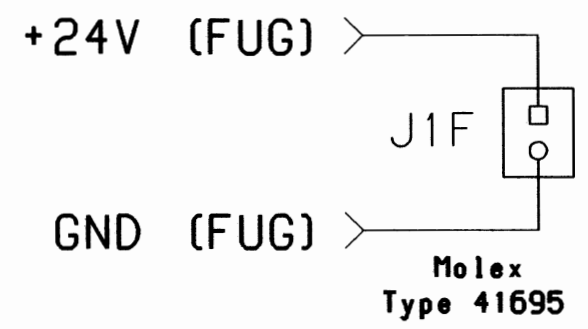
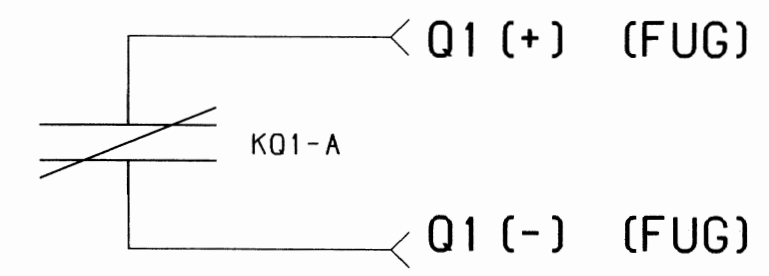
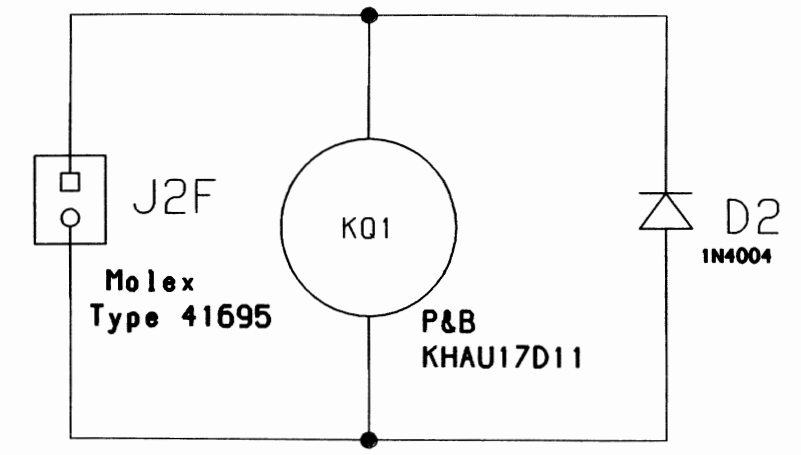
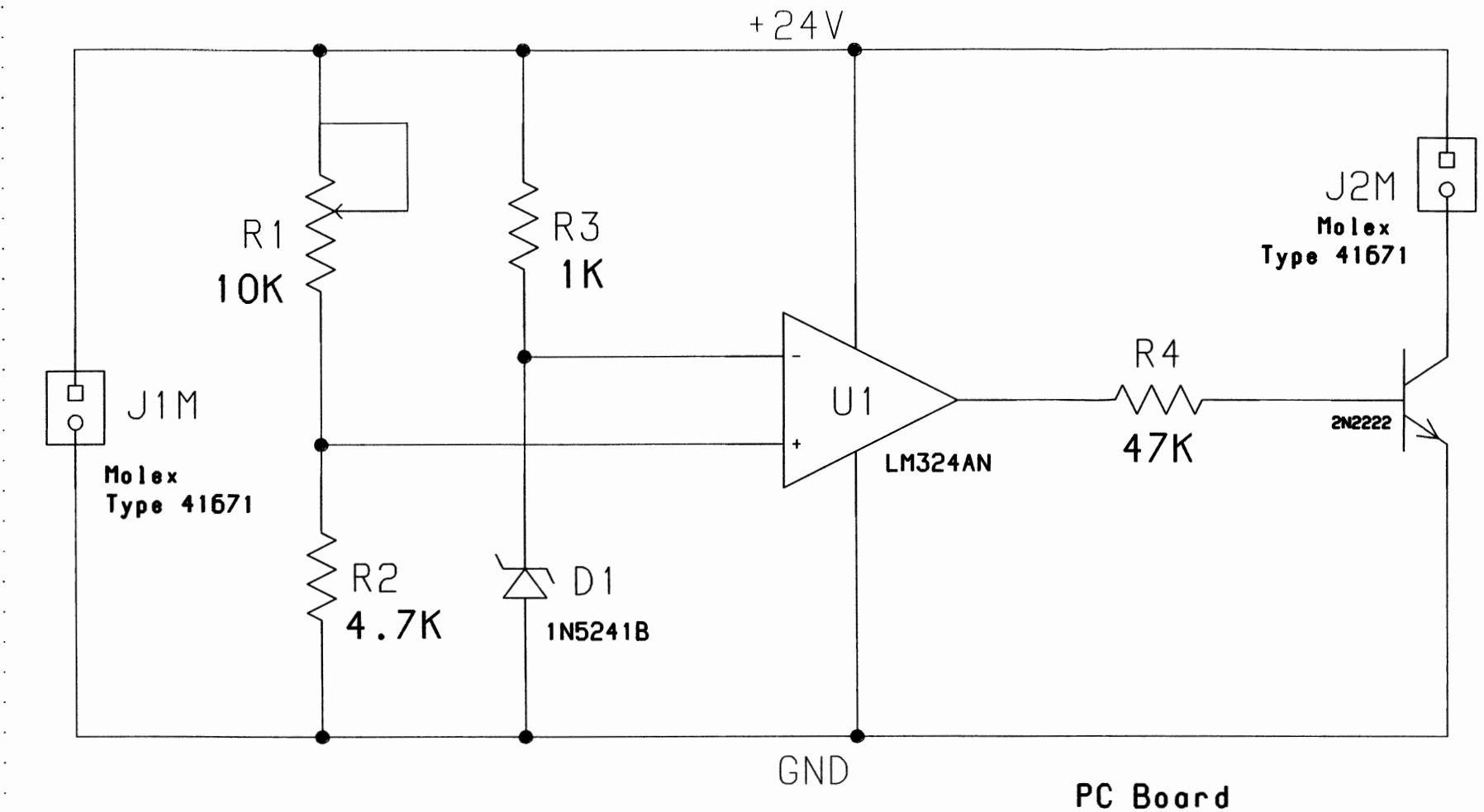
PARTS 27



- Notes:
- 1. J1, J2 are Molex type 41695 series connectors. ORDER
 - 2. TB1 is on 8 pin BEAU type 65506 ORDER

		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE : FRACTIONS DECIMALS ANGLES XX XXX	CONTRACT NO.		Quench Interlock Interface for Wang Dipole Magnet System					
			APPROVALS	DATE						
			DRAWN J. Reimund	6/17/96						
			MATERIAL	CHECKED		SIZE C			CODE IDENT NO.	DRAWING NO.
		FINISH			SCALE NA				SHEET 1	OF 1
NEXT ASSY	USED ON									
APPLICATION		DO NOT SCALE DRAWING								

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED



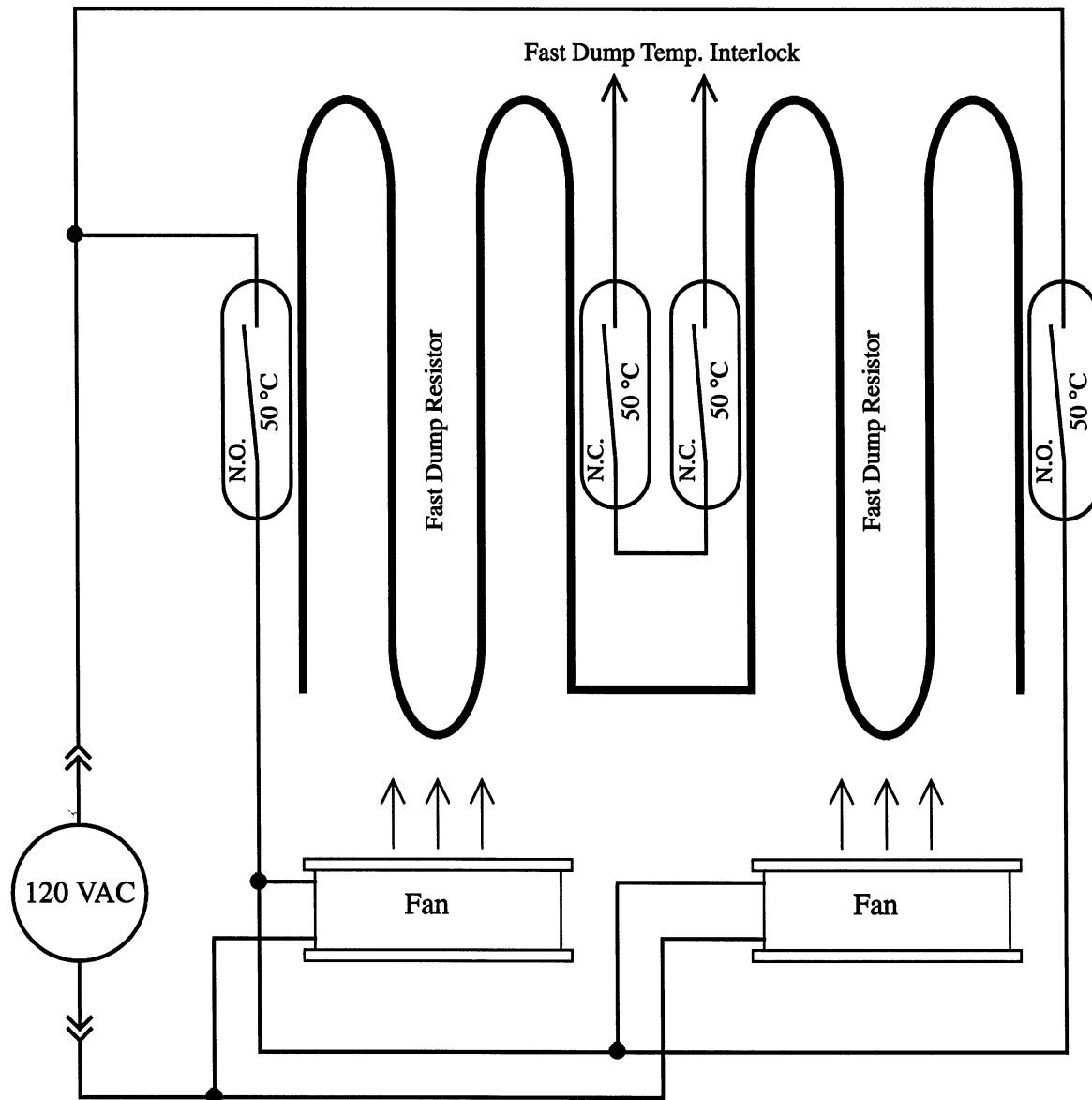
Note:
1. Resistors are 1/4 watt

NEXT ASSY USED ON APPLICATION		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE : FRACTIONS DECIMALS ANGLES XX XXX	CONTRACT NO.		FUG Power Supply Auxilliary Quency Trip Circuit		
			APPROVALS	DATE			
			DRAWN J. Reimund	5/95			
			CHECKED				
		MATERIAL			SIZE B	CODE IDENT NO.	DRAWING NO.
			FINISH				
		DO NOT SCALE DRAWING			SCALE NA	SHEET 1 OF 1	

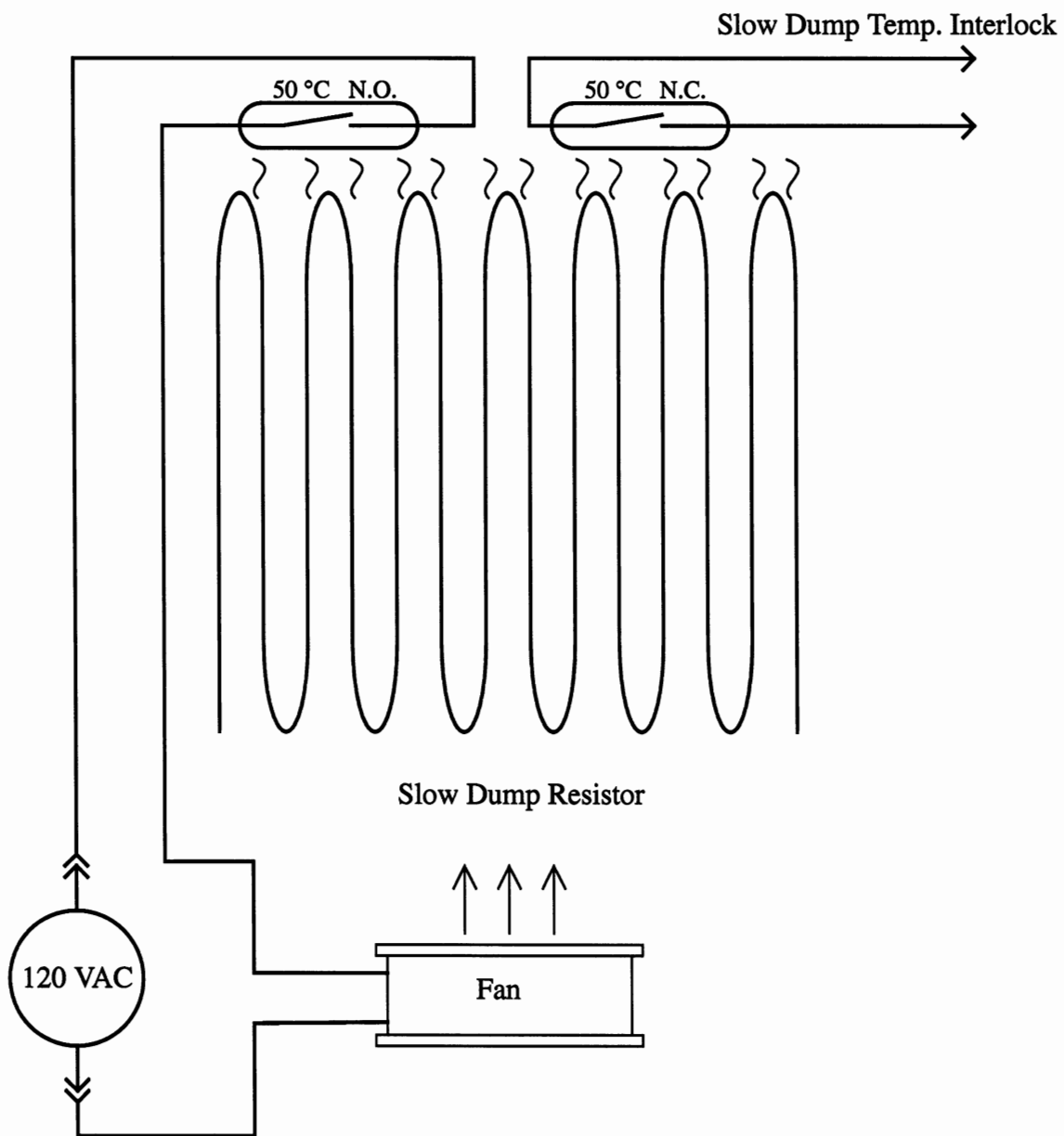
01/25/95

**Dump Resistor
Fan Cooling
and
Temperature Interlock
Scheme**

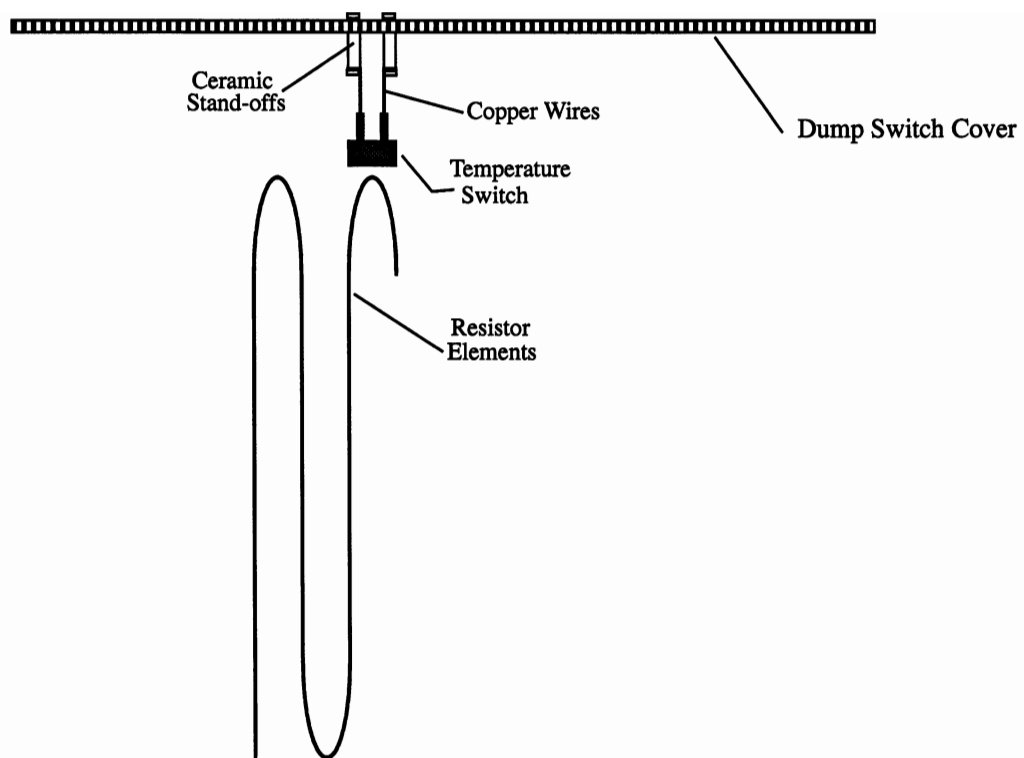
James Reimund



Fast Dump Resistor Cooling System and Interlock



Slow Dump Cooling System and Interlock

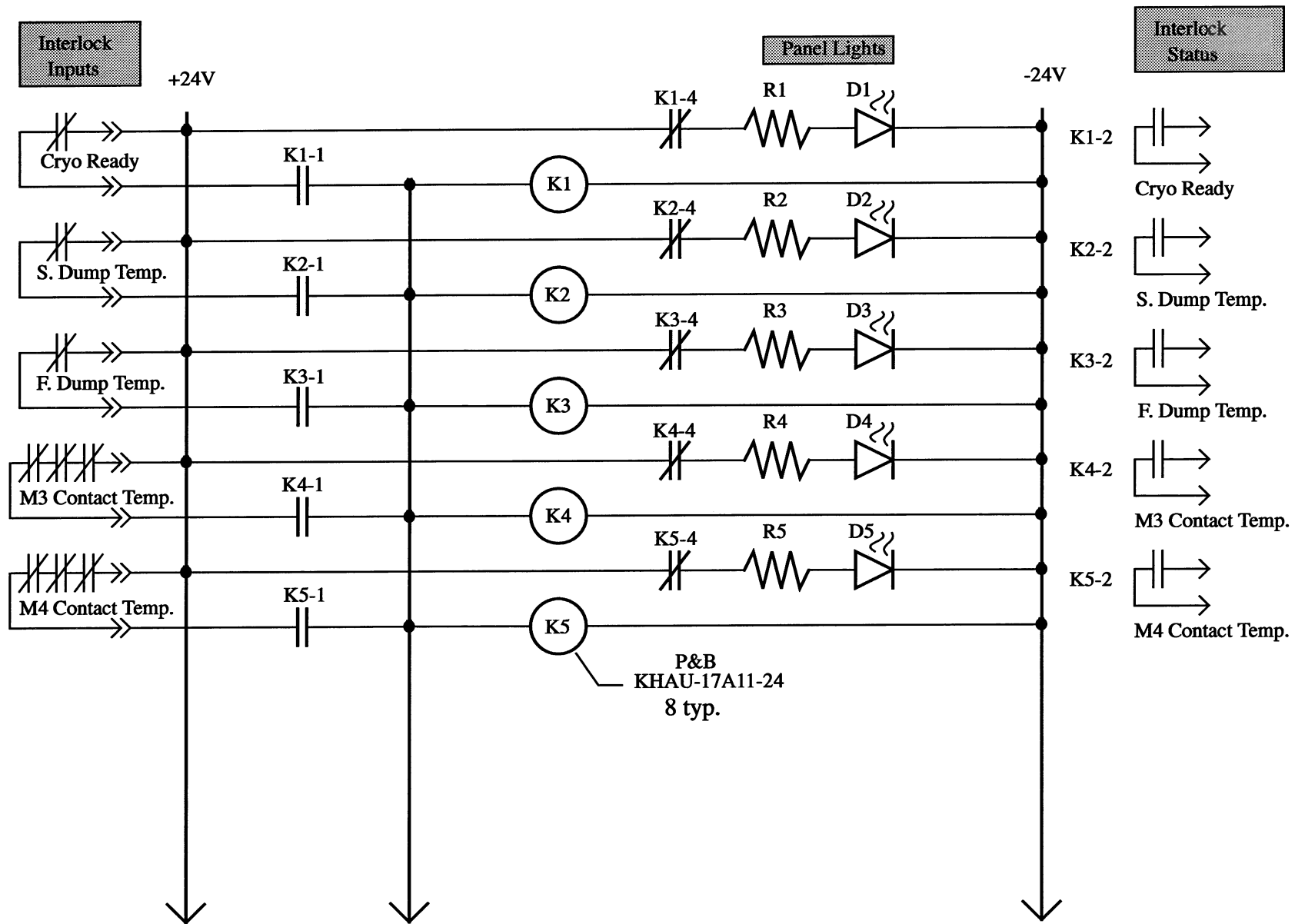


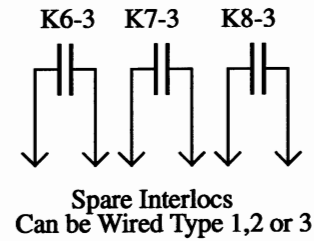
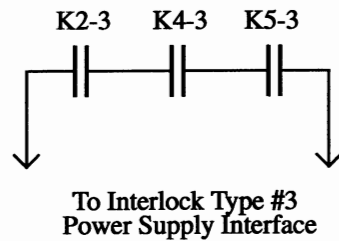
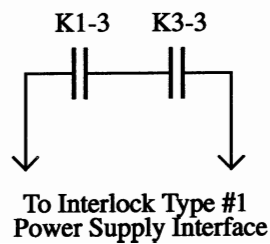
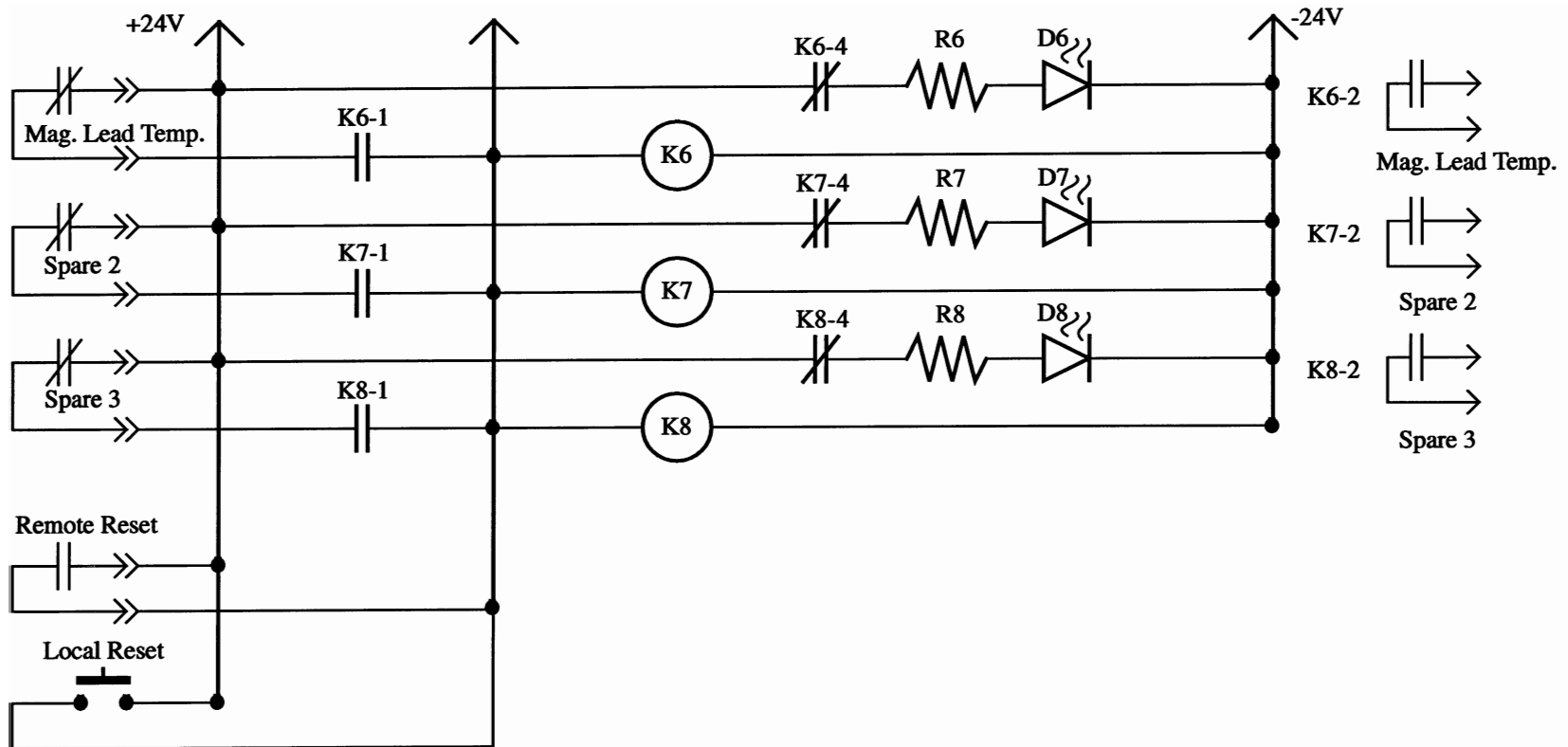
Dump Resistor Thermal Switch Mounting

01/23/94

Auxilliary Interlocks
for the
Hall A Dipole Power Supply

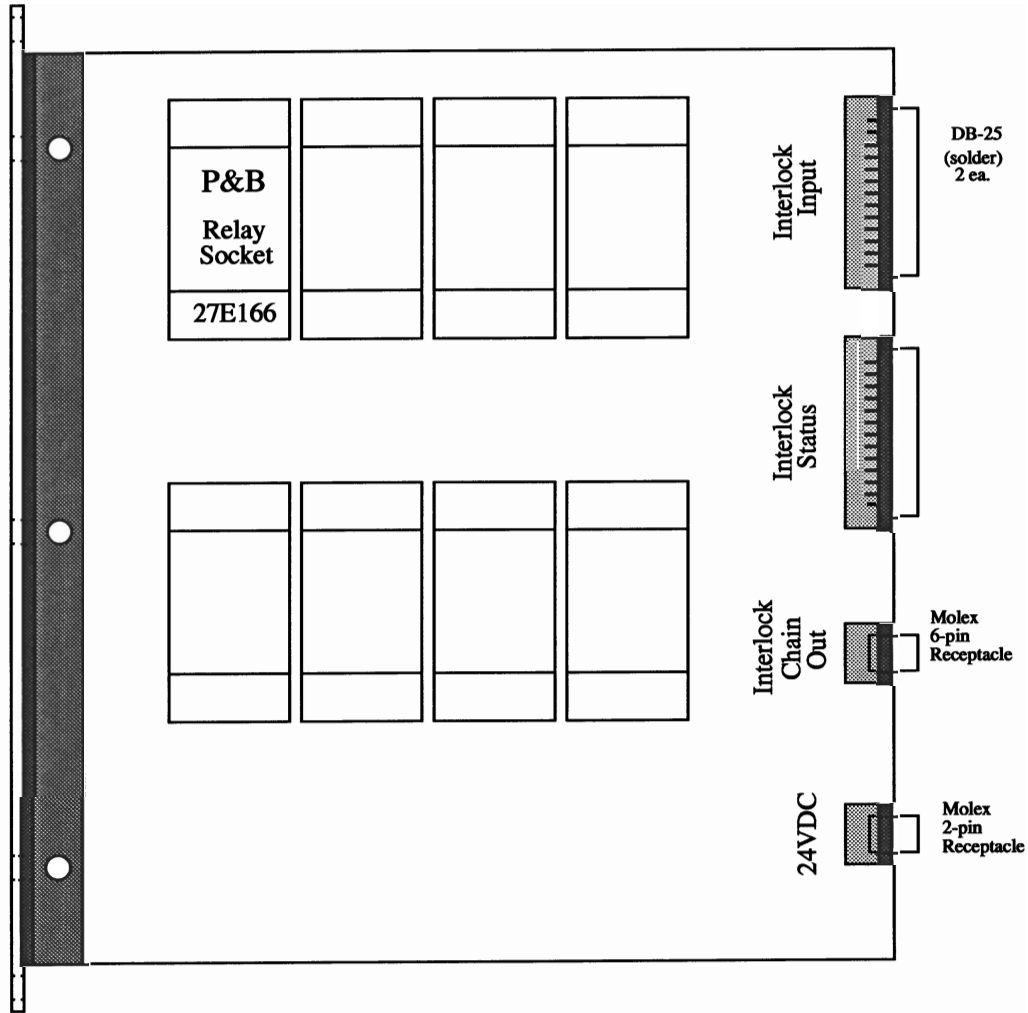
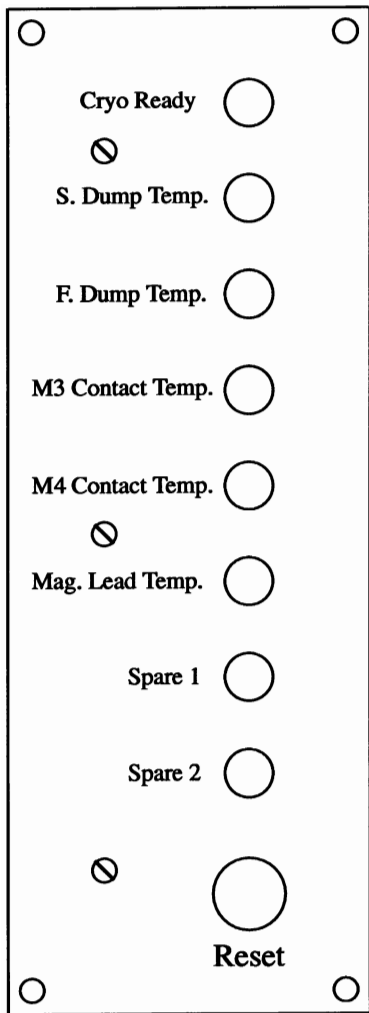
James Reimund
Physics Dept.
Hall A





Note:

Type #3 interlock achieved by connecting in series with door interlock or creating an Allen-Bradley PLC type #3 interlock.



Cryogenics Ready Interlock

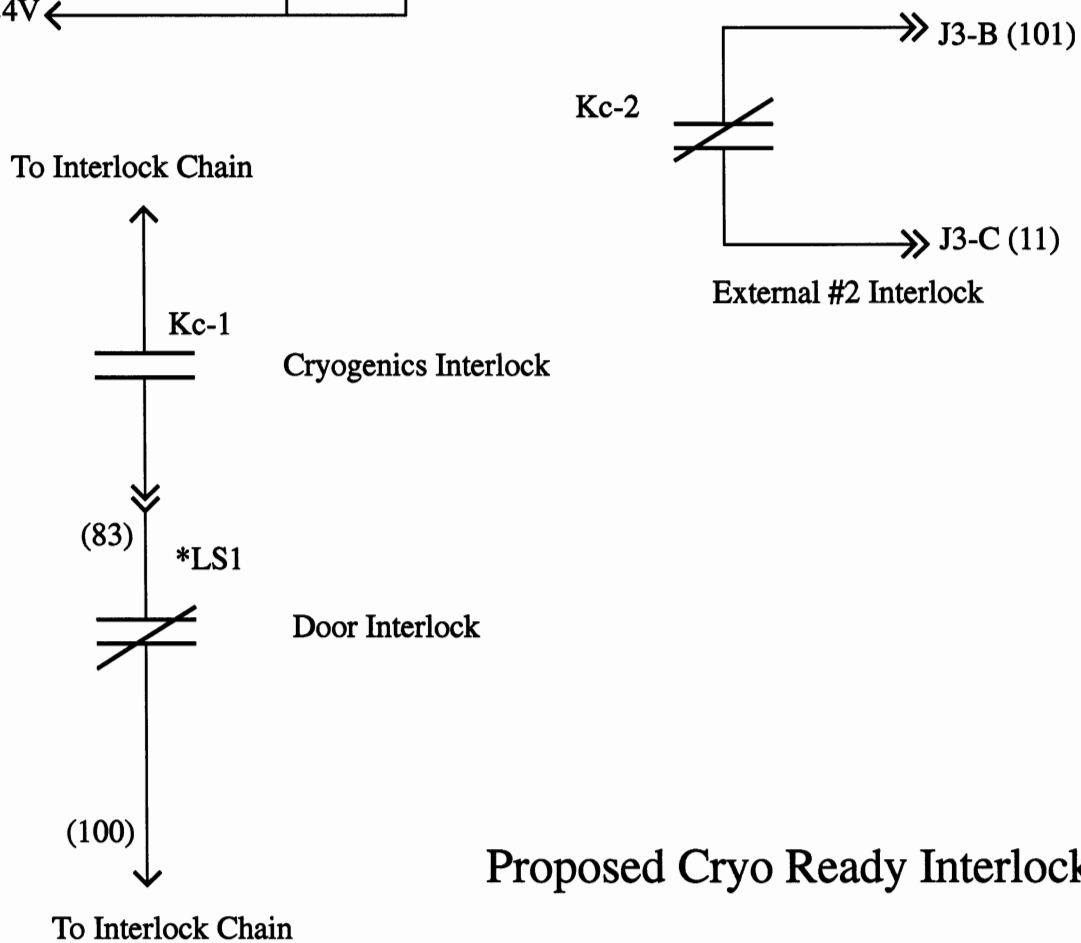
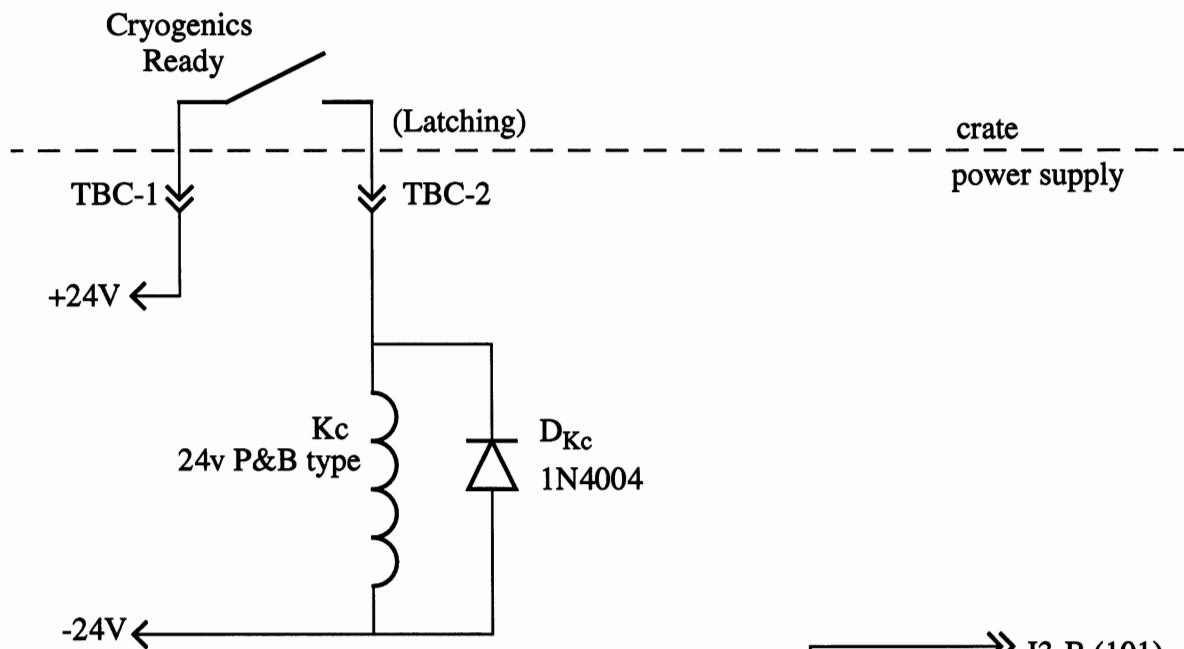
This circuit receives an external “Cryogenics Ready” contact that would be provided by the cryogenics control system. Closure of this contact would indicate that:

1. Helium liquid level is nominal.
2. Nitrogen liquid level is nominal.
3. Cooling gas is flowing in leads at some established flow rate.

This contact needs to latch “OPEN” on a fault condition.

With this contact in the closed state, relay Kc energizes causing contact Kc-1 to close and make up the door lock chain. This chain is used because there is currently no external interlock which, when tripped, will cause a slow dump. The door interlocks do cause a slow dump and the wiring is easily accessible. A terminal block or connector will need to be installed on top of the power supply.

Monitoring of fault conditions is important, even if only at the local panel. To achieve this function, an additional pair of contacts are needed to at least turn on a local panel light. The “External 2” interlock incorporated into the existing power supply causes only a warning light (yellow) to illuminate when tripped. Using a normally closed set of contacts from relay Kc could be connected to this interlock. When the cryogenics interlock is made up, the light would be out. When the interlock is tripped, the light would be on informing the operator of the reason for the power supply turning off and going into slow dump mode. This modification requires the relabeling of the front panel to say “Cryogenics” instead of “#2 External Interlock”.



Proposed Cryo Ready Interlock

* Could also be LS2 or LS3