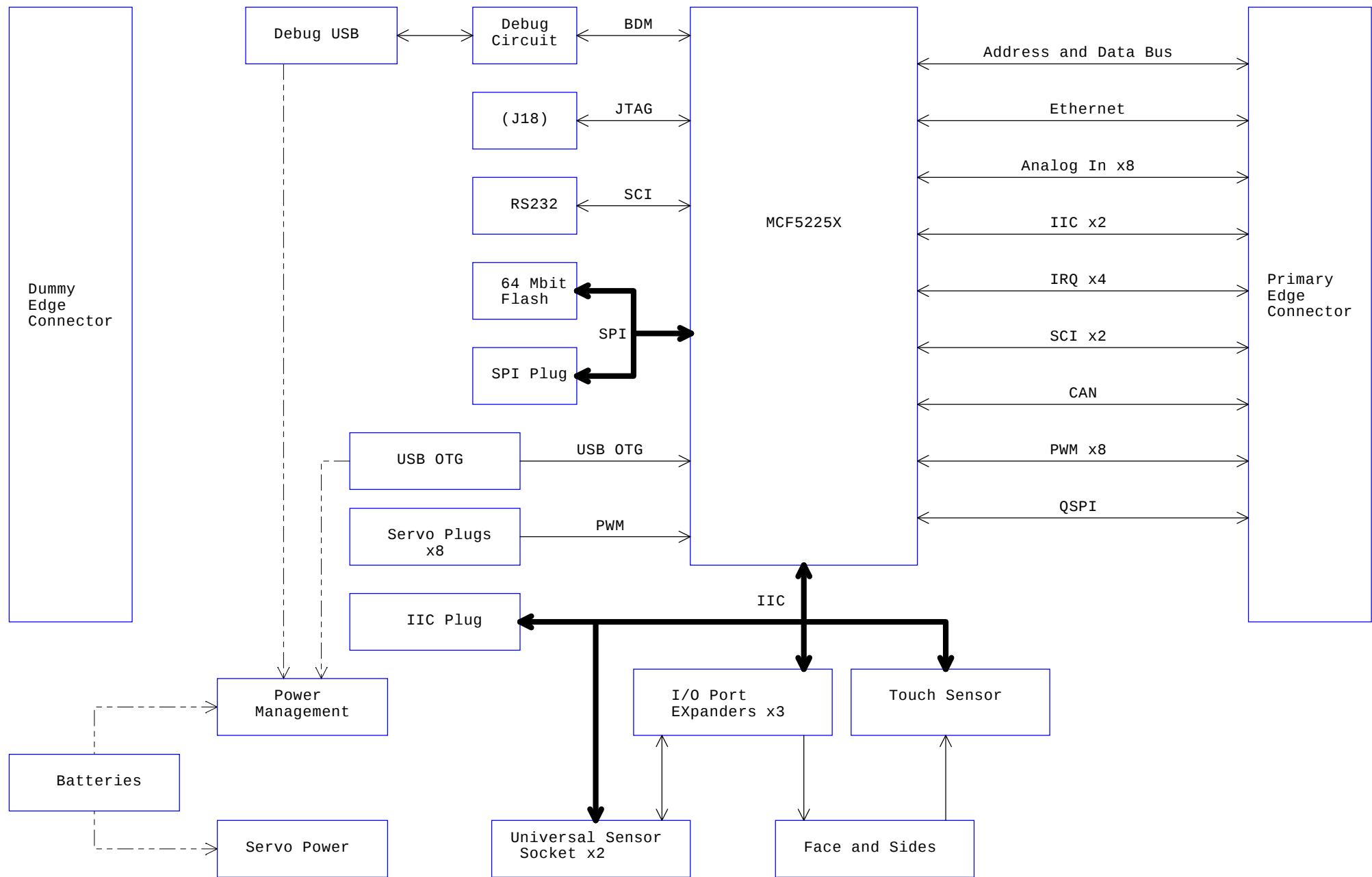
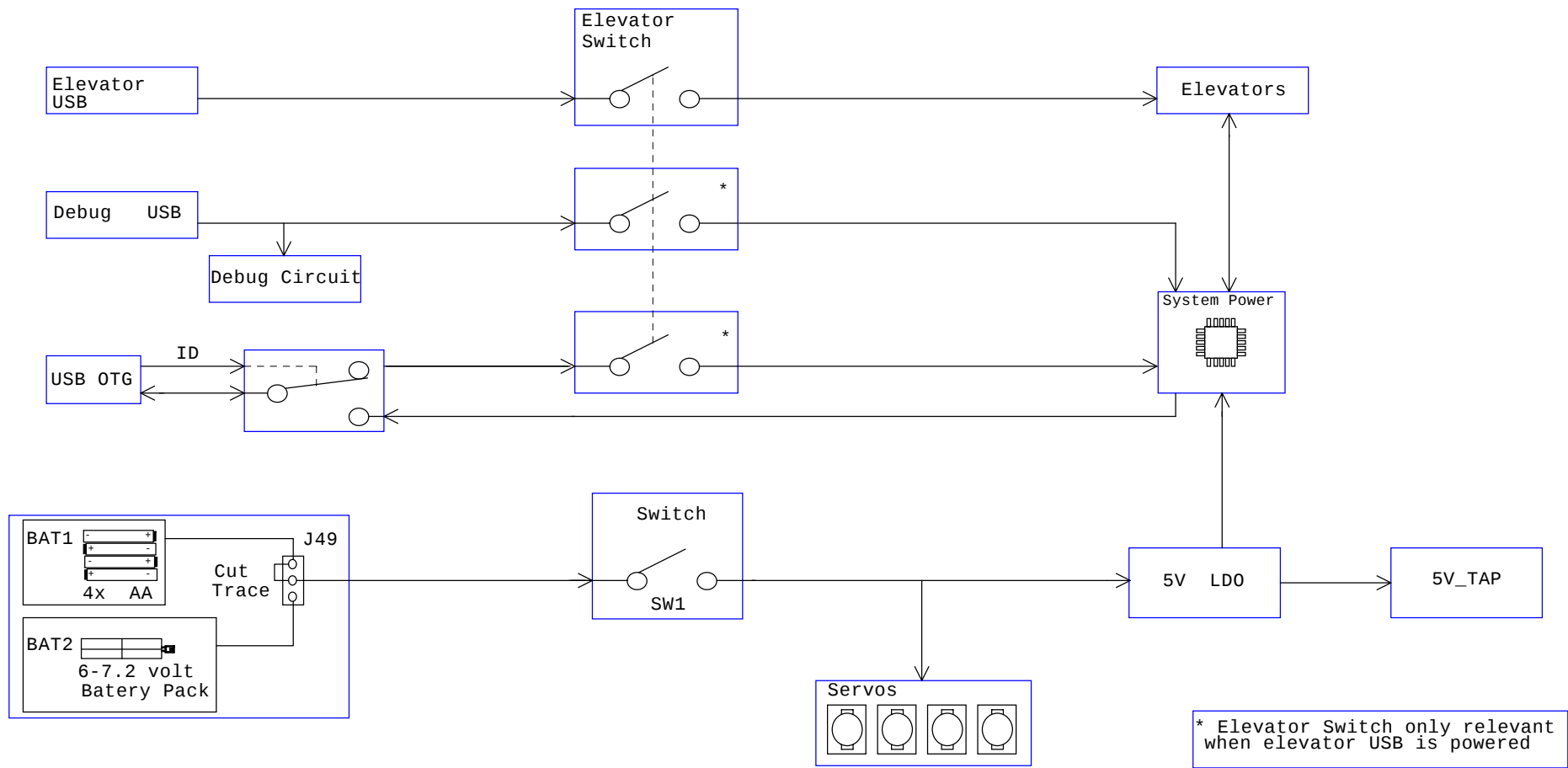
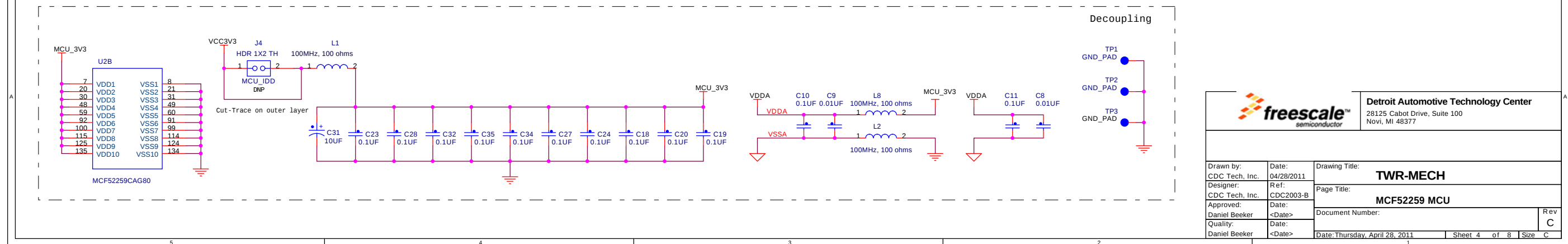
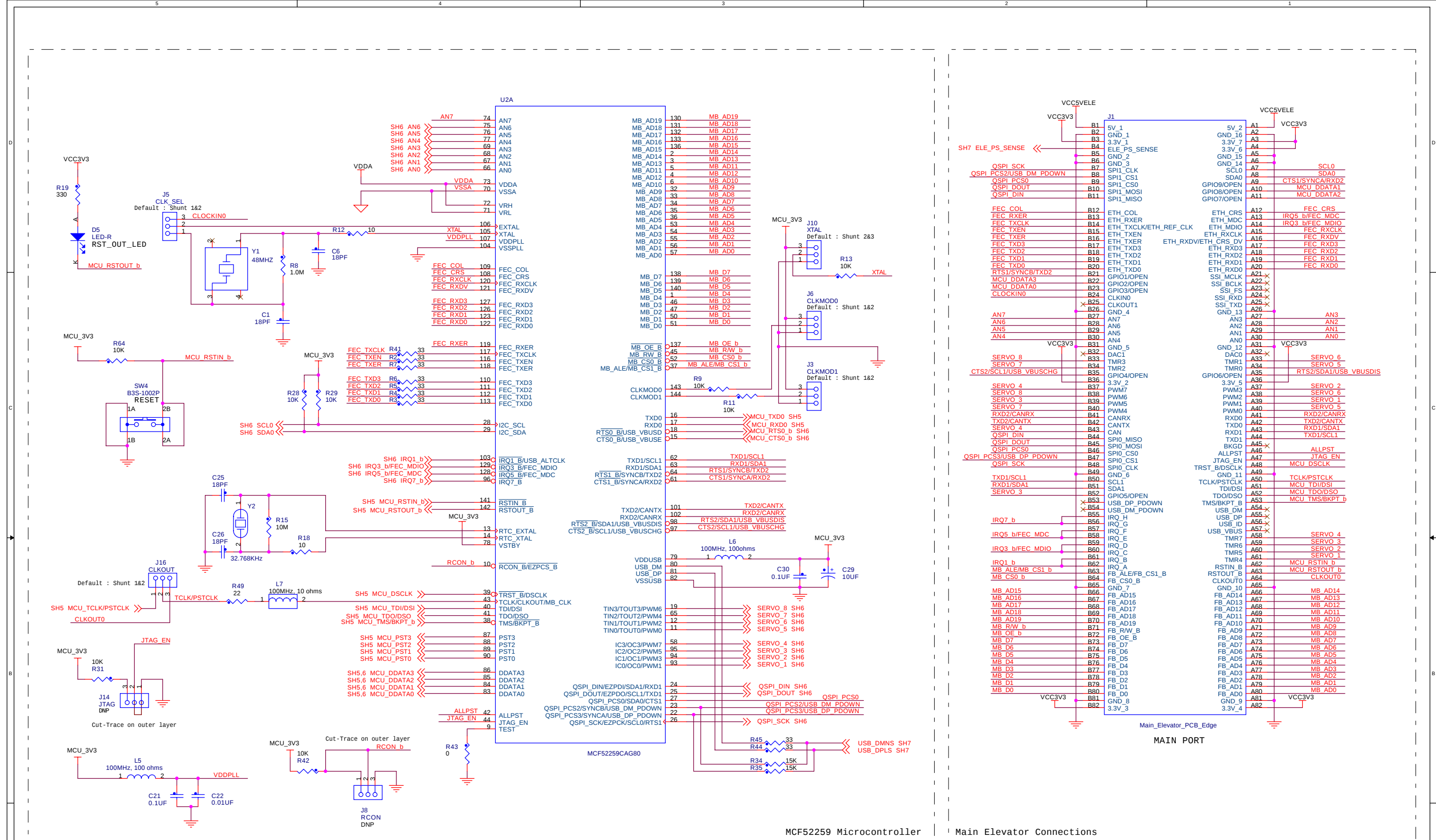
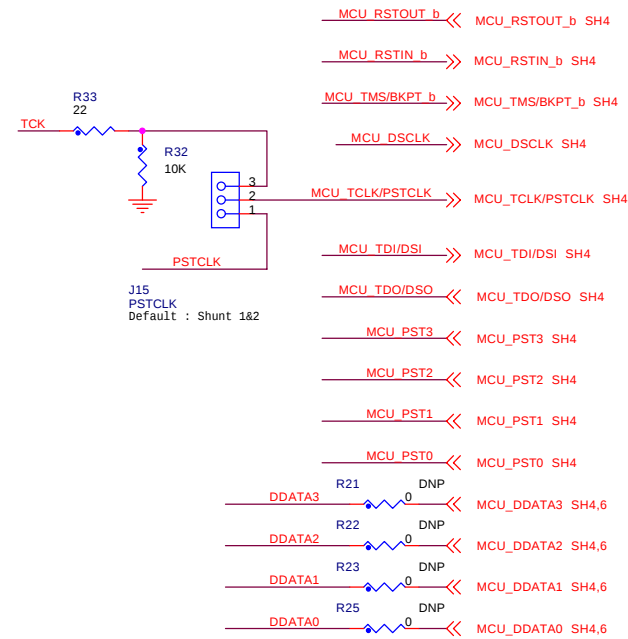
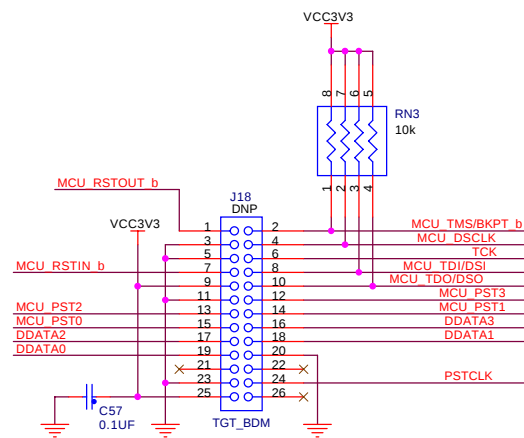




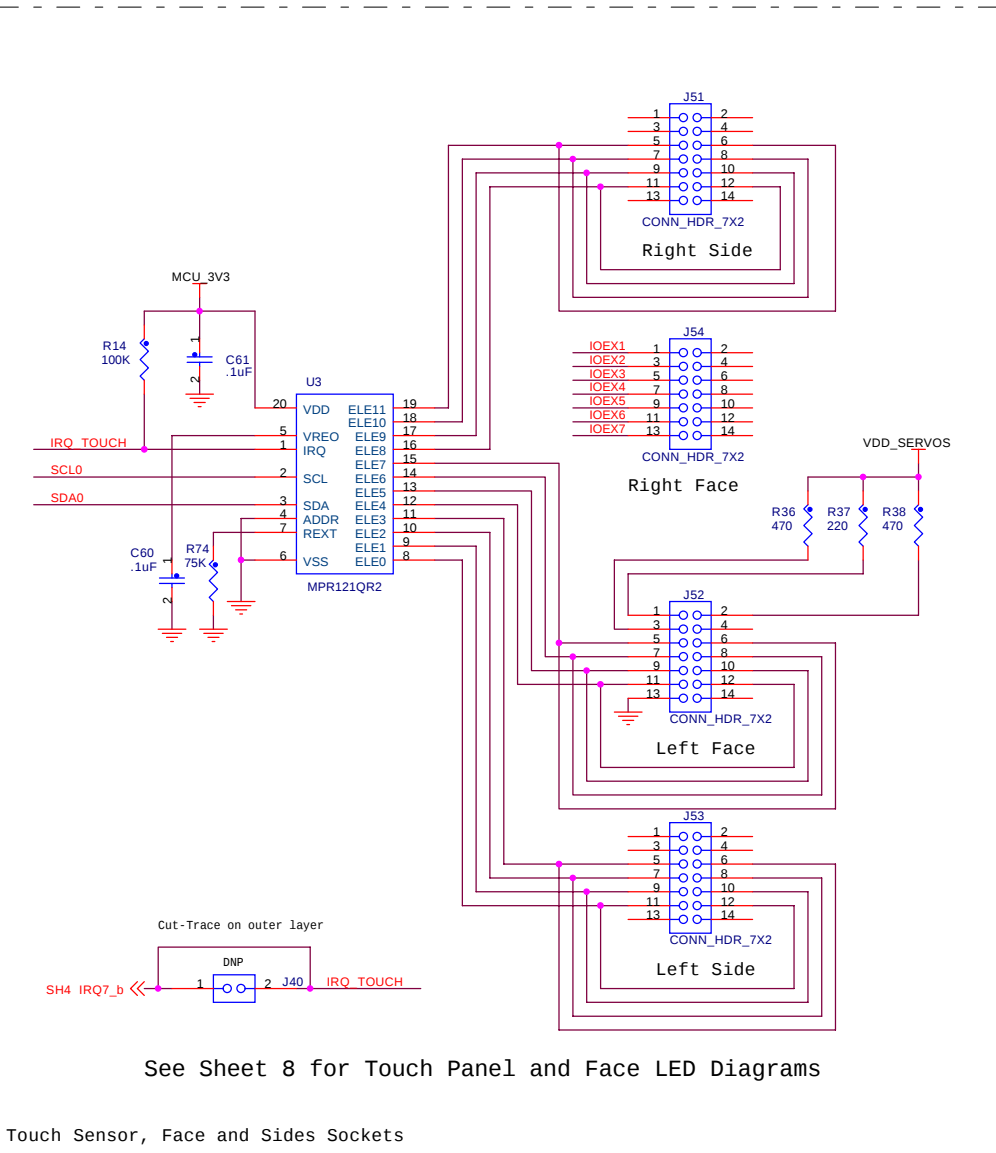
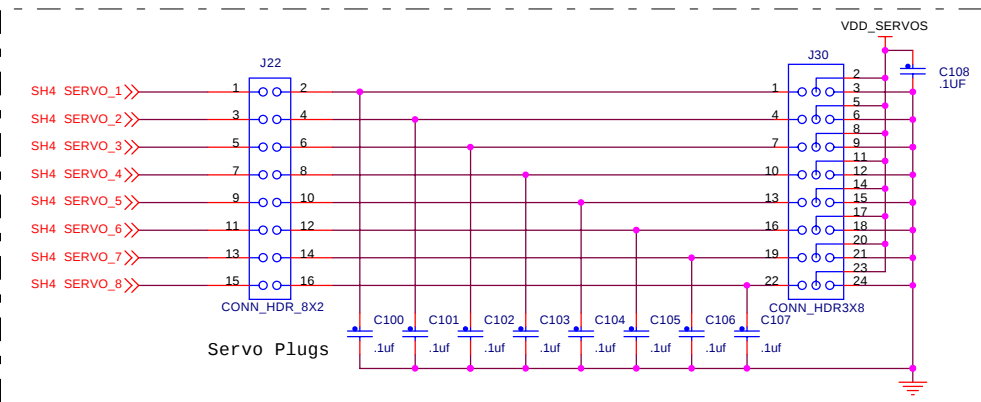
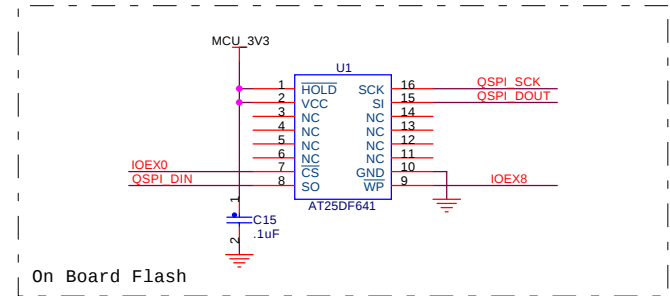
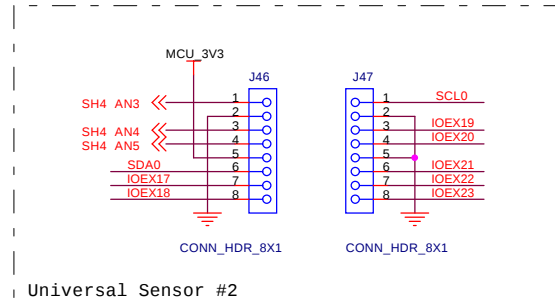
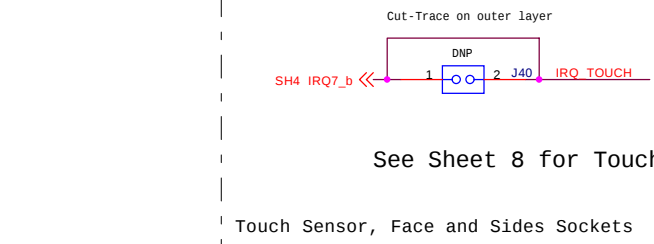
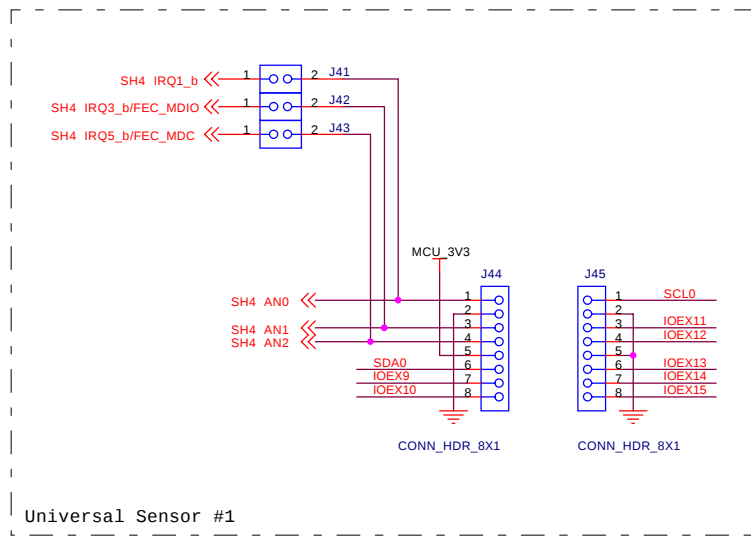
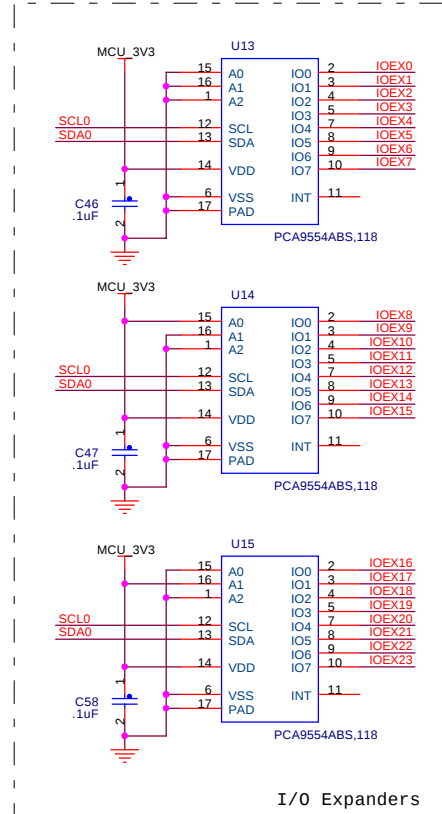
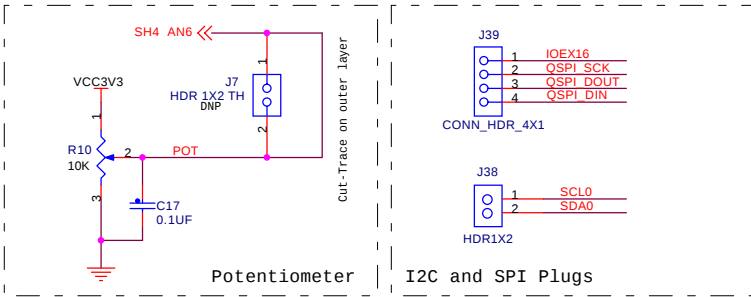
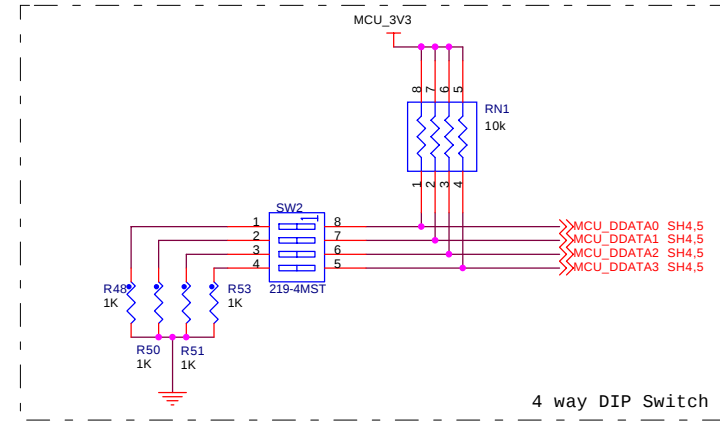
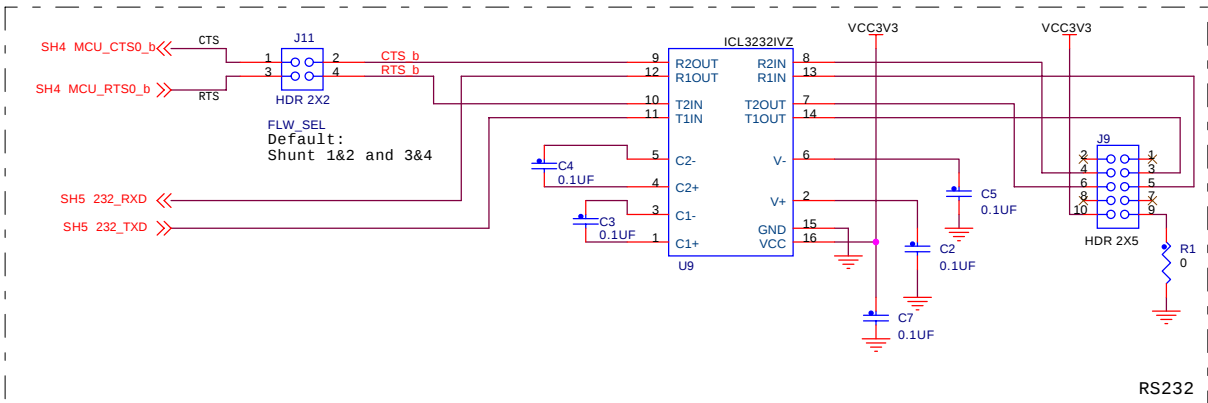


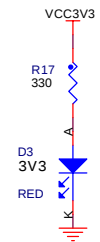
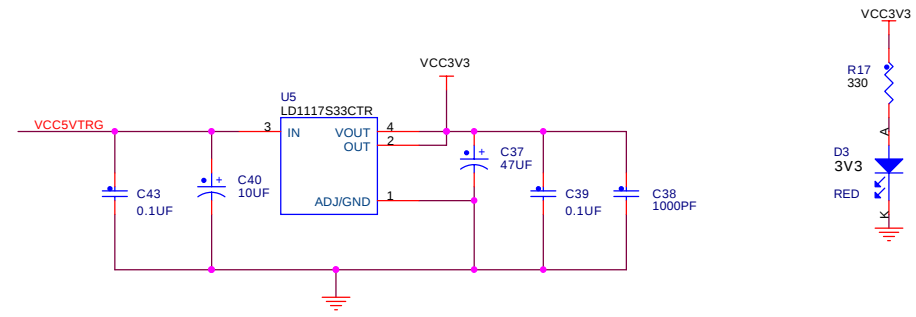
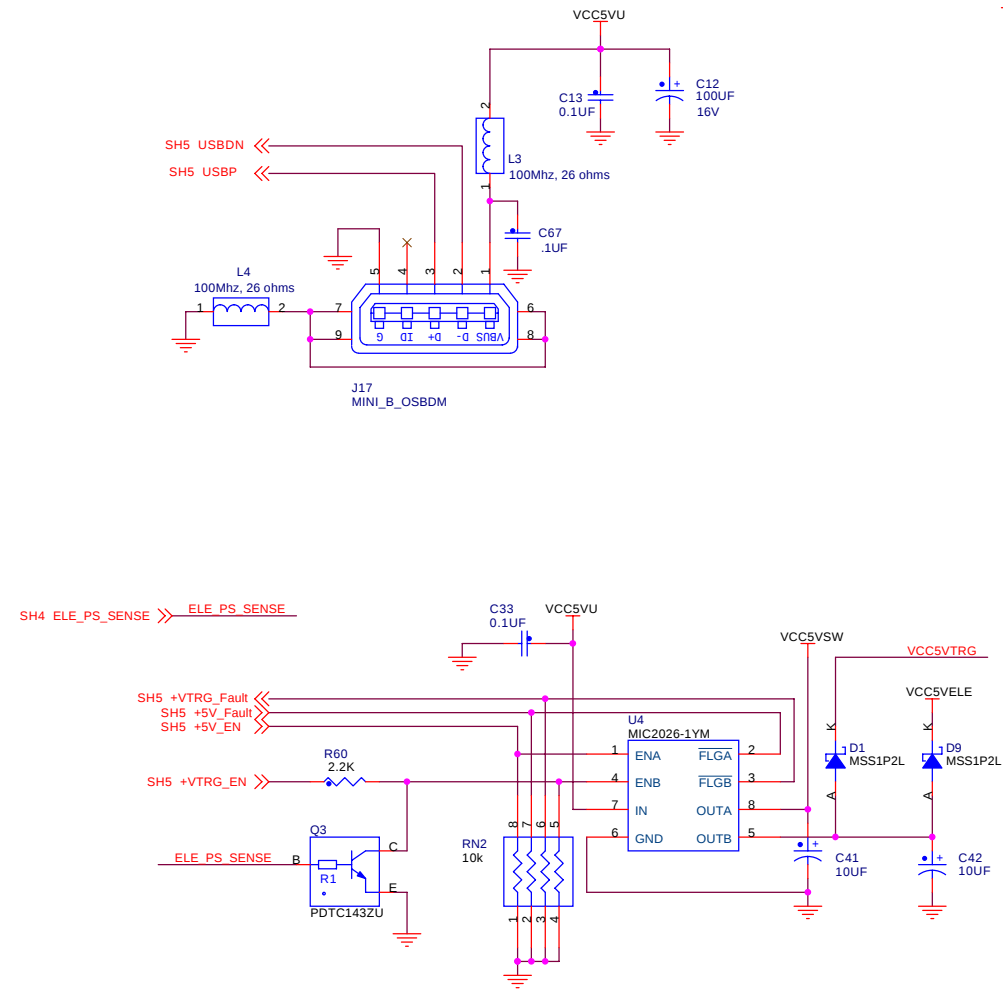
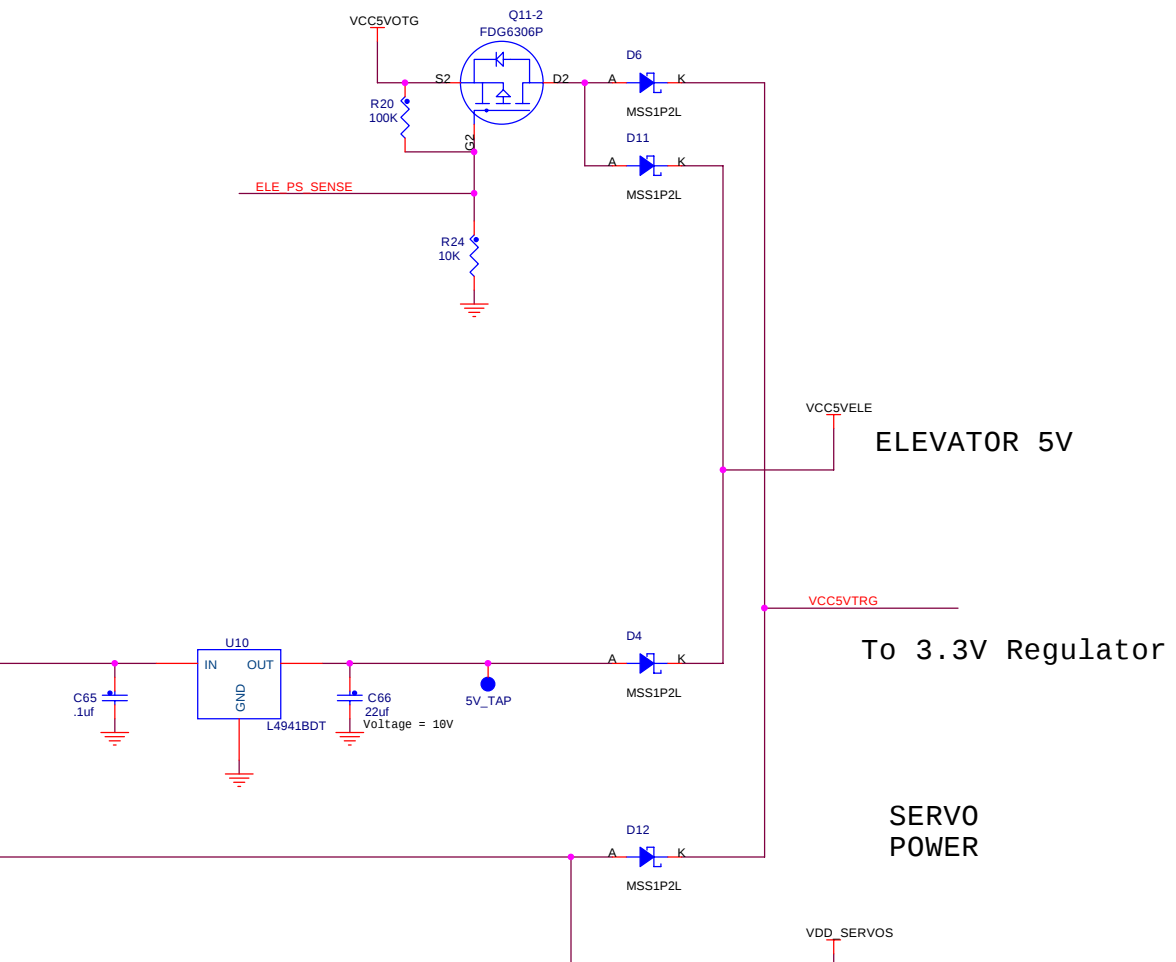
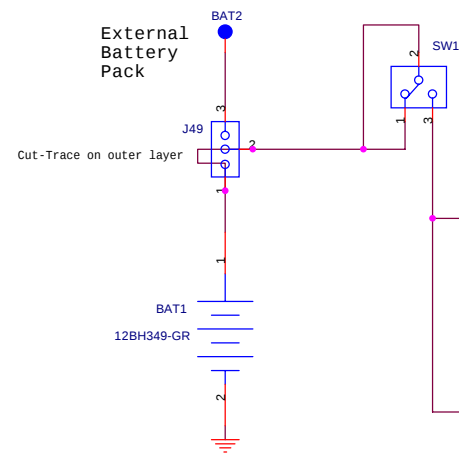
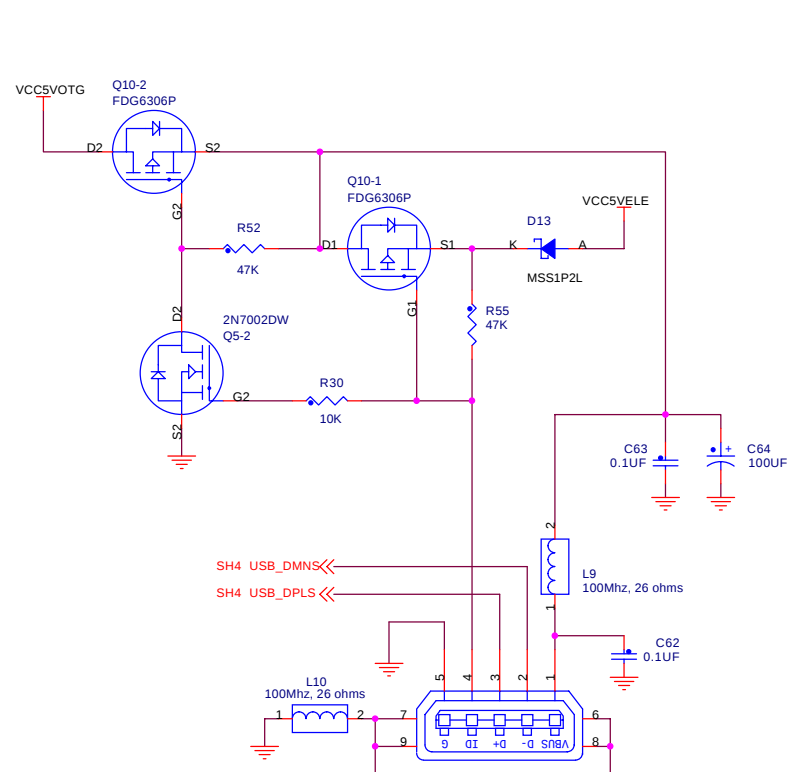




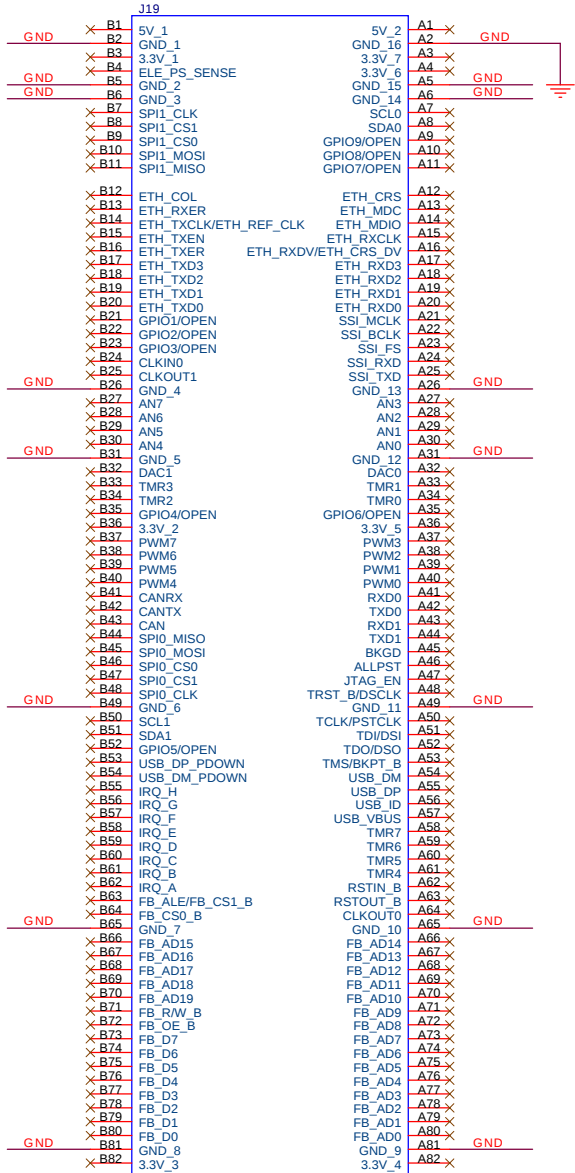
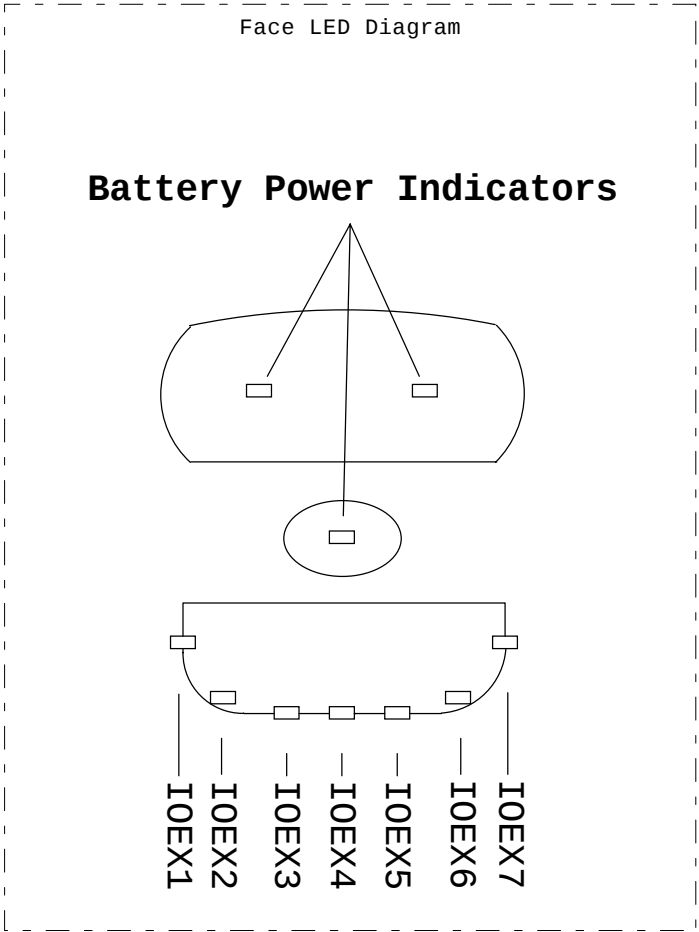
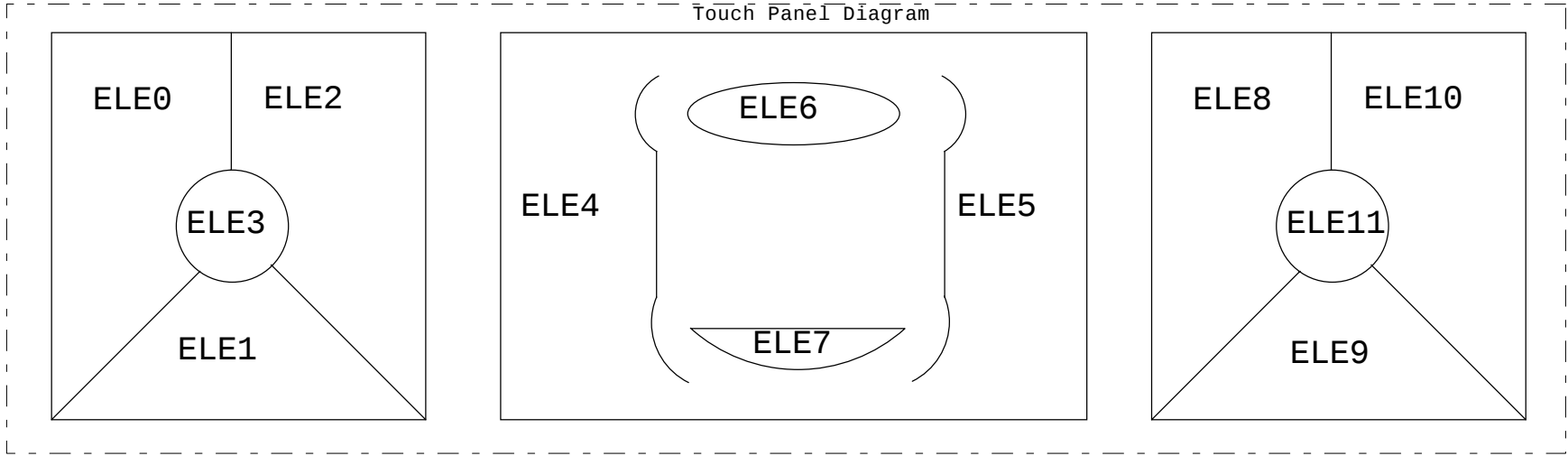


SH4 SCL0 >> SCL0  
SH4 SDA0 >> SDA0  
SH4 QSPI\_SCK << QSPI\_SCK  
SH4 QSPI\_DOUT << QSPI\_DOUT  
SH4 QSPI\_DIN << QSPI\_DIN





|                             |                     |                                                                                               |                       |
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| Designer:<br>CDC Tech, Inc. | Ref:<br>CDC2003-B   | Page Title:<br><b>POWER</b>                                                                   |                       |
| Approved:<br>Daniel Beeker  | Date:<br><Date>     | Document Number:                                                                              | Rev<br><b>C</b>       |
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DUMMY PORT



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| Date: Thursday, April 28, 2011 Sheet 8 of 8 Size C |                     |                                   |