

Circuit Connection Adds Current–Mode Operation To PFM Boost Converters

Maxim's PFM boost controllers offer high efficiency and low quiescent current; however, by externally adding a current mode signal into the feedback, the stability and output ripple are improved. This circuit connection requires no additional ICs or components versus the standard connection.

A control scheme used in certain boost–controller ICs from Maxim (current–limited pulse–frequency modulation, or PFM), achieves high efficiency over a wide range of output current by combining the low quiescent current of PFM with the load–driving capability of pulse–width modulation (PWM). To provide current–mode control as well, simply connect the load, output filter capacitor, and lower feedback resistor to the current–sense pin (CS) instead of ground (**Figure 1**).

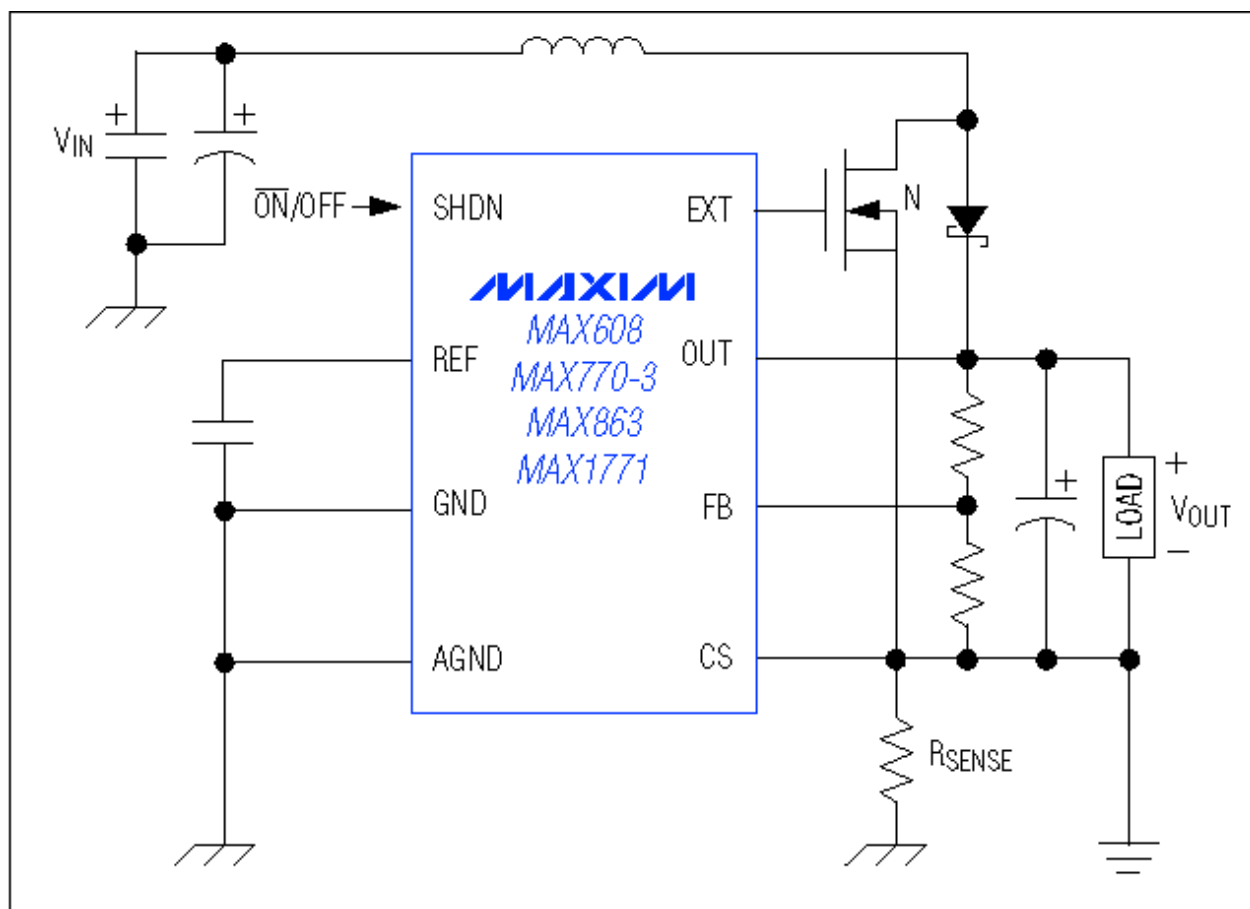


Figure 1. This circuit adds current–mode operation to Maxim's current–limited PFM boost controllers without increasing the quiescent current.

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When operating with medium to heavy loads, the Figure 1 circuit exhibits lower output ripple and a more stable inductor current (**Figure 2a**) than do the standard application circuits represented by typical waveforms in **Figure 2b**. The improvements gained by these connections have no effect on the quiescent current and require no additional circuitry, but they do require separate input and output grounds as shown, connected only by R_{SENSE} .

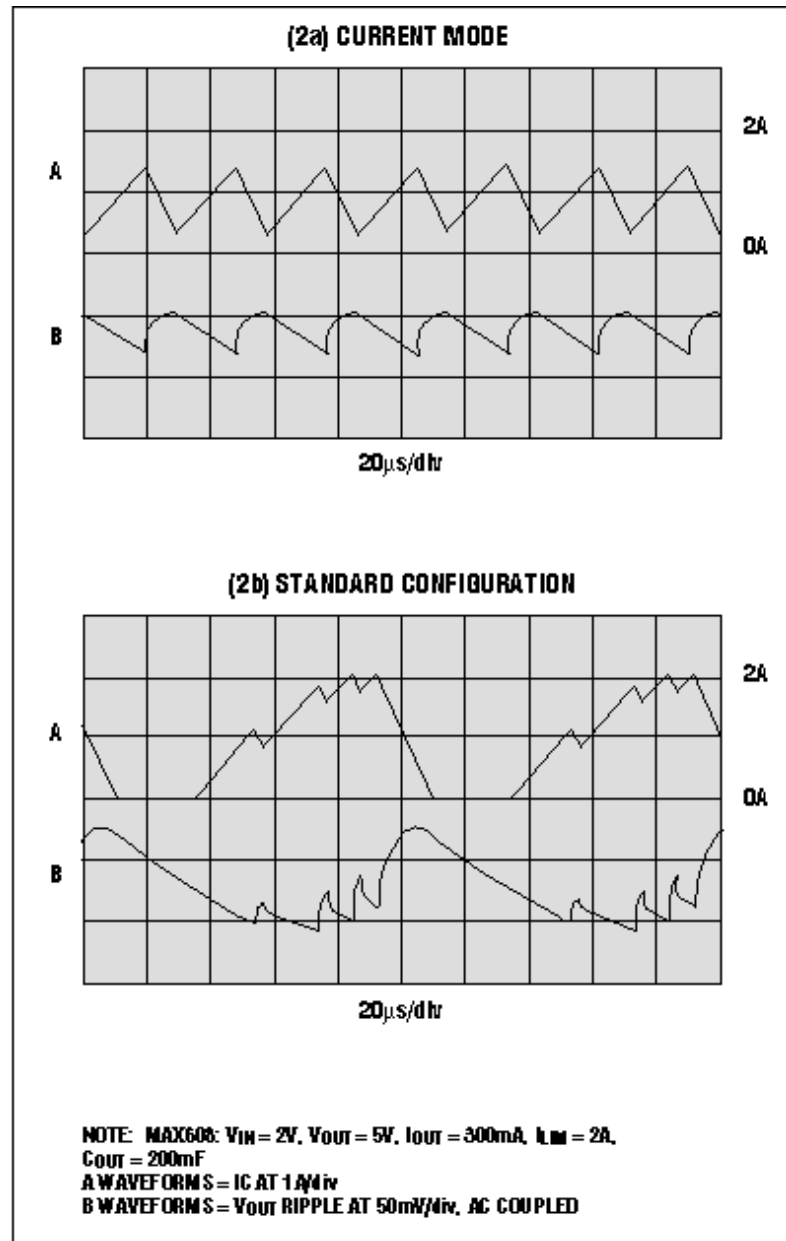


Figure 2. The Figure 1 circuit's inductor–current and output–ripple waveforms (a) exhibit better stability and lower ripple amplitude than those of the standard operating configuration (b).

This circuit achieves current–mode control by constantly monitoring the inductor current through R_{SENSE} : via the field–effect transistor during t_{ON} (which depends on the magnitude of V_{IN}), and via the diode and output filter capacitor during t_{OFF} (minimum 2.3 μ s). Flowing through R_{SENSE} , the inductor current creates

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a signal at CS that couples through the output filter capacitor and adds to the normal feedback signal at FB. Connecting the load between OUT and CS prevents this CS signal from adding to the ripple at V_{OUT}.