19-4757; Rev 1; 3/98

EVALUATION KIT AVAILABLE

880MHz, Low-Noise, Low-Distortion Op Amps in SOT23-5

General Description

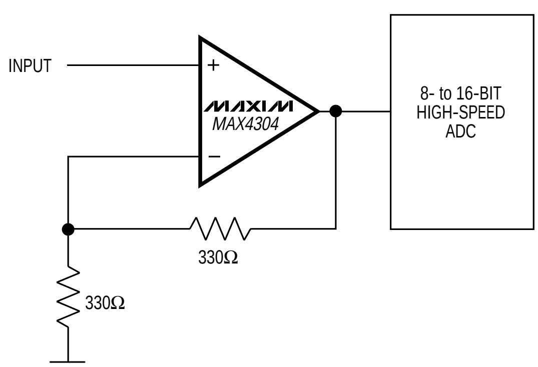
The MAX4104/MAX4105/MAX4304/MAX4305 op amps feature ultra-high speed, low noise, and low distortion in a SOT23 package. The unity-gain-stable MAX4104 requires only 20mA of supply current while delivering 880MHz bandwidth and 400V/µs slew rate. The MAX4304, compensated for gains of +2V/V or greater, delivers a 730MHz bandwidth and a 1000V/µs slew rate. The MAX4105 is compensated for a minimum gain of +5V/V and delivers a 430MHz bandwidth and a 1400V/sec slew rate. The MAX4305 has +10V/V minimum gain compensation and delivers a 350MHz bandwidth and a 1400V/µs slew rate.

Low voltage noise density of 2.1nV/$\sqrt{Hz}$ and -88dBc spurious-free dynamic range make these devices ideal for low-noise/low-distortion video and telecommunications applications. These op amps also feature a wide output voltage swing of ±3.7V and ±70mA output current-drive capability. For space-critical applications, they are available in a miniature 5-pin SOT23 package.

Applications

- Video ADC Preamp
- Pulse/RF Telecom Applications
- Video Buffers and Cable Drivers
- Ultrasound
- Active Filters
- ADC Input Buffers

Typical Application Circuit



ADC BUFFER WITH GAIN (A_{VCL} = 2V/V)

Features

- **Low 2.1nV/$\sqrt{Hz}$ Voltage Noise Density**
- **Ultra-High 880MHz -3dB Bandwidth (MAX4104)**
- **100MHz 0.1dB Gain Flatness (MAX4104/4105)**
- **1400V/µs Slew Rate (MAX4105/4305)**
- **-88dBc SFDR (5MHz, R_L = 100Ω) (MAX4104/4304)**
- **High Output Current Drive: ±70mA**
- **Low Differential Gain/Phase Error: 0.01%/0.01° (MAX4104/4304)**
- **Low ±1mV Input Offset Voltage**
- **Available in Space-Saving 5-Pin SOT23 Package**

Selector Guide

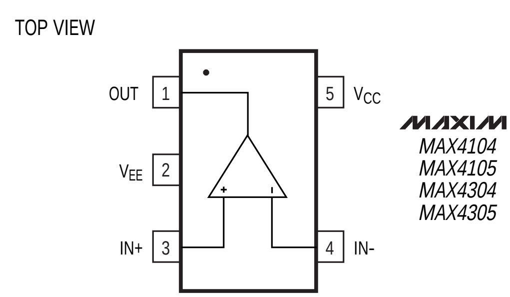
PART	MINIMUM STABLE GAIN (V/V)	BANDWIDTH (MHz)	PIN-PACKAGE
MAX4104	1	880	5-pin SOT23, 8-pin SO
MAX4304	2	730	5-pin SOT23, 8-pin SO
MAX4105	5	430	5-pin SOT23, 8-pin SO
MAX4305	10	350	5-pin SOT23, 8-pin SO

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE	SOT TOP MARK
MAX4104ESA	-40°C to +85°C	8 SO	—
MAX4104EUK-T	-40°C to +85°C	5 SOT23-5	ACCO

Ordering Information continued at end of data sheet.

Pin Configurations



SOT23-5

Pin Configurations continued at end of data sheet.

MAX4104/MAX4105/MAX4304/MAX4305

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{CC} to V_{EE})..+12V
Voltage on Any Pin to Ground..........(V_{EE} - 0.3V) to (V_{CC} + 0.3V)
Short-Circuit Duration (V_{OUT} to GND)........................Continuous
Continuous Power Dissipation (T_A = +70°C)
5-pin SOT23 (derate 7.1mW/°C above +70°C)...........571mW
8-pin SO (derate 5.9mW/°C above +70°C).................471mW
Operating Temperature Range ....-40°C to +85°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10sec) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V, V_{EE} = -5V, V_{CM} = 0, R_L = 100kΩ, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Supply Voltage Range	V_{CC}/V_{EE}	Guaranteed by PSRR test		±3.5	±5	±5.5	V
Input Offset Voltage	V_{OS}	V_{OUT} = 0	MAX4_0_ESA		1	6	mV
			MAX4_0_EUK		1	8	
Input Offset Voltage Drift	TCV_{OS}				2.5		µV/°C
Input Bias Current	I_B				32	70	µA
Input Offset Current	I_{OS}				0.5	5.0	µA
Differential Input Resistance	R_{IN}	-0.8V ≤ V_{IN} ≤ 0.8V			6		kΩ
Common-Mode Input Resistance	R_{IN}	Either input			1.5		MΩ
Input Common-Mode Voltage Range	V_{CM}	Guaranteed by CMRR test		-2.8		+4.1	V
Common-Mode Rejection Ratio	CMRR	-2.8V ≤ V_{CM} ≤ 4.1V		80	95		dB
Positive Power-Supply Rejection Ratio	PSSR+	V_{CC} = 3.5V to 5.5V		75	85		dB
Negative Power-Supply Rejection Ratio	PSRR-	V_{EE} = -3.5V to -5.5V		55	65		dB
Quiescent Supply Current	I_S	V_{OUT} = 0			20	27	mA
Open-Loop Gain	A_{VOL}	-2.8V ≤ V_{OUT} ≤ 2.8V, R_L = 100Ω		55	65		dB
Output Voltage Swing	V_{OUT}	R_L = 100kΩ		±3.5	-3.7 to +3.8		V
		R_L = 100Ω		±3.0	-3.5 to +3.4		
Output Current Drive	I_{OUT}	R_L = 30Ω		±53	±70		mA
Short-Circuit Output Current	I_{SC}	R_L = short to ground			80		mA
Open-Loop Output Impedance	Z_{OUT}				9		Ω

AC ELECTRICAL CHARACTERISTICS

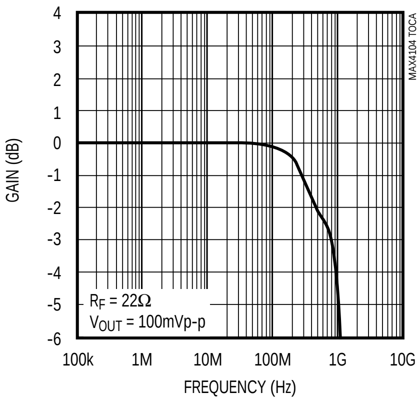
(V_{CC} = +5V, V_{EE} = -5V, V_{CM} = 0, R_L = 100Ω; A_V = +1V/V for MAX4104, +2V/V for MAX4304, +5V/V for MAX4105, +10V/V for MAX4305; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP	MAX	UNITS
-3dB Bandwidth	$BW_{(-3dB)}$	V_{OUT} = 100mVp-p		MAX4104		880		MHz
				MAX4304		730		
				MAX4105		430		
				MAX4305		350		
0.1dB Bandwidth	$BW_{(0.1)}$	V_{OUT} = 100mVp-p		MAX4104		100		MHz
				MAX4304		70		
				MAX4105		100		
				MAX4305		75		
Full-Power Bandwidth	FPBW	V_{OUT} = 2Vp-p		MAX4104		115		MHz
				MAX4304		285		
				MAX4105		380		
				MAX4305		325		
Slew Rate	SR	V_{OUT} = 2Vp-p		MAX4104		400		V/µs
				MAX4304		1000		
				MAX4105		1400		
				MAX4305		1400		
Settling Time to 0.1%	t_S	V_{OUT} = 2Vp-p		to 0.1%		20		ns
				to 0.01%		25		
Spurious-Free Dynamic Range	SFDR	V_{OUT} = 2Vp-p	MAX4104/ MAX4304	f_C = 5MHz		-88		dBc
				f_C = 20MHz		-67		
			MAX4105/ MAX4305	f_C = 5MHz		-74		
				f_C = 20MHz		-61		
Differential Gain Error	DG	NTSC, R_L = 150Ω		MAX4104/MAX4304		0.01		%
				MAX4105/MAX4305		0.02		
Differential Phase Error	DP	NTSC, R_L = 150Ω		MAX4104/MAX4304		0.01		degrees
				MAX4105/MAX4305		0.02		
Input Noise Voltage Density	e_n	f = 1MHz				2.1		$nV/\sqrt{Hz}$
Input Noise Current Density	i_n	f = 1MHz				3.1		$pA/\sqrt{Hz}$
Output Impedance	Z_{OUT}	f = 10MHz				1		Ω

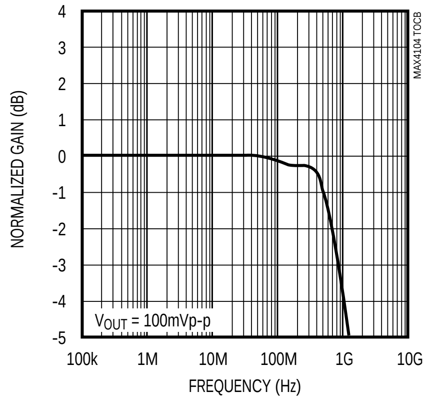
Typical Operating Characteristics

(V_{CC} = +5V, V_{EE} = -5V, R_F = 330Ω, R_L = 100Ω, T_A = +25°C, unless otherwise noted.)

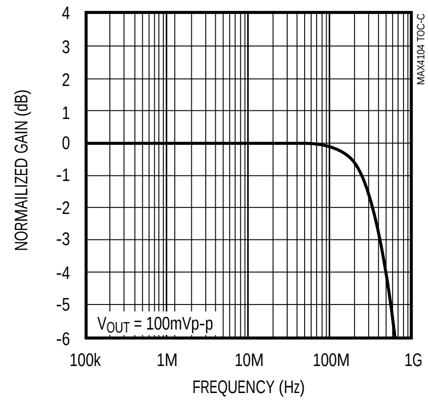
MAX4104 SMALL-SIGNAL GAIN vs. FREQUENCY (A_V = +1)



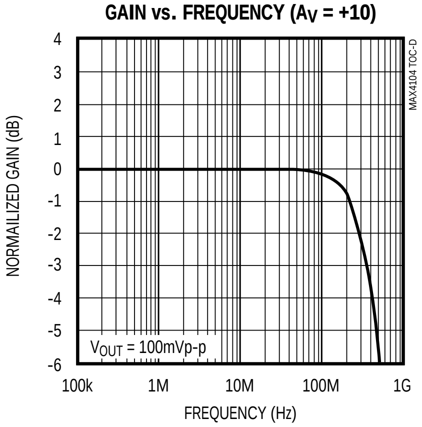
MAX4304 SMALL-SIGNAL GAIN vs. FREQUENCY (A_V = +2)



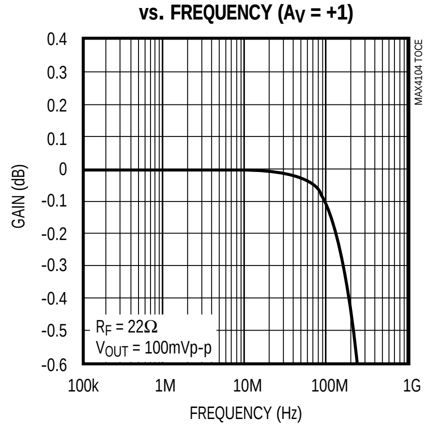
MAX4105 SMALL-SIGNAL GAIN vs. FREQUENCY (A_V = +5)



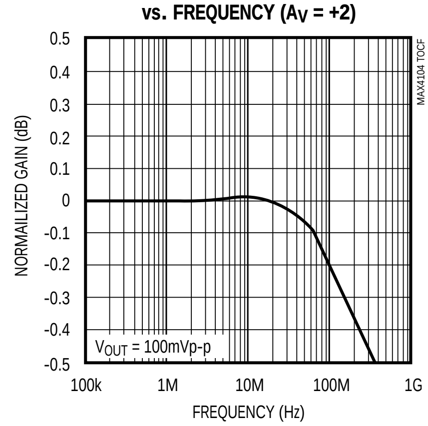
MAX4305 SMALL-SIGNAL GAIN vs. FREQUENCY (A_V = +10)



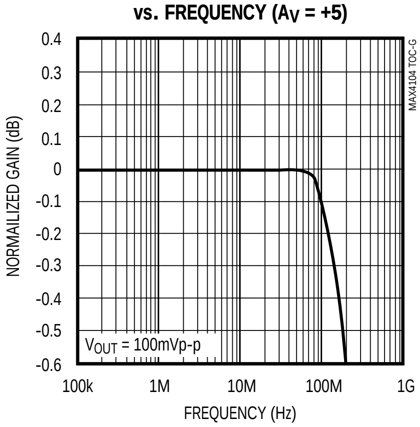
MAX4104 GAIN FLATNESS vs. FREQUENCY (A_V = +1)



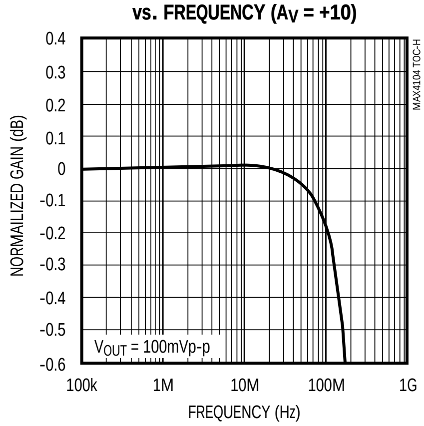
MAX4304 GAIN FLATNESS vs. FREQUENCY (A_V = +2)



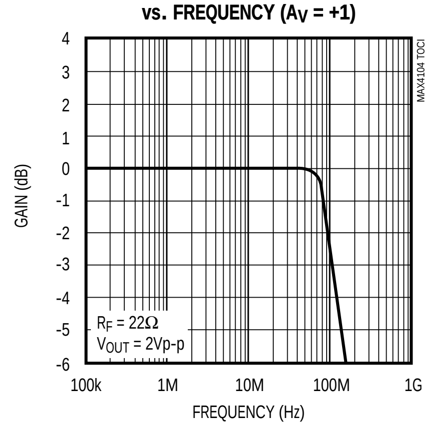
MAX4105 GAIN FLATNESS vs. FREQUENCY (A_V = +5)



MAX4305 GAIN FLATNESS vs. FREQUENCY (A_V = +10)



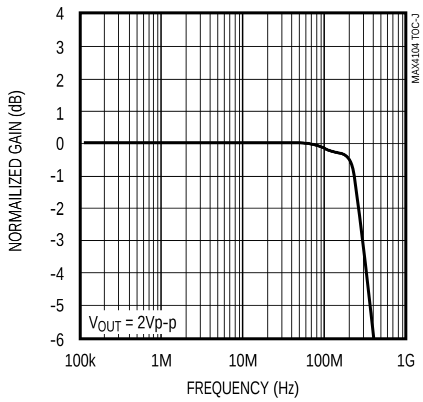
MAX4104 LARGE-SIGNAL GAIN vs. FREQUENCY (A_V = +1)



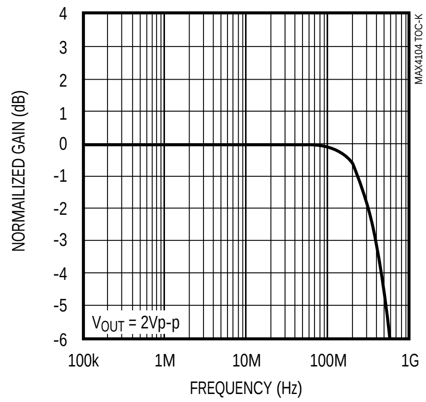
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, R_F = 330Ω, R_L = 100Ω, T_A = +25°C, unless otherwise noted.)

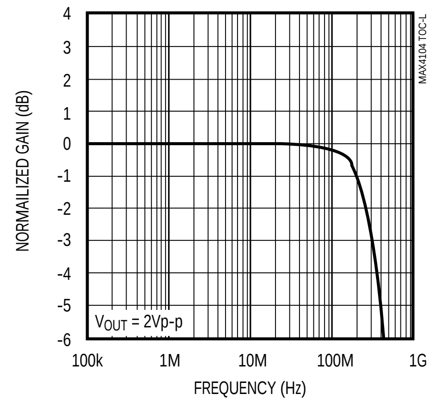
MAX4304 LARGE-SIGNAL GAIN vs. FREQUENCY (A_V = +2)



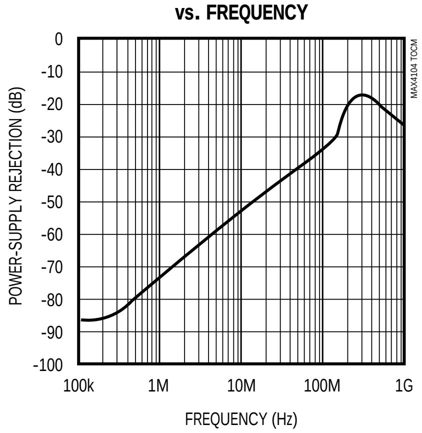
MAX4105 LARGE-SIGNAL GAIN vs. FREQUENCY (A_V = +5)



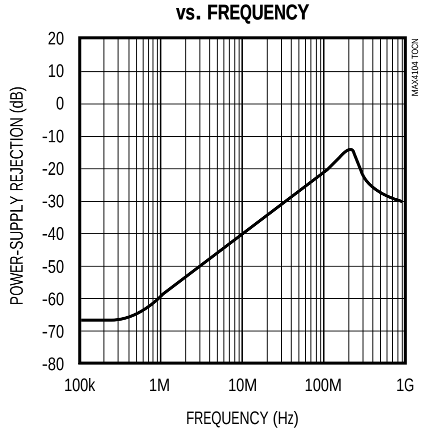
MAX4305 LARGE-SIGNAL GAIN vs. FREQUENCY (A_V = +10)



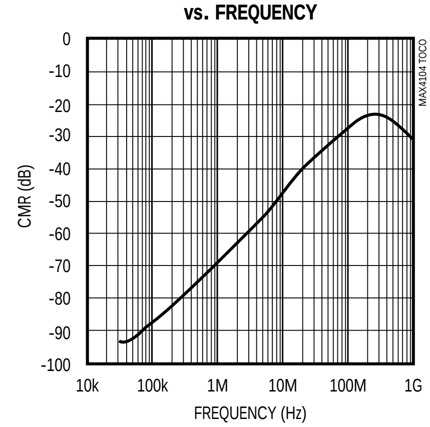
POSITIVE POWER-SUPPLY REJECTION vs. FREQUENCY



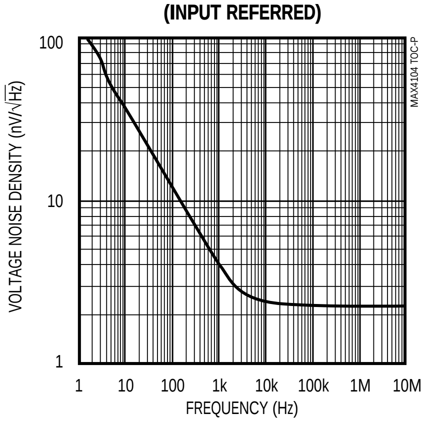
NEGATIVE POWER-SUPPLY REJECTION vs. FREQUENCY



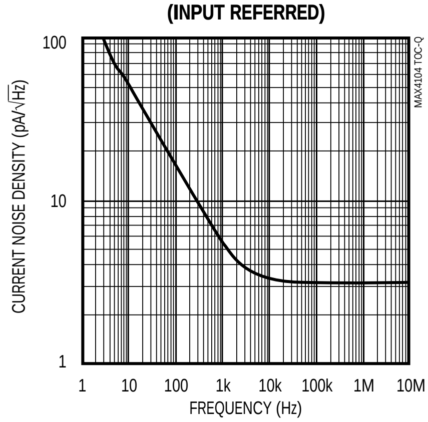
COMMON-MODE REJECTION vs. FREQUENCY



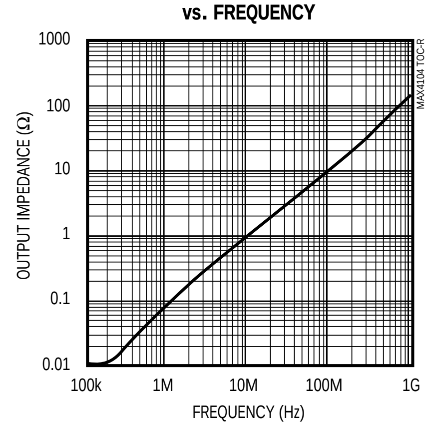
VOLTAGE NOISE DENSITY vs. FREQUENCY (INPUT REFERRED)



CURRENT NOISE DENSITY vs. FREQUENCY (INPUT REFERRED)



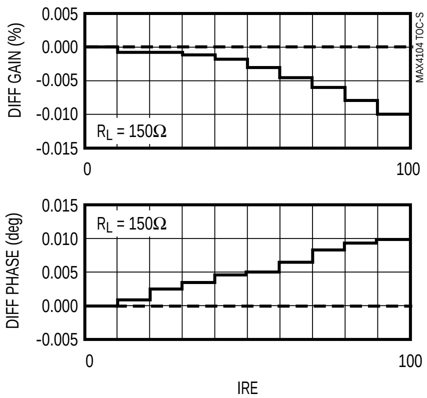
CLOSED-LOOP OUTPUT IMPEDANCE vs. FREQUENCY



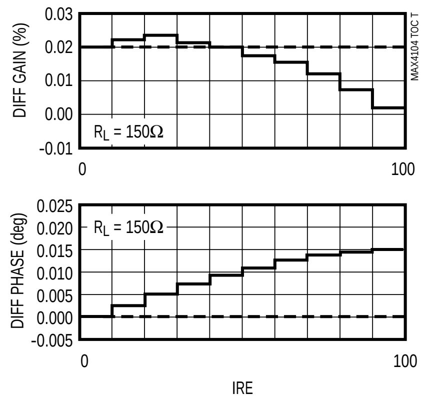
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, R_F = 330Ω, R_L = 100Ω, T_A = +25°C, unless otherwise noted.)

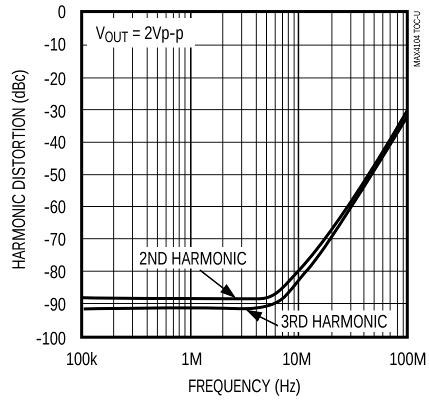
MAX4104/MAX4304 DIFFERENTIAL GAIN AND PHASE



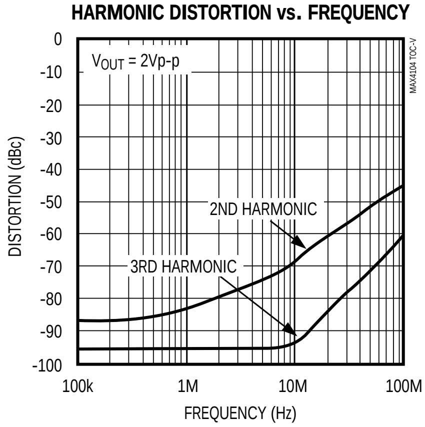
MAX4105/MAX4305 DIFFERENTIAL GAIN AND PHASE



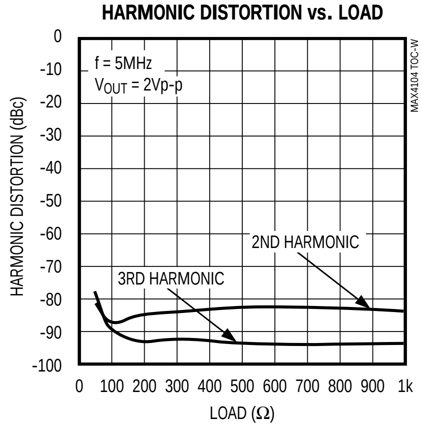
MAX4104/MAX4304 HARMONIC DISTORTION vs. FREQUENCY



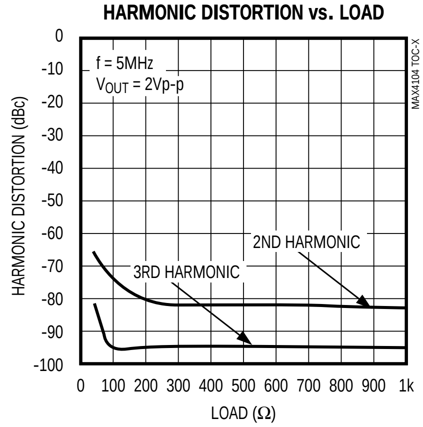
MAX4105/MAX4305 HARMONIC DISTORTION vs. FREQUENCY



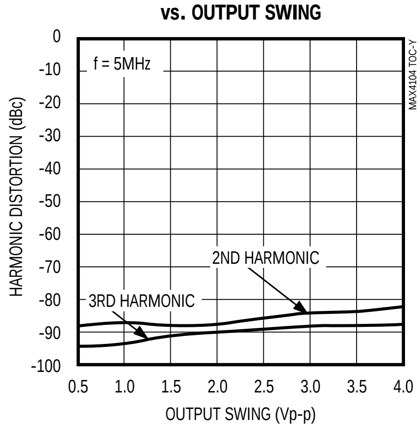
MAX4104/MAX4304 HARMONIC DISTORTION vs. LOAD



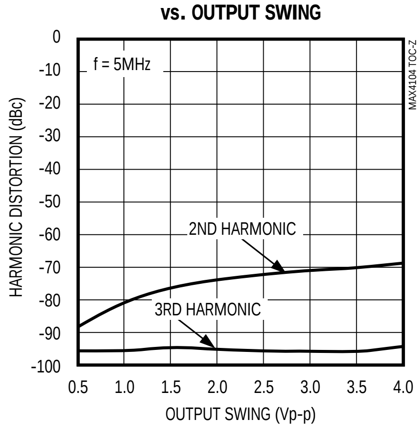
MAX4105/MAX4305 HARMONIC DISTORTION vs. LOAD



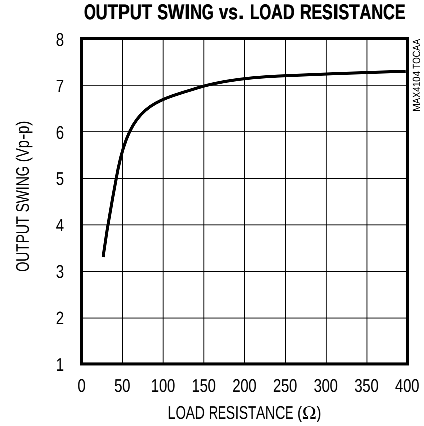
MAX4104/MAX4304 HARMONIC DISTORTION vs. OUTPUT SWING



MAX4105/MAX4305 HARMONIC DISTORTION vs. OUTPUT SWING



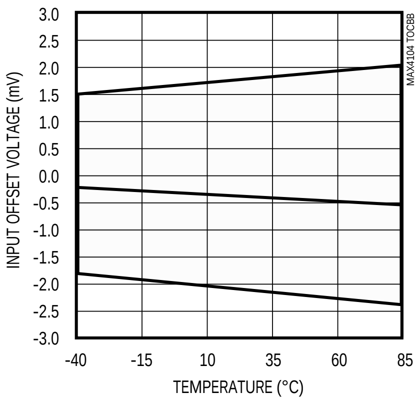
OUTPUT SWING vs. LOAD RESISTANCE



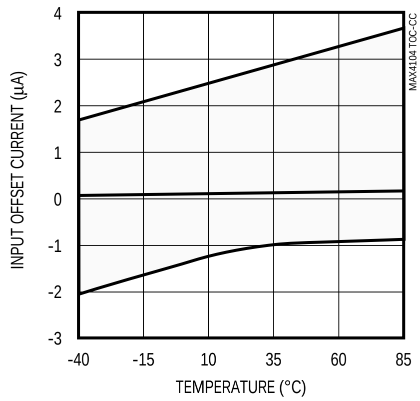
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, R_F = 330Ω, R_L = 100Ω, T_A = +25°C, unless otherwise noted.)

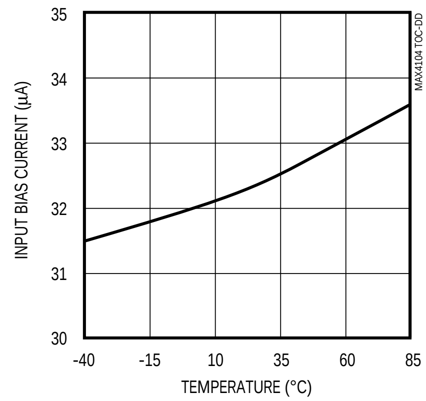
INPUT OFFSET VOLTAGE vs. TEMPERATURE



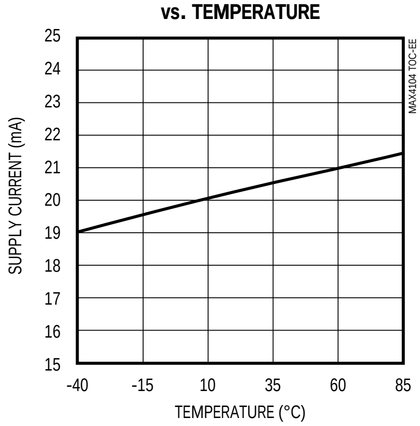
INPUT OFFSET CURRENT vs. TEMPERATURE



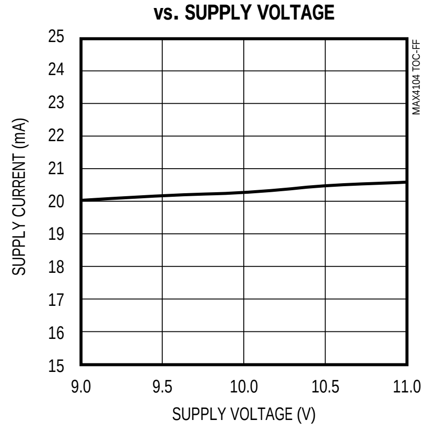
INPUT BIAS CURRENT vs. TEMPERATURE



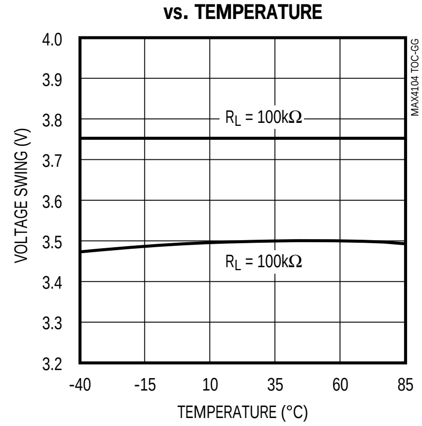
SUPPLY CURRENT vs. TEMPERATURE



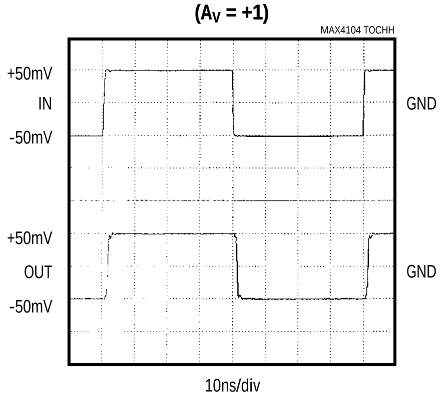
SUPPLY CURRENT vs. SUPPLY VOLTAGE



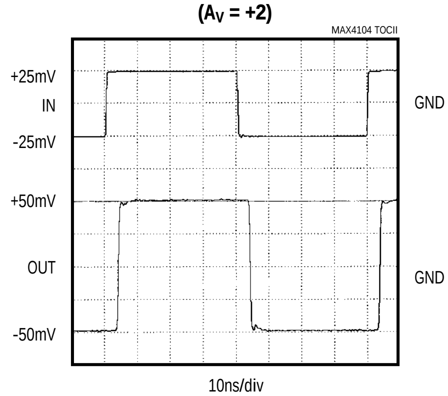
POSITIVE OUTPUT VOLTAGE SWING vs. TEMPERATURE



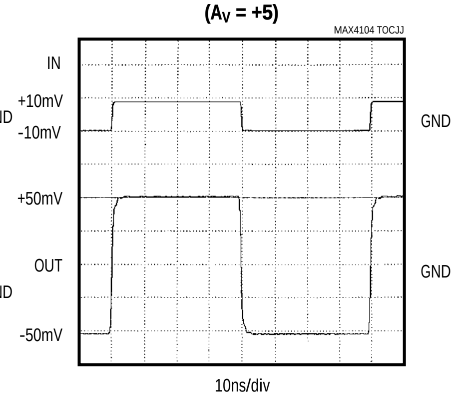
MAX4104 SMALL-SIGNAL PULSE RESPONSE (A_V = +1)



MAX4304 SMALL-SIGNAL PULSE RESPONSE (A_V = +2)



MAX4105 SMALL-SIGNAL PULSE RESPONSE (A_V = +5)

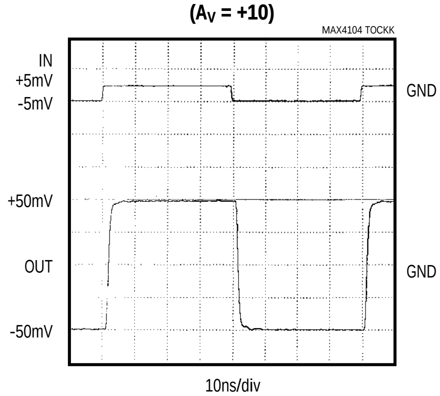


880MHz, Low-Noise, Low-Distortion Op Amps in SOT23-5

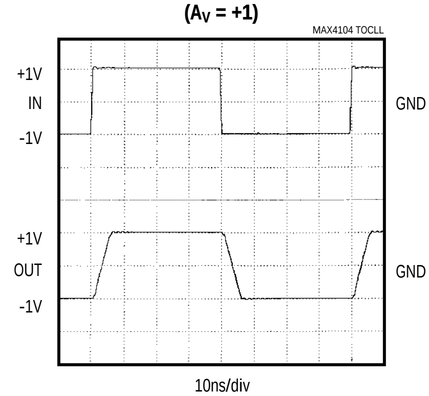
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, R_F = 330Ω, R_L = 100Ω, T_A = +25°C, unless otherwise noted.)

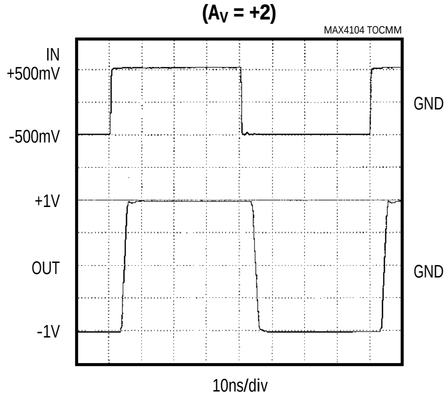
MAX4305
SMALL-SIGNAL PULSE RESPONSE
(A_V = +10)



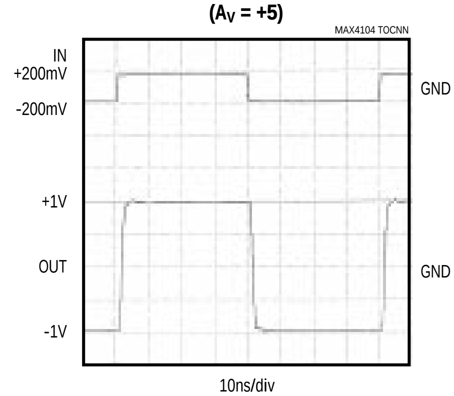
MAX4104
LARGE-SIGNAL PULSE RESPONSE
(A_V = +1)



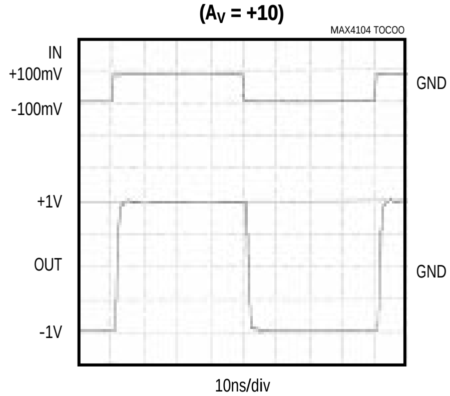
MAX4305
LARGE-SIGNAL PULSE RESPONSE
(A_V = +2)



MAX4105
LARGE-SIGNAL PULSE RESPONSE
(A_V = +5)



MAX4305
LARGE-SIGNAL PULSE RESPONSE
(A_V = +10)



Pin Description

PIN		NAME	FUNCTION
SOT23-5	SO		
—	1, 5, 8	N.C.	Not internally connected.
4	2	IN-	Amplifier Inverting Input
3	3	IN+	Amplifier Noninverting Input
2	4	V_{EE}	Negative Power Supply
1	6	OUT	Amplifier Output
5	7	V_{CC}	Positive Power Supply

Detailed Description

The MAX4104/MAX4105/MAX4304/MAX4305 are ultra-high-speed, low-noise amplifiers featuring -3dB bandwidths up to 880MHz, 0.1dB gain flatness up to 100MHz, and low differential gain and phase errors of 0.01% and 0.01°, respectively. These devices operate on dual power supplies ranging from ±3.5V to ±5.5V and require only 20mA of supply current.

The MAX4104/MAX4304/MAX4105/MAX4305 are optimized for minimum closed-loop gains of +1V/V, +2V/V, +5V/V and +10V/V (respectively) with corresponding -3dB bandwidths of 880MHz, 730MHz, 430MHz, and 350MHz. Each device in this family features a low input voltage noise density of only 2.1nV/$\sqrt{Hz}$ (at 1MHz), an output current drive of ±70mA, and spurious-free dynamic range as low as -88dBc (5MHz, R_L = 100Ω).

Applications Information

Layout and Power-Supply Bypassing

The MAX4104/MAX4105/MAX4304/MAX4305 have an extremely high bandwidth, and consequently require careful board layout, including the possible use of constant-impedance microstrip or stripline techniques.

To realize the full AC performance of these high-speed amplifiers, pay careful attention to power-supply bypassing and board layout. The PC board should have at least two layers: a signal and power layer on one side, and a large, low-impedance ground plane on the other side. The ground plane should be as free of voids as possible. With multilayer boards, locate the ground plane on a layer that incorporates no signal or power traces.

Regardless of whether or not a constant-impedance board is used, it is best to observe the following guidelines when designing the board:

1) Do not use wire-wrapped boards (they are much too inductive) or breadboards (they are much too capacitive).
2) Do not use IC sockets. IC sockets increase reactances.
3) Keep signal lines as short and straight as possible. Do not make 90° turns; round all corners.
4) Observe high-frequency bypassing techniques to maintain the amplifier's accuracy and stability.
5) Bear in mind that, in general, surface-mount components have shorter bodies and lower parasitic reactance, resulting in greatly improved high-frequency performance over through-hole components.

The bypass capacitors should include 1nF and 0.1µF ceramic surface-mount capacitors between each supply pin and the ground plane, located as close to the package as possible. Optionally, place a 10µF tantalum capacitor at the power supply pins' point of entry to the PC board to ensure the integrity of incoming supplies. The power-supply trace should lead directly from the tantalum capacitor to the V_{CC} and V_{EE} pins. To minimize parasitic inductance, keep PC traces short and use surface-mount components.

Input termination resistors and output back-termination resistors, if used, should be surface-mount types, and should be placed as close to the IC pins as possible.

DC and Noise Errors

The MAX4104/MAX4105/MAX4304/MAX4305 output offset voltage, V_{OUT} (Figure 1), can be calculated with the following equation:

$$V_{OUT} = [V_{OS} + (I_B+ \times R_S) + (I_B- \times (R_F \parallel R_G))]\ [1 + R_F / R_G]$$

where:

V_{OS} = input offset voltage (in volts)

$1 + R_F/R_G$ = amplifier closed-loop gain (dimensionless)

I_B+ = noninverting input bias current (in amps)

I_B- = inverting input bias current (in amps)

R_G = gain-setting resistor (in ohms)

R_F = feedback resistor (in ohms)

R_S = source resistor at noninverting input (in ohms)

The following equation represents output noise density:

$$e_{n(OUT)} = \left[1 + \frac{R_F}{R_G}\right] \sqrt{\left(i_n \times R_S\right)^2 + \left[i_n \times \left(R_F \parallel R_G\right)\right]^2 + e_n^{\,2}}$$

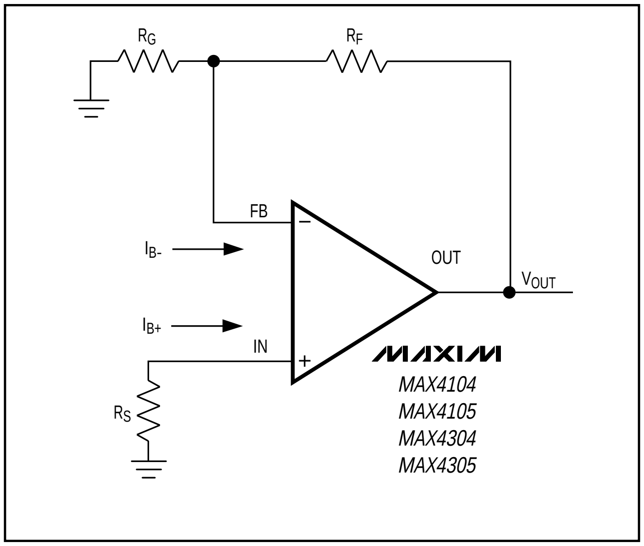


Figure 1. Output Offset Voltage

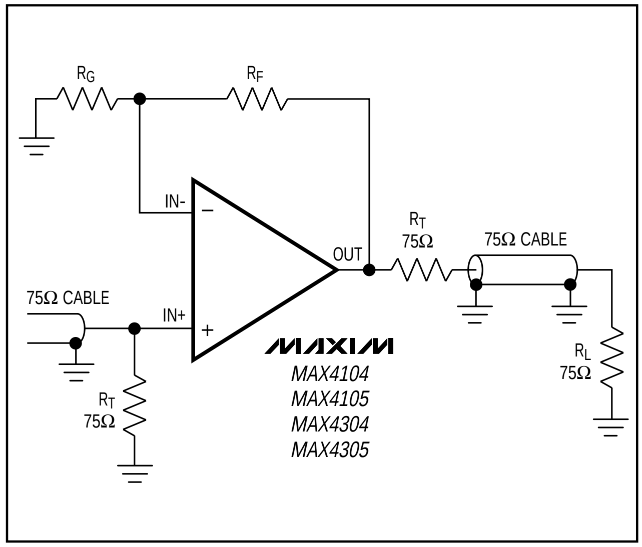


Figure 2. Video Line Driver

where:

i_n = input noise current density (in pA/$\sqrt{Hz}$)

e_n = input noise voltage density (in nV/$\sqrt{Hz}$)

The MAX4104/MAX4105/MAX4304/MAX4305 have a very low, 2.1nV/$\sqrt{Hz}$ input noise voltage density and 3.1pA/$\sqrt{Hz}$ input noise current density.

An example of DC-error calculations, using the MAX4304 typical data and the typical operating circuit with $R_F = R_G = 330\Omega$ ($R_F \parallel R_G = 165\Omega$) and $R_S = 50\Omega$, gives:

$$V_{OUT} = \left[\left(32 \times 10^{-6}\right)(50) + \left(32 \times 10^{-6}\right)(165\Omega) + 1 \times 10^{-3}\right][1 + 1]$$
$$V_{OUT} = 15.8mV$$

Calculating total output noise in a similar manner yields the following:

$$e_{n(OUT)} =$$
$$[1+1]\sqrt{\left(3.1 \times 10^{-12} \times 50\right)^2 + \left(3.1 \times 10^{-12} \times 165\right)^2 + \left(2.1 \times 10^{-9}\right)^2}$$
$$e_{n(OUT)} = 4.3nV\sqrt{Hz}$$

With a 200MHz system bandwidth, this calculates to 60.8µV_{RMS} (approximately 365µVp-p, using the six-sigma calculation).

ADC Input Buffers

Input buffer amplifiers can be a source of significant error in high-speed ADC applications. The input buffer is usually required to rapidly charge and discharge the ADC's input, which is often capacitive. In addition, the input impedance of a high-speed ADC often changes very rapidly during the conversion cycle—a condition that demands an amplifier with very low output impedance at high frequencies to maintain measurement accuracy. The combination of high-speed, fast slew rate, low noise, and low-distortion available in the MAX4104/MAX4105/MAX4304/MAX4305 makes them ideally suited for use as buffer amplifiers in high-speed ADC applications.

Video Line Driver

The MAX4104/MAX4105/MAX4304/MAX4305 are optimized to drive coaxial transmission lines when the cable is terminated at both ends, as shown in Figure 2. To minimize reflections and maximize power transfer, select the termination resistors to match the characteristic impedance of the transmission line. Cable frequency response can cause variations in the flatness of the signal.

Driving Capacitive Loads

The MAX4104/MAX4105/MAX4304/MAX4305 provide maximum AC performance when driving no output load capacitance. This is the case when driving a correctly terminated transmission line (i.e., a back-terminated cable).

In most amplifier circuits, driving a large load capacitance increases the chance of oscillations occurring. The amplifier's output impedance and the load capacitor combine to add a pole and excess phase to the loop response. If the pole's frequency is low enough

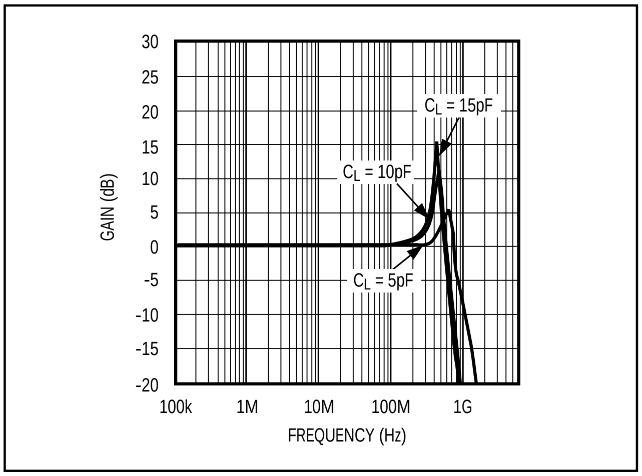


Figure 3a. MAX4104 Frequency Response with Capacitive Load and No Isolation Resistor

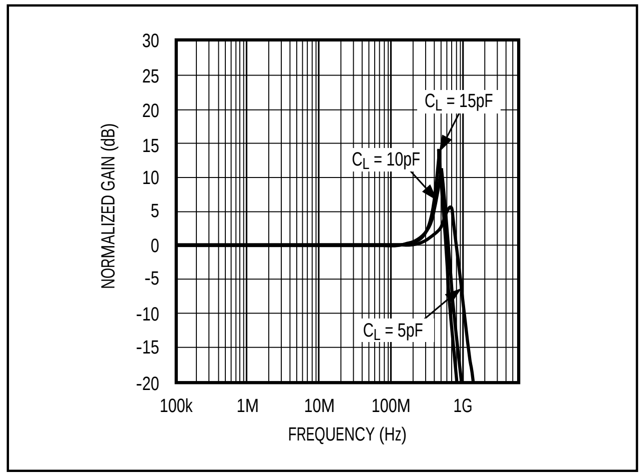


Figure 3b. MAX4304 Frequency Response with Capacitive Load and No Isolation Resistor

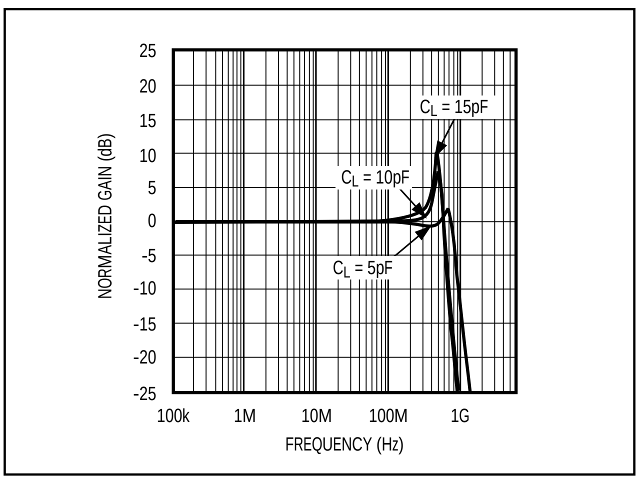


Figure 3c. MAX4105 Frequency Response with Capacitive Load and No Isolation Resistor

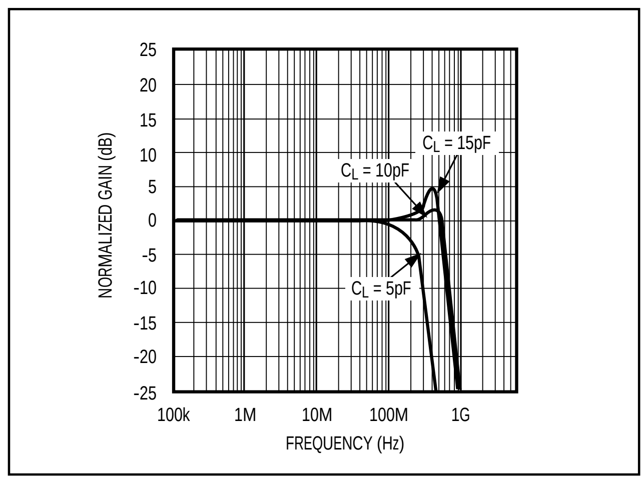


Figure 3d. MAX4305 Frequency Response with Capacitive Load and No Isolation Resistor

and phase margin is degraded sufficiently, oscillations may result.

A second concern when driving capacitive loads originates from the amplifier's output impedance, which appears inductive at high frequencies. This inductance forms an L-C resonant circuit with the capacitive load, which causes peaking in the frequency response and degrades the amplifier's phase margin.

The MAX4104/MAX4105/MAX4304/MAX4305 drive capacitive loads up to 10pF without oscillation. However, some peaking may occur in the frequency domain (Figure 3). To drive larger capacitance loads or to reduce ringing, add an isolation resistor between the amplifier's output and the load (Figure 4).

The value of R_{ISO} depends on the circuit's gain and the capacitive load (Figure 5). Figure 6 shows the MAX4104/MAX4105/MAX4304/MAX4305 frequency response with the isolation resistor and a capacitive load. With higher capacitive values, bandwidth is dominated by the RC network formed by R_{ISO} and C_L; the bandwidth of the amplifier itself is much higher. Also note that the isolation resistor forms a divider that decreases the voltage delivered to the load.

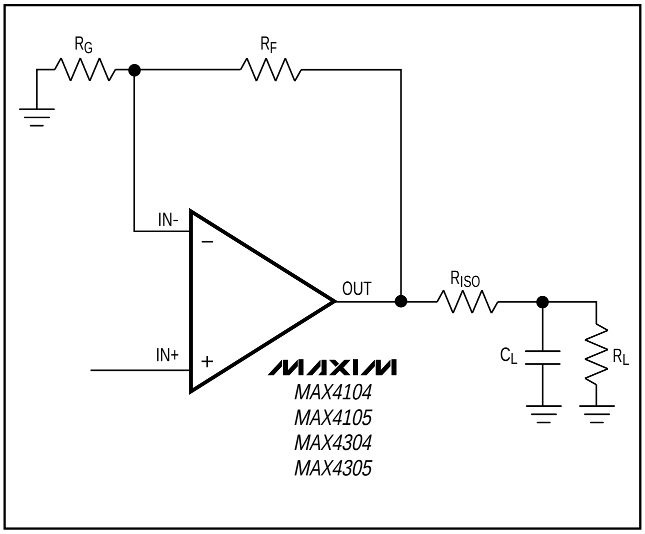


Figure 4. Using an Isolation Resistor (R_{ISO}) for High Capacitive Loads

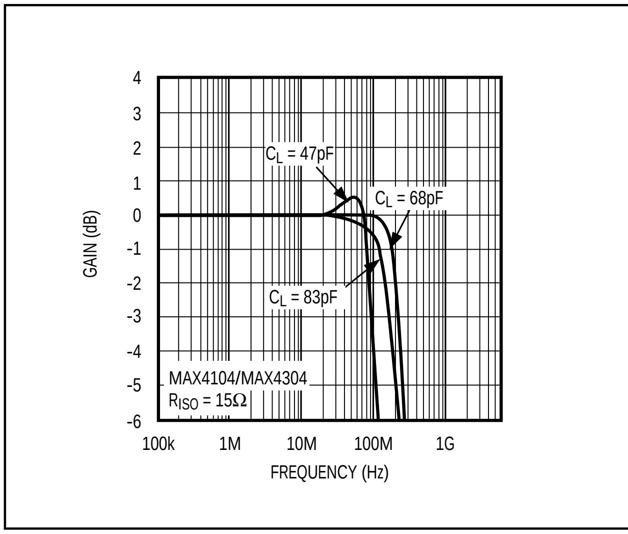


Figure 6. Frequency Responses vs. Capacitive Load with 15Ω Isolation Resistor

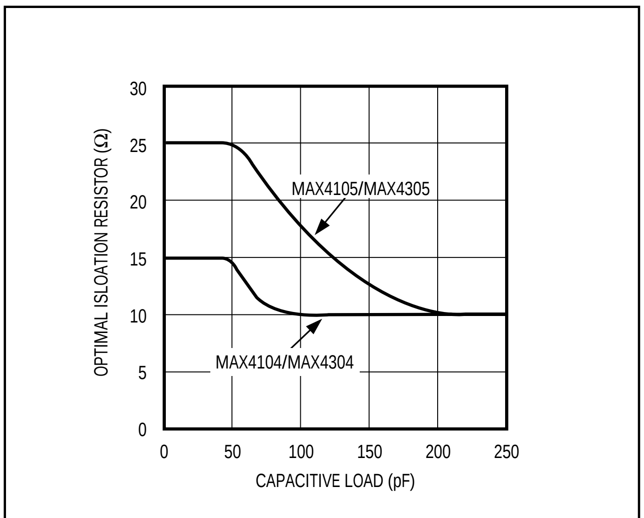


Figure 5. Optimal Isolation Resistor (R_{ISO}) vs. Capacitive Load

Maxim's High-Speed Evaluation Boards

Figure 7 shows a suggested layout for Maxim's high-speed, single-amplifier evaluation boards. This board was developed using the techniques described previously (*see Layout and Power-Supply Bypassing Section*). The smallest available surface-mount resistors were used for the feedback and back-termination resistors to minimize the distance from the IC to these resistors, thus reducing the capacitance associated with longer lead lengths.

SMA connectors were used for best high-frequency performance. Because distances are extremely short, performance is unaffected by the fact that inputs and outputs do not match a 50Ω line. However, in applications that require lead lengths greater than 1/4 of the wavelength of the highest frequency of interest, constant-impedance traces should be used.

Fully assembled evaluation boards are available for the MAX4104 in an 8-pin SO package.

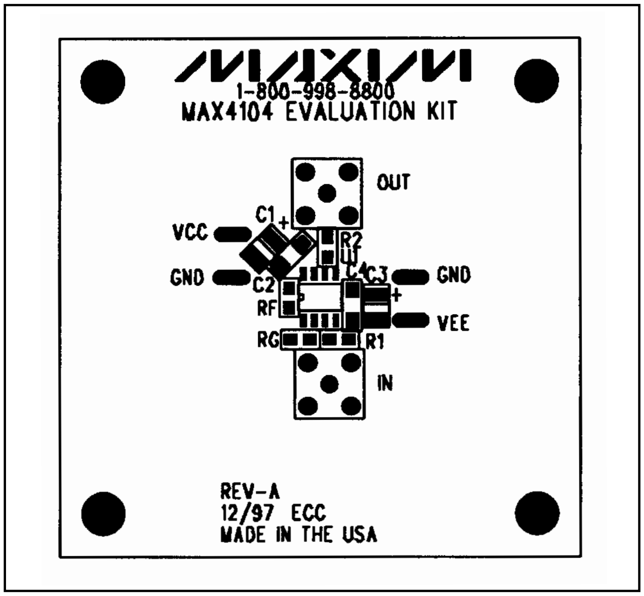


Figure 7a. MAX4104 EV Kit (SO) Component Placement Guide

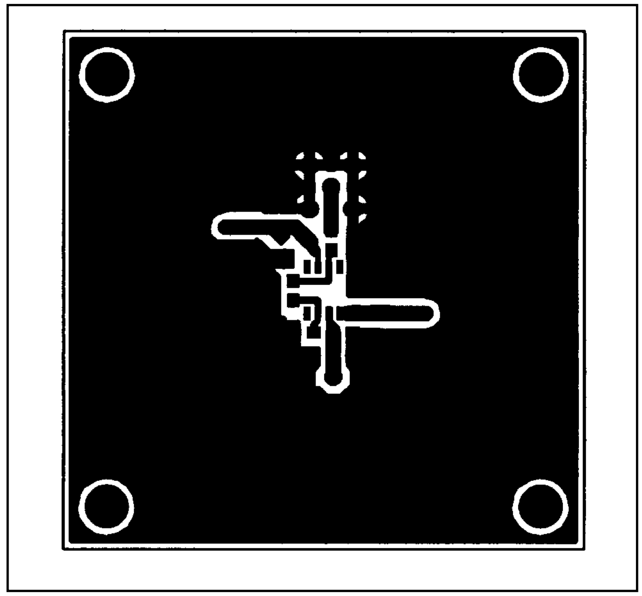


Figure 7b. MAX4104 EV Kit (SO) PC Board Layout—Component Side

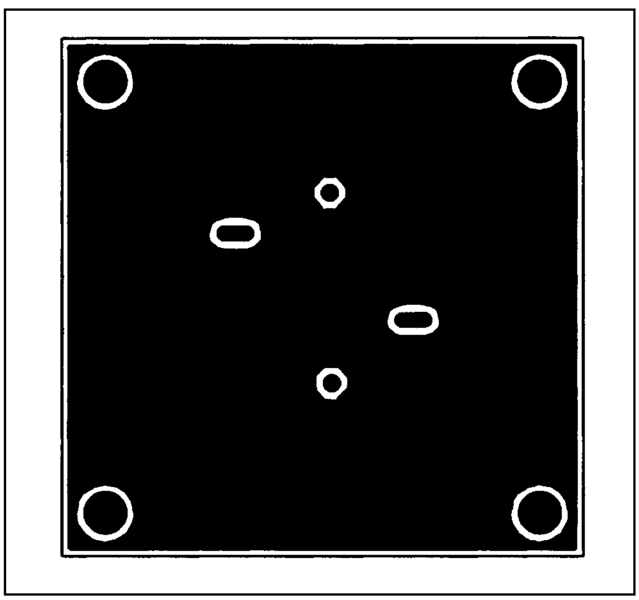
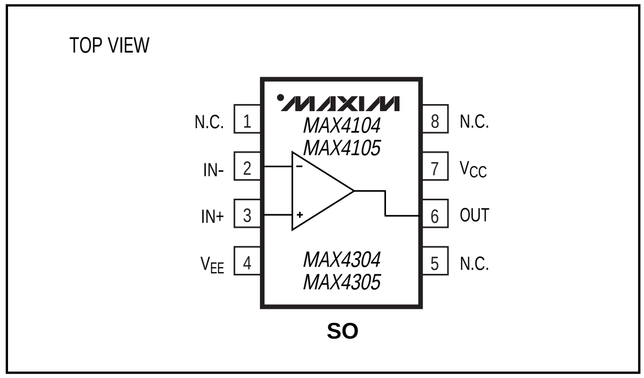


Figure 7c. MAX4104 EV Kit (SO) PC Board Layout—Solder Side

Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE	SOT TOP MARK
MAX4105ESA	-40°C to +85°C	8 SO	—
MAX4105EUK-T	-40°C to +85°C	5 SOT23-5	ACCP
MAX4304ESA	-40°C to +85°C	8 SO	—
MAX4304EUK-T	-40°C to +85°C	5 SOT23-5	ACCQ
MAX4305ESA	-40°C to +85°C	8 SO	—
MAX4305EUK-T	-40°C to +85°C	5 SOT23-5	ACCR

Pin Configurations (continued)

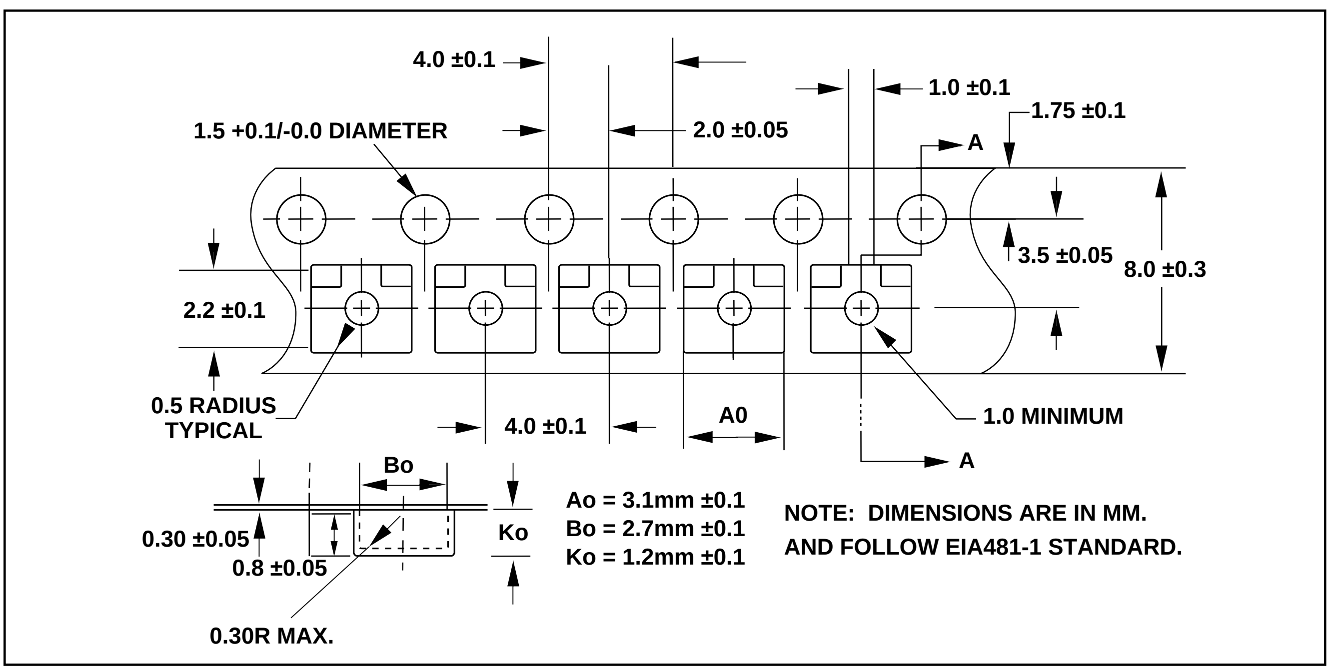


Chip Information

TRANSISTOR COUNT: 44

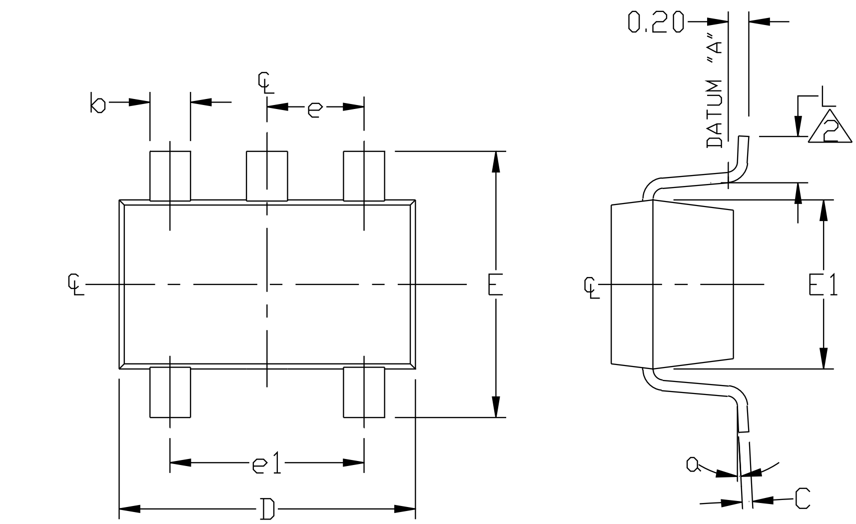
SUBSTRATE CONNECTED TO V_{EE}

Tape-and-Reel Information

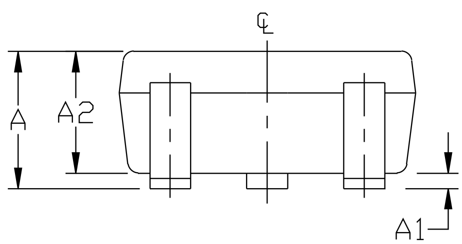


Package Information

SOT5L.EPS



SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.35	0.50
C	0.08	0.20
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.75
L	0.35	0.55
e	0.95 REF	
e1	1.90 REF	
a	0°	10°

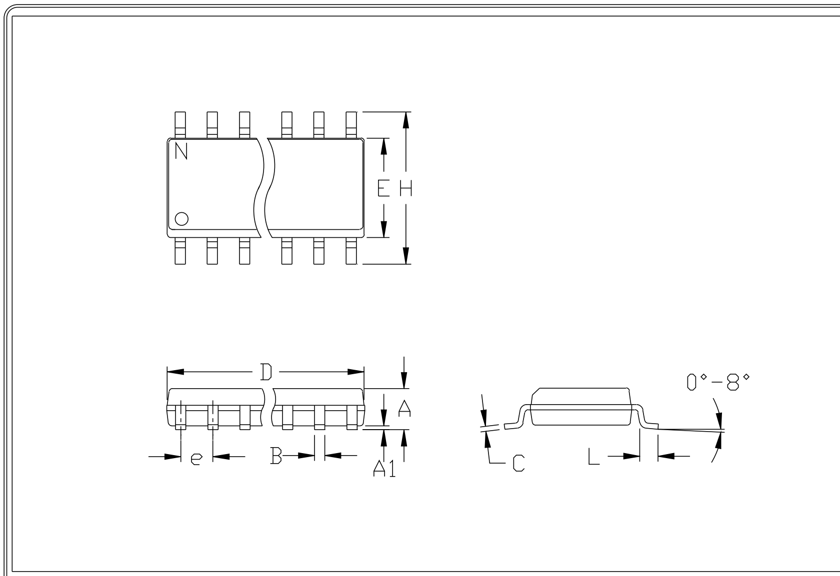


NOTE:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. (2) FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE.
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.

MAXIM
PROPRIETARY INFORMATION
TITLE:
PACKAGE OUTLINE, SOT23, 5L
APPROVAL | DOCUMENT CONTROL NO. 21-0057 | REV B | 1/1

Package Information (continued)



	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050		1.27	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27

	INCHES		MILLIMETERS			
	MIN	MAX	MIN	MAX	N	MS012
D	0.189	0.197	4.80	5.00	8	A
D	0.337	0.344	8.55	8.75	14	B
D	0.386	0.394	9.80	10.00	16	C

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
3. LEADS TO BE COPLANAR WITHIN .102mm (.004")
4. CONTROLLING DIMENSION: MILLIMETER
5. MEETS JEDEC MS012-XX AS SHOWN IN ABOVE TABLE
6. N = NUMBER OF PINS

MAXIM 120 SAN GABRIEL DR. SUNNYVALE CA 94086 FAX (408) 737 7194 PROPRIETARY INFORMATION | TITLE: PACKAGE FAMILY OUTLINE: SOIC .150" | 1/1 | DOCUMENT CONTROL NUMBER 21-0041 REV A