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APPROVED BY: DATE:	ELECTRONIC COMPONENTS GROUP SHARP CORPORATION	REPRESENTATIVE DIVISION
J. Yoshikawa Man. 10. 1797	SPECIFICATION	OPTO-ELECTRONIC DEVICES DIV.
I	22 No.	ng name
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BY	Opto ELE	c-Electronic Devices Div. COM Group RP CORPORATION

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1. Application

This specification applies to the outline and characteristics of photocoupler Model No. PC814.

2. Outline

Refer to the attached drawing No. CY8996K02.

3. Ratings and characteristics

Refer to the attached sheet, page 4 to 6.

4. Reliability

Refer to the attached sheet, page 7.

5. Incoming inspection

Refer to the attached sheet, page 8.

6. Supplement

- 6.1 Isolation voltage shall be measured in the following method.
 - (1) Short between anode and cathode on the primary side, and among collector, emitter on the secondary side.
 - (2) The dielectric withstand tester with zero-cross circuit shall be used.
 - (3) The wave form of applied voltage shall be a sine wave.

 (It is recommended that the isolation voltage be measured in insulation oil.)

6.2 Collector current (Ic) Delivery rank table

("○" mark indicates business dealing name of ordered product)

Rank at delivery	Business dealing name	Rank mark	Ic (mA)
0	PC814XP	A or no mark	0.2 to 3.0
	PC814XP1	A	0.5 to 1.5

Test conditions	
$I_{ m F}$ = $\pm1{ m mA}$ $V_{ m CE}$ = $5{ m C}$ T a= $25{ m C}$	

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6.3 Package specification

Refer to the attached sheet, page 9 to 11.

6.4 This Model is approved by UL.

Approved Model No.: PC814

UL file No.: E64380

- 6.5 This photocoupler is designed for AC input..
- 6.6 This product is not designed against irradiation.

This product is assembled with electrical input and output.

This product incorporates non-coherent light emitting diode.

6.7 ODS materials

This product shall not contain the following materials. Also, the following materials shall not be used in the production process for this product.

Materials for ODS : CFC_S, Halon, Carbon tetrachloride,

1.1.1-Trichloroethane (Methylchloroform)

6.8 Brominated flame retardants

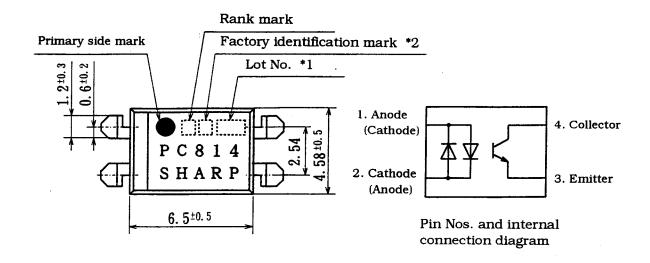
Specific brominated flame retardants such as the \mbox{PBBO}_S and \mbox{PBB}_S are not used in this device at all.

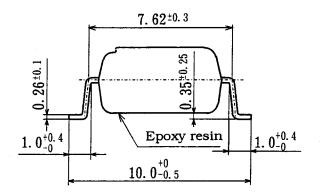
7. Notes

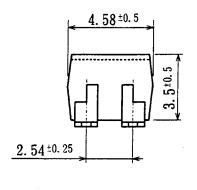
Refer to the attached sheet-1.

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2. Outline







*1) 2-digit number shall be marked according to DIN standard.

*2) Factory identification mark shall be or shall not be marked.

Product mass: Approx. 0.21g

UNIT : 1/1 mm		
Name	PC814 Outline Dimensions (Business dealing name : PC814XP)	
Drawing No.	CY8996K02	

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3. Ratings and characteristics

3.1 Absolute maximum ratings

Ta=25°C

Parameter		Symbol	Rating	Unit
	*1 Forward current	$I_{ m F}$	±50	mA
Input	*2 Peak forward current	I_{FM}	±1	A
	*1 Power dissipation	Р	70	mW
	Collector-emitter voltage	V_{CEO}	35	V
0.1.1	Emitter-collector voltage	V _{ECO}	6	V
Output	Collector current	Ic	50	mA
	*1 Collector power dissipation	Pc	150	mW
*1 Total power dissipation Operating temperature Storage temperature *3 Isolation voltage *4 Soldering temperature		Ptot	200	mW
		Topr	-30 to +100	Č
		Tstg	-55 to +125	Ç
		Viso	5	kVrms
		Tsol	260	Ç

^{*1} The derating factors of absolute maximum ratings due to ambient temperature are shown in Fig. 1 to 4.

^{*2} Pulse width $\leq 100 \mu s$, Duty ratio : 0.001 (Refer to Fig. 5)

^{*3} AC for 1 min, 40 to 60%RH

^{*4} For 10 s

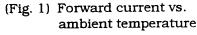
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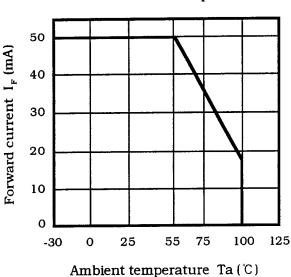
3.2 Electro-optical characteristics

Ta=25℃

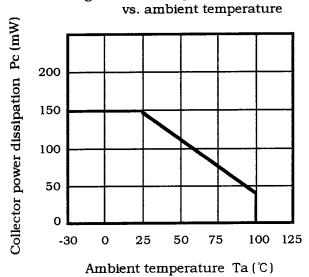
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage	V_{F}	I _F =±20mA	-	1.2	1.4	V
Input	Peak forward voltage	$V_{\rm FM}$	I _{FM} =±0.5A	-	-	3.0	V
	Terminal capacitance	Ct	V=0, f=1kHz	-	50	250	pF
	Dark current	I _{CEO}	V _{CE} =20V, I _F =0	-	•	100	nA
Output	Collector-emitter breakdown voltage	$\mathrm{BV}_{\mathrm{CEO}}$	Ic=0.1mA I _F =0	35	-	-	V
	Emitter-collector breakdown voltage	$\mathrm{BV}_{\mathrm{ECO}}$	$I_{E}=10 \ \mu A, \ I_{F}=0$	6	-	<u>.</u>	V
	Collector current	I c	$I_{F} = \pm 1 \text{mA}$ $V_{CE} = 5V$	0.2	-	3.0	mA
	Collector-emitter saturation voltage	V _{CE(sat)}	I _F =±20mA Ic=1mA	-	0.1	0.2	V
Transfer	Isolation resistance	Riso	DC500V 40 to 60%RH	5×10 ¹⁰	1011	-	Ω
charac- teristics	Floating capacitance	Cf	V=0, f=1MHz	-	0.6	1.0	pF
	Cut-off frequency	fc	V_{CE} =5V, Ic=2mA R _L =100 Ω , -3dB	15	80	-	kHz
	Response time (Rise)	tr	V _{CE} =2V Ic=2mA	-	4	18	μS
	Response time (Fall)	tf	$R_L=100 \Omega$	-	3	18	μs

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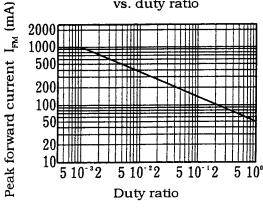




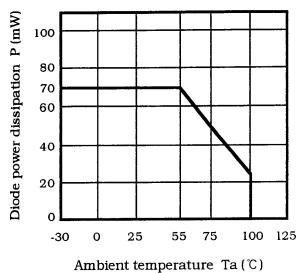
(Fig. 3) Collector power dissipation



(Fig. 5) Peak forward current vs. duty ratio

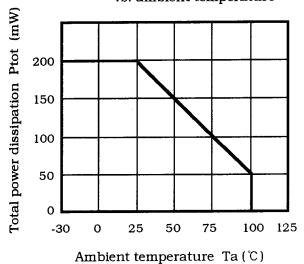


(Fig. 2) Diode power dissipation vs. ambient temperature



implem temperature ra (0)

(Fig. 4) Total power dissipation vs. ambient temperature



Pulse width ≦100 μs Ta=25℃

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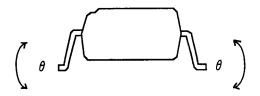
4. Reliability

The reliability of products shall satisfy items listed below.

Confidence level: 90% LTPD: 10%/20%

Test Items	Test Conditions	Failure Judgement Criteria	Samples (n) Defective(C)
Solderability *2	230℃, 5 s		n=11, C=0
Soldering heat	260℃, 10 s		n=11, C=0
Terminal strength (Tension)	Weight: 5N 5 s/each terminal		n=11, C=0
Terminal strength (Bending) *3	Weight: 2.5N 2 times/each terminal	$V_F > U \times 1.2$	n=11, C=0
Mechanical shock	15000m/s ² , 0.5ms 3 times/ \pm X, \pm Y, \pm Z direction	I_{CEO} >U×2 I_{C} <l×0.7< td=""><td>n=11, C=0</td></l×0.7<>	n=11, C=0
Variable frequency vibration	100 to 2000 to 100Hz/4min 200m/s ² 4 times/ X, Y, Z direction	$ m V_{CE(sat)} > U imes 1.2$	n=11, C=0
Temperature cycling	1 cycle -55°C to +125°C (30min) (30min) 20 cycles test	11 1 1 1 1 1 1 1 1 1	n=22,C=0
High temp. and high humidity storage	+60℃, 90%RH, 1000h	U : Upper specification limit	n=22,C=0
High temp. storage	+125℃, 1000h	L : Lower specification	n=22,C=0
Low temp. storage	-55°C, 1000h	limit	n=22,C=0
Operation life	I _F =±50mA, Ptot=200mW Ta=25℃, 1000h		n=22,C=0

- *1 Test method, conforms to JIS C 7021.
- *2 Solder shall adhere at the area of 95% or more of immersed portion of lead, and pin hole or other holes shall not be concentrated on one portion.
- *3 Terminal bending direction is shown below.



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5. Incoming inspection

- 5.1 Inspection items
- (1) Electrical characteristics

$$V_F$$
, I_{CEO} , $V_{CE(sat)}$, Ic, Riso, Viso

- (2) Appearance
- 5.2 Sampling method and Inspection level

A single sampling plan, normal inspection level II based on ISO 2859 is applied. The AQL according to the inspection items are shown below.

Defect	Inspection item	AQL (%)
Major defect	Electrical characteristics Unreadable marking	0.1
Minor defect	Appearance defect except the above mentioned.	0.4

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6.3 Package specifications

6.3.1 Taping conditions

(1) Tape structure and Dimensions (Refer to the attached sheet, Page 10)

The tape shall have a structure in which a cover tape is sealed heat-pressed on the carrier tape of protect against static electricity.

(2) Reel structure and Dimensions (Refer to the attached sheet, Page 11)

The taping reel shall be of plastic with its dimensions as shown in the attached drawing.

(3) Direction of product insertion (Refer to the attached sheet, Page 11)

Product direction in carrier tape shall direct to the anode mark at the hole side on the tape.

(4) Joint of tape

The cover tape and carrier tape in one reel shall be jointless.

(5) The way to repair taped failure devices

The way to repair taped failure devices cut a bottom of carrier tape with a cutter, and after replacing to good devices, the cut portion shall be sealed with adhesive tape.

6.3.2 Adhesiveness of cover tape

• The exfoliation force between carrier tape and cover tape shall be 0.2N to 1N for the angle from 160° to 180° .

6.3.3 Rolling method and quantity

• Wind the tape back on the reel so that the cover tape will be outside the tape. Attach more than 20cm of blank tape to the trailer and the leader of the tape and fix the both ends with adhesive tape. One reel shall contain 2000pcs.

6.3.4 Marking

- The outer packaging case shall be marked with following information.
 - * Model No. * Number of pieces delivered * Production date

6.3.5 Storage condition

• Taped products shall be stored at the temperature 5 to 30℃ and the humidities lower than 70%RH.

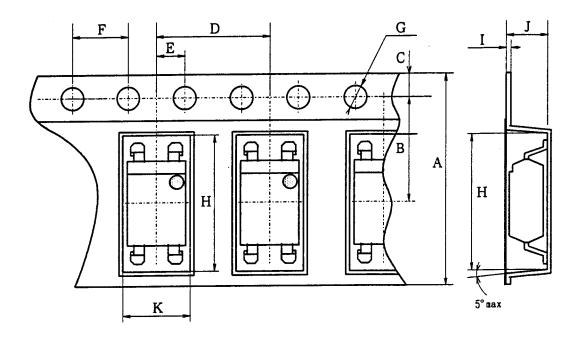
6.3.6 Safety protection during shipping

 There shall be no deformation of component or degradation of electrical characteristics due to shipping.

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Tape structure and Dimensions

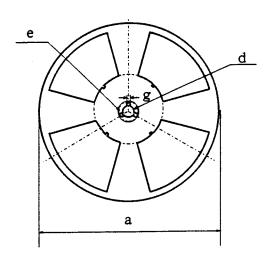


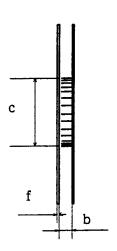
Dimensions list (Unit: mm)

A	В	С	D	E	
±0.3	±0.1 7.5	±0.1 1.75	±0.1 8.0	±0.1 2.0	
F	G	Н	I	J	К
±0.1	+0.1 -0.0 \$\phi\$ 1.5	±0.1 10.4	±0.05	±0.1 4.2	±0.1 5.1

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Reel structure and Dimensions

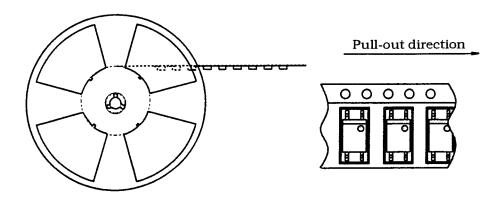




Dimensions list (Unit: mm)

Ī	а	b	С	d	e	f	g
	330	17.5 ± 1.5	100±1.0	13±0.5	23±1	2.0±0.5	2.0±0.5

Direction of product insertion



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PC814XP Attach
sheet-1-1

Precautions for Photocouplers

1 For cleaning

(1) Solvent cleaning: Solvent temperature 45°C or less Immersion for 3 min or less

(2) Ultrasonic cleaning: The effect to device by ultrasonic cleaning differs

by cleaning bath size, ultrasonic power

output, cleaning time, PCB size or device mounting condition etc. Please test it in actual using condition and confirm that doesn't occur any defect before starting

the ultrasonic cleaning.

(3) Applicable solvent: Ethyl alcohol, Methyl alcohol, Isopropyl alcohol

In case when the other solvent is used, there are cases that the packaging resin is eroded. Please use the other solvent after thorough confirmation is performed in actual using condition.

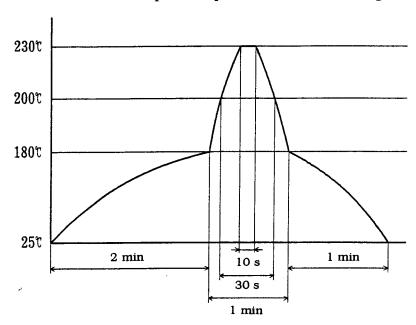
- 2. The LED used in the Photocoupler generally decreases the light emission power by operation. In case of long operation time, please design the circuit with considering the degradation of the light emission power of the LED. (50%/5years)
- 3. There are cases that the deviation of the CTR and the degradation of the light emission power of the LED become big at I_F is less than 1.0mA. Please design the circuit with considering this point.

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4. Precautions for Soldering Photocouplers

(1) If solder reflow:

It is recommended that only one soldering be done at the temperature and the time within the temperature profile as shown in the figure below.



(2) Other precautions

An infrared lamp used to heat up for soldering may cause a localized temperature rise in the resin. So keep the package temperature within that specified in Item 2. Also avoid immersing the resin part in the solder.

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