PREPARED BY: DATE:			SPEC No.	ED-95151
M. Kataks Morrinder 28, 1995	SHA	RPA	ESUE	November 28, 1995
APPROVED BY: DATE:			PAGE*	O Pages
J. Josephone Nov 37 182	ELECTRONIC CO GROUP SHARP		REPRESEN	TATIVE DIVISION
	SPECIFI	CATION		ro-electronic vices div.
DEVICE SP.	ECIFICATION FOR	Business de	ealing name	
PHOT	OCOUPLER	O PCS17VI	PC817X	
MODEL No.	C817	O PC817XI PC817XI1 PC817XI2 PC817XI3 PC817XI4	PC817XI PC817XI PC817XI PC817XI	17 18 19
	2017	PC817XI5	FCSITA	
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### 1. Application

This specification applies to the outline and characteristics of photocoupler Model No. PC817series.

#### 2. Outline

Refer to the attached drawing No. CY7073K02.

### 3. Ratings and characteristics

Refer to the attached sheet, page 3 to 6.

#### 4. Reliability

Refer to the attached sheet, page 7.

#### 5. Incoming inspection

Refer to the attached sheet, page 8.

### 6. Supplement

- 6.1 Isolation voltage shall be measured in the following method.
- (1) Short between anode to cathode on the primary side and between collector to emitter on the secondary side.
- (2) The dielectric withstand tester with zero-cross circuit shall be used.
- (3) The wave form of applied voltage shall be a sine wave.
  (It is recommended that the isolation voltage be measured in insulation oil.)

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# 6.2 Business dealing name

("O" mark indicates business dealing name of ordered product)

Ordered product	Business dealing name	Rank mark	Ic (mA)
0	PC817XI	A, B, C, D or no mark	2.5 to 30
-	PC817XI1	A	4.0 to 8.0
	PC817XI2	В.	6.5 to 13
	PC817XI3	С	10 to 20
	PC817XI4	D	15 to 30
	PC817XI5	A or B	4.0 to 13
	PC817XI6	B or C	6.5 to 20
	PC817XI7	C or D	10 to 30
	PC817XI8	A, B or C	4.0 to 20
	PC817XI9	B, C or D	6.5 to 30
	PC817XI0	A, B, C or D	4.0 to 30

Test	
conditions	
I <sub>F</sub> =5mA	
•	
$V_{CE}=5V$	
CL	
Ta=25℃	
14-200	

## 6.3 This Model is approved by UL.

Approved Model No.: PC817

UL file No.: E64380

## 6.4 This product is not designed against irradiation.

This product is assembled with electrical input and output.

This product incorporates non-coherent light emitting diode.

#### 7. Notes

Refer to the attached sheet-1-1, 2.

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# 3. Ratings and characteristics

### 3.1 Absolute maximum ratings

Ta=25℃

	Parameter	Symbol	Rating	Unit
	*1 Forward current	I <sub>F</sub>	50	mA
Innut	*2 Peak forward current	I <sub>FM</sub>	1	A
Input	Reverse voltage	V <sub>R</sub>	6 _	V
	*l Power dissipation	P	70	mW
	Collector-emitter voltage	V <sub>CEO</sub>	35	V
Output	Emitter-collector voltage	V <sub>ECO</sub>	6	v
	Collector current	Ic	50	mA
	*1 Collector power dissipation	Pc	150	mW
*1 Total power dissipation		Ptot	200	mW
	*3 Isolation voltage	Viso	5	kVrms
Operating temperature		Topr	-30 to +100	Ç
Storage temperature		Tstg	-55 to +125	J
	*4 Soldering temperature	Tsol	260	Ç

<sup>\*1</sup> The derating factors of absolute maximum ratings due to ambient temperature are shown in Fig. 1 to 4.

<sup>\*2</sup> Pulse width  $\leq$  100  $\mu$ s, Duty ratio : 0.001 (Refer to Fig. 5)

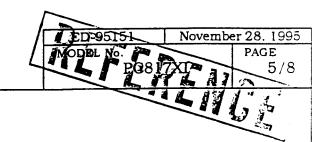
<sup>\*3</sup> AC for 1 min, 40 to 60%RH

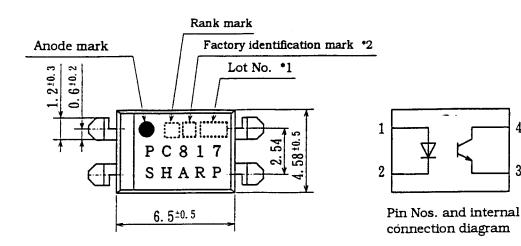
<sup>\*4</sup> For 10 s

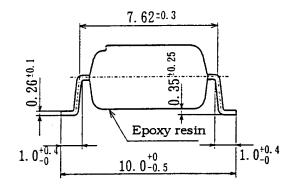
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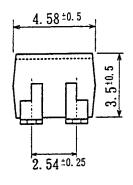
# 3.2 Electro-optical characteristics

	Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
	Forward voltage	$V_{\rm F}$	I <sub>F</sub> =20mA	-	1.2	1.4	V
Y	Peak forward voltage	$V_{\rm FM}$	I <sub>FM</sub> =0.5A	-	-	3.0	V
Input	Reverse current	I <sub>R</sub>	V <sub>R</sub> =4V	-	-	10	μΑ
	Terminal capacitance	Ct	V=0, f=1kHz	-	30	250	pF
	Dark current	I <sub>CEO</sub>	V <sub>CE</sub> =20V, I <sub>F</sub> =0	-	-	100	пA
Output	Collector-emitter breakdown voltage	BV <sub>CEO</sub>	Ic=0.1mA I <sub>F</sub> =0	35	•	•	V
	Emitter-collector breakdown voltage	BV <sub>ECO</sub>	$I_{E}=10 \mu A, I_{F}=0$	6	•	-	V
	Collector current	Ic	I <sub>F</sub> =5mA, V <sub>CE</sub> =5V	2.5	-	30	mA
	Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>F</sub> =20mA Ic=1mA	-	0.1	0.2	v
Transfer	Isolation resistance	R <sub>ISO</sub>	DC500V 40 to 60%RH	5×10 10	1011	-	Ω
charac- teristics	Floating capacitance	Cf	V=0, f=1MHz	-	0.6	1.0	pF
	Cut-off frequency	fc	$V_{CE}$ =5V, Ic=2mA R <sub>L</sub> =100 $\Omega$ , -3dB	<del>-</del>	80	-	kHz
	Rise time	tr	V <sub>CE</sub> =2V Ic=2mA	-	4	18	μs
	Fall time	tf	$R_L=100 \Omega$	-	3	18	μs



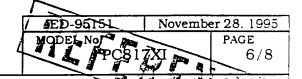




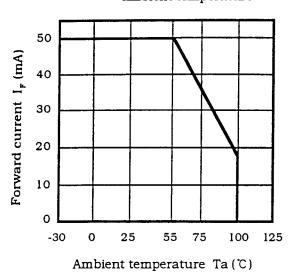


- \*1) 2-digit number shall be marked according to DIN standard.
- \*2) Factory identification mark shall be or shall not be marked.
- \*3) Marking is laser marking

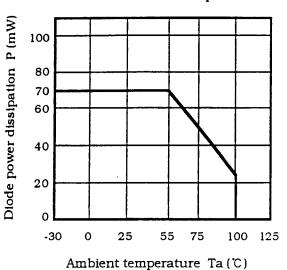
	UNIT: 1/1 mm
Name	PC817 Outline Dimensions (Business dealing name : PC817XI)
Drawing	



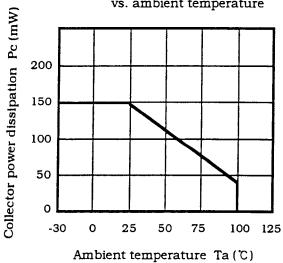
(Fig. 1) Forward current vs. ambient temperature



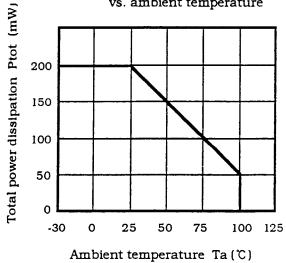
(Fig. 2) Diode power dissipation vs. ambient temperature



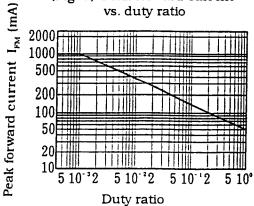
(Fig. 3) Collector power dissipation vs. ambient temperature



(Fig. 4) Total power dissipation vs. ambient temperature



(Fig. 5) Peak forward current vs. duty ratio



Pulse width  $\leq 100 \, \mu \, \text{s}$ Ta=25℃

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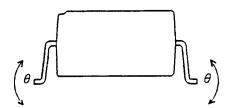
### 4. Reliability

The reliability of products shall be satisfied with items listed below.

Confidence level: 90% LTPD: 10%/20%

Test Items	Test Conditions	Failure Judgement Criteria	Samples (n) Defective(C)
Solderability *2	230°C, 5 s		n=11, C=0
Soldering heat	260°C, 10 s	<del>-</del> -	n=11, C=0
Terminal strength (Tension)	Weight: 5N 5 s/each terminal	: V <sub>F</sub> >U×1.2	n=11, C=0
Terminal strength (Bending) *3	Weight: 2.5N 2 times/each terminal	I <sub>R</sub> >U×2	n=11, C=0
Mechanical shock	$15000 \text{m/s}^2$ , 0.5ms $3 \text{ times}/\pm X$ , $\pm Y$ , $\pm Z$ direction	$I_{CEO} > U \times 2$ $I_{C} < L \times 0.7$	n=11, C=0
Variable frequency vibration	100 to 2000 to 100Hz/4min 200m/s <sup>2</sup> 4 times/ X, Y, Z direction	V <sub>CE(sat)</sub> >U×1.2	n=11, C=0
Temperature cycling	1 cycle -55°C to +125°C (30min) (30min) 20 cycles test		n=22,C=0
High temp. and high humidity storage	+60°C, 90%RH, 1000h	U : Upper specification limit	n=22,C=0
High temp. storage	+125°C, 1000h	L : Lower	n=22,C=0
Low temp. storage	-55℃, 1000h	specification limit	n=22,C=0
Operation life	I <sub>F</sub> =50mA, Ptot=200mW Ta=25℃, 1000h		n=22,C=0

- \*1 Test method, conforms to JIS C 7021.
- \*2 Solder shall adhere at the area of 95% or more of immersed portion of lead and pin hole or other holes shall not be concentrated on one portion.
- \*3 Terminal bending direction is shown below.



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## 5. Incoming inspection

- 5.1 Inspection items
- (1) Electrical characteristics

$$V_{F}$$
,  $I_{R}$ ,  $I_{CEO}$ ,  $V_{CE(sat)}$ , Ic,  $R_{ISO}$ , Viso

(2) Appearance

# 5.2 Sampling method and Inspection level

A single sampling plan, normal inspection level II based on ISO 2859 is applied. The AQL according to the inspection items are shown below.

Defect	Inspection item	AQL (%)
Major defect	Electrical characteristics Unreadable marking	0.1
Minor defect	Appearance defect except the above mentioned.	0.4

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sheet-1-1

## Precautions for Photocouplers

1 For cleaning

(1) Solvent cleaning: Solvent temperature 45°C or less Immersion for 3 min or less

(2) Ultrasonic cleaning: The affect to device by ultrasonic cleaning is different

by cleaning bath size, ultrasonic power

output, cleaning time, PWB size or device mounting condition etc. Please test it in actual using condition and confirm that doesn't occur any defect before starting

the ultrasonic cleaning.

Applicable solvent : Ethyl alcohol, Methyl alcohol

Freon TE · TF, Diflon-solvent S3-E

Please refrain form using Chloro Fluoro Carbon type solvent to clean device as much as possible since it is internationally restricted to protect the ozonosphere. Before you use alternative solvent you are requested to confirm that it does not attack package resin.

2. The LED used in the Photocoupler generally decreases the light emission power by operation. In case of long operation time, please design the circuit with considering the degradation of the light emission power of the LED. (50%/5years)

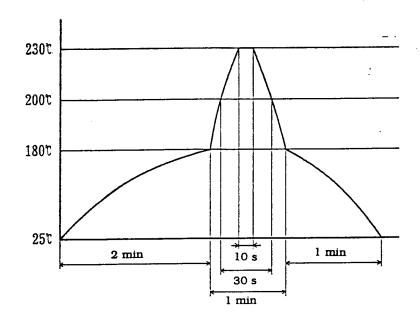
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sheet-1-2

3. Precaution for Soldering Photocoupler

### (1) If solder reflow:

It is recommended that only one soldering be done at the temperature and the time within the temperature profile as shown in the figure.



### (2) Other precautions

An infrared lamp used to heat up for soldering may cause a localized temperature rise in the resin. So keep the package temperature within that specified in Item (1). Also avoid immersing the resin part in the solder.

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