SLCS015A - DECEMBER 1988 - REVISED AUGUST 2003

- Operates From a 5-V Supply
- Self-Biasing Inputs
- Hysteresis . . . 10 mV Typ
- Response Time . . . 6 ns Typ
- Maximum Operating Frequency
  ... 50 MHz Typ

#### 

NC - No internal connection

## description/ordering information

The TL714C is a high-speed differential comparator fabricated with bipolar Schottky process technology. The circuit has differential inputs and a TTL-compatible logic output with symmetrical switching characteristics.

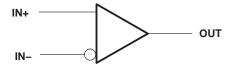
The device operates from a single 5-V supply and is useful as a disk-memory read-chain data comparator.

### ORDERING INFORMATION

TA	PACKAGE <sup>†</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	PDIP (P)	Tube of 50	TL714CP	TL714CP
	COIC (D)	Tube of 75	TL714CD	TI 74.40
	SOIC (D)	Reel of 2500	TL714CDR	TL714C

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

## symbol

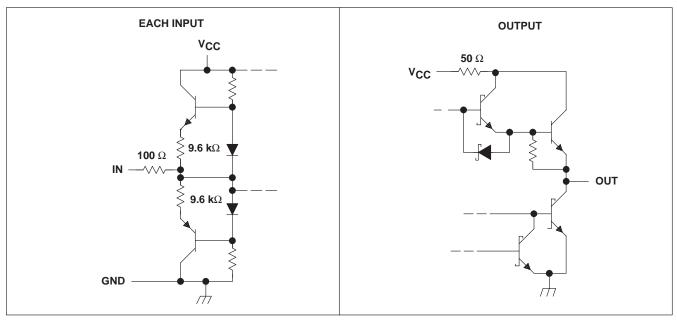




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## schematic of inputs and outputs



All resistor values shown are nominal.

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V <sub>CC</sub> (see Note 1)	7 V
Differential input voltage, V <sub>ID</sub> (see Note 2)	
Input voltage range, V <sub>I</sub>	$\dots$ V <sub>CC</sub> to GND
Low-level output current, I <sub>OL</sub>	40 mA
Package thermal impedance, θ <sub>JA</sub> (see Notes 3 and 4): D package	97°C/W
P package	85°C/W
Operating virtual junction temperature, T <sub>J</sub>	150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C
Storage temperature range, T <sub>stg</sub>	$-65^{\circ}$ C to $150^{\circ}$ C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltage, are with respect to the network ground.
  - 2. Differential voltage values are at IN+ with respect to IN-.
  - 3. Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
  - 4. The package thermal impedance is calculated in accordance with JESD 51-7.

## recommended operating conditions

		MIN	MAX	UNIT
VCC	Supply voltage	4.75	5.25	V
.,		1.4		.,
VIC	Common-mode input voltage	to V <sub>CC</sub> -1.4		V
loh	High-level output current		-1	mA
loL	Low-level output current		16	mA
$T_A$	Operating free-air temperature	0	70	°C



# electrical characteristics over free-air operating temperature range, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP <sup>†</sup>	MAX	UNIT
VT	Threshold voltage (V <sub>T+</sub> and V <sub>T-</sub> )	V <sub>IC</sub> = 1.4 V to 3.6 V		-75 <sup>‡</sup>		75	mV
V <sub>hys</sub>	Hysteresis (V <sub>T+</sub> – V <sub>T-</sub> )			2	10	30	mV
Vон	High-level output voltage	$V_{ID} = 100 \text{ mV},$	$I_{OH} = -1 \text{ mA}$	2.7	3.4		V
VOL	Low-level output voltage	$V_{ID} = -100 \text{ mV},$	$I_{OL} = 16 \text{ mA}$		0.4	0.5	V
los	Short-circuit output current			-30		-110	mA
rį	Differential input resistance			2.9			kΩ
ICC	Supply current	$V_{ID} = -100 \text{ mV},$	I <sub>O</sub> = 0		7	12	mA

<sup>&</sup>lt;sup>†</sup> All typical values are at  $T_A = 25$ °C.

## switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$

PARAMETER		TEST CONDITIONS		MIN	TYP <sup>†</sup>	MAX	UNIT
f <sub>max</sub>	Maximum operating frequency	$V_{ID} = \pm 250 \text{ mV},$ $C_L = 25 \text{ pF},$	$t_r = t_f = 4 \text{ ns},$ Input duty cycle = 50%		50		MHz
<sup>t</sup> PLH	Propagation delay time, low-to-high-level output	$V_{ID} = \pm 100 \text{ mV},$	C <sub>L</sub> = 25 pF,		6	12	ns
tPHL	Propagation delay time, high-to-low-level output	See Figures 1 and 2			6	12	ns
t <sub>r</sub>	Rise time	$V_{ID} = \pm 100 \text{ mV},$ See Figure 3	C <sub>L</sub> = 25 pF,		4	8	ns
tf	Fall time				4	8	ns

 $<sup>\</sup>overline{\dagger}$  All typical values are at  $T_A = 25^{\circ}$ C.

## PARAMETER MEASUREMENT INFORMATION

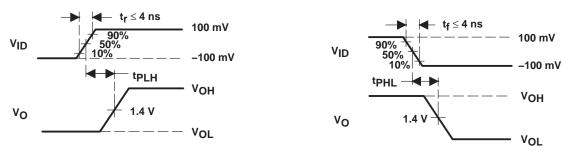


Figure 1. Propagation Delay Time, Low to High (t<sub>PLH</sub>)

Figure 2. Propagation Delay Time, High to Low (t<sub>PHL</sub>)

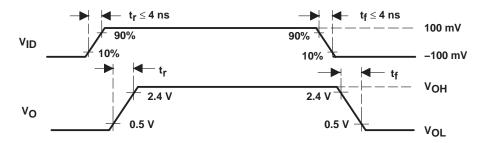


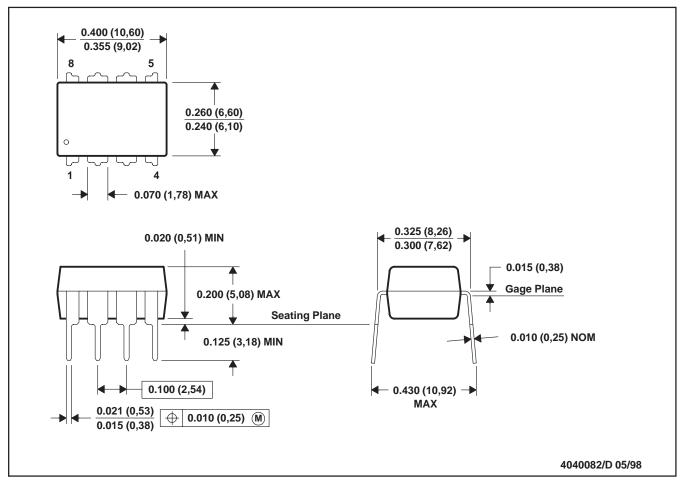
Figure 3. Rise and Fall Times (t<sub>r</sub>, t<sub>f</sub>)



<sup>&</sup>lt;sup>‡</sup> The algebraic convention, where the more-negative limit is designated as minimum, is used in this data sheet for input threshold voltage levels only.

## P (R-PDIP-T8)

## PLASTIC DUAL-IN-LINE



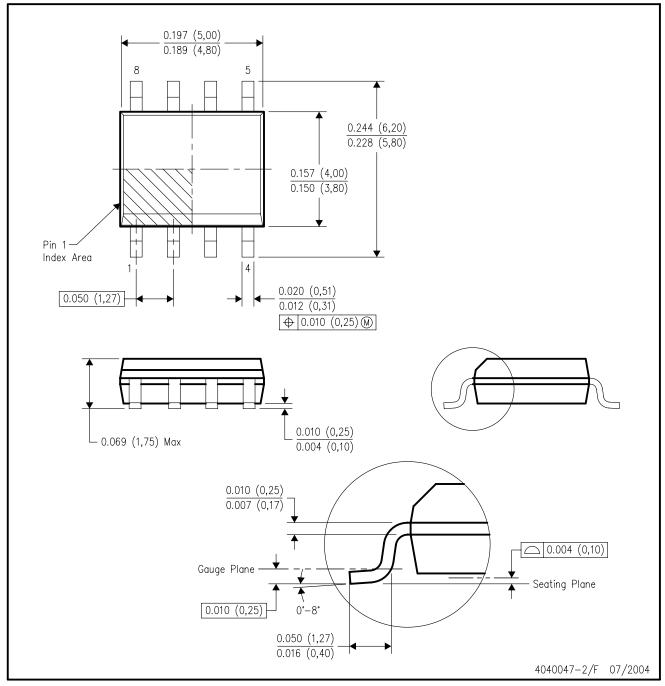
NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

For the latest package information, go to  $http://www.ti.com/sc/docs/package/pkg\_info.htm$ 

## D (R-PDSO-G8)

## PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MS-012 variation AA.



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