

4	3	2	DWG. NO. C0979B01	SH. 1	REV. -	1
D	REVISIONS					
	REV.	REMARKS	DRAWN	DATE	APPROVED	
C	TABLE OF CONTENTS					
	SHEET	SECTION				
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	10	FPGA I/O				
11	FPGA POWER & CONFIGURATION					
12	VOLTAGE REGULATORS, FUSES & SCAN TRIGGER INPUT					
B	NOTES:					
	1. ALL RESISTORS IN OHMS, 1/8W, 1% UNLESS OTHERWISE SPECIFIED.					
	2. ALL CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.					
	3. FOR FABRICATION DETAILS OF 32 CHANNEL ADC MODULE SEE JEFFERSON LAB DRAWING C0979C02.					
	4. FOR ARTWORK OF 32 CHANNEL ADC MODULE SEE JEFFERSON LAB DRAWING C0979C03.					
	5. FOR ASSEMBLY OF 32 CHANNEL ADC MODULE SEE JEFFERSON LAB DRAWING C0979C04.					
	A	DRAWN EESIC		DATE 11/9/07		Jefferson Lab Thomas Jefferson National Accelerator Facility NEWPORT NEWS, VIRGINIA UNITED STATES DEPARTMENT OF ENERGY
CHECKED		DATE				
APPR/ORIGINATOR		DATE				
APPROVED				TITLE CONTROLS 32 CHANNEL ADC MODULE SCHEMATIC		
ENGINEER C. SEATON		SIZE B		CAD I.D. NO. //EECAD... C0979S1 Rev -.sch		
		DRAWING NO. C0979B01		REV. -		
		SCALE NONE		SHOWN ON		
				SHEET 1 OF 12		

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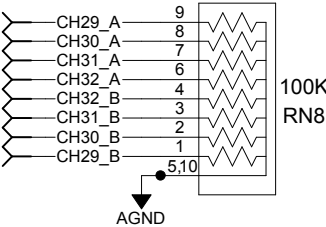
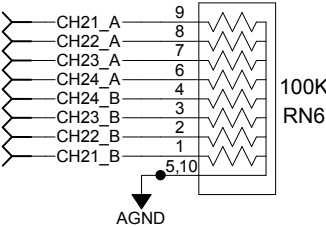
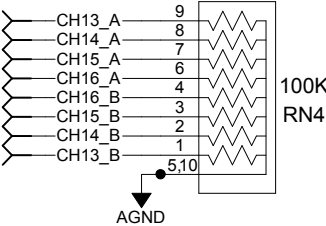
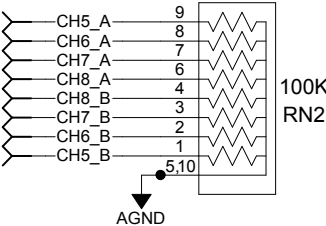
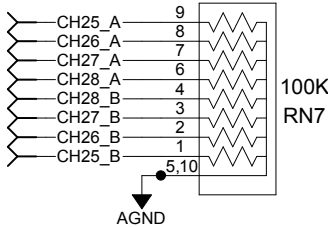
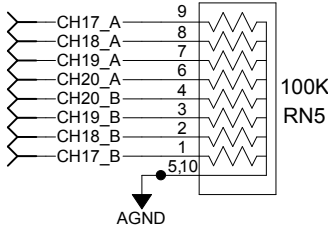
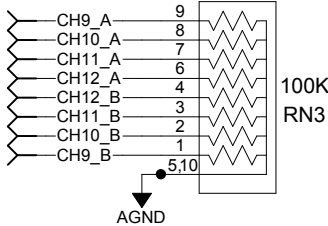
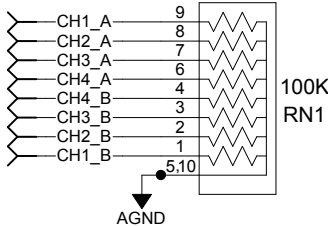
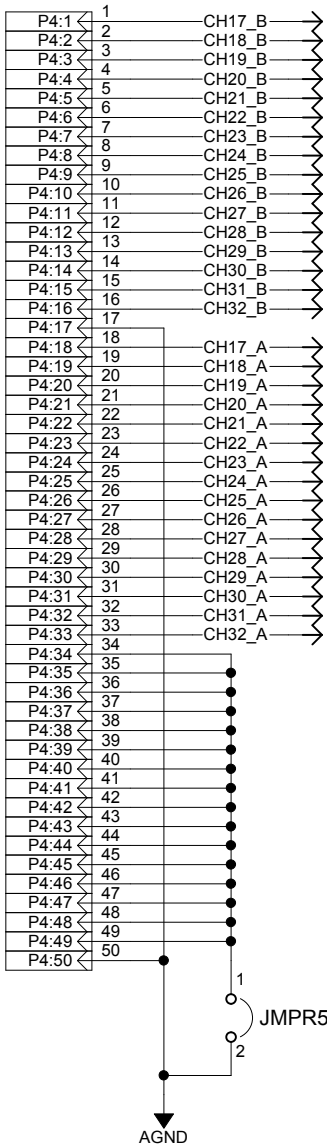
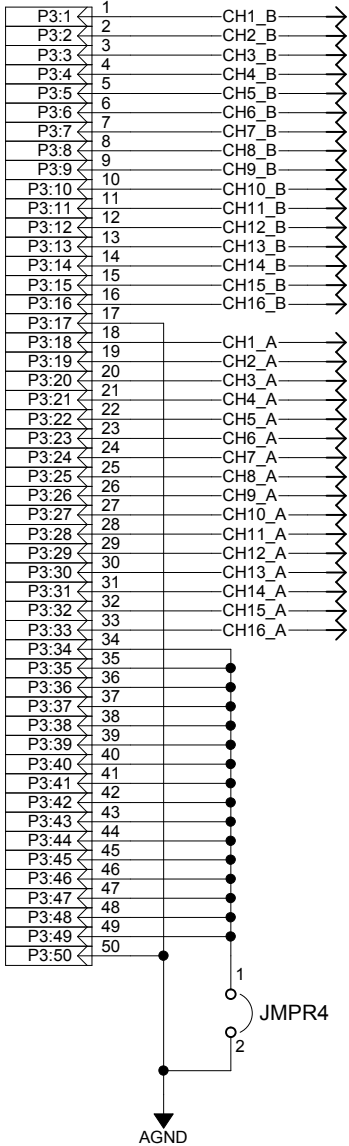
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ANALOG INPUT CONNECTORS

TITLE			
CONTROLS			
32 CHANNEL ADC MODULE			
SCHEMATIC			
SIZE	CAD I.D. NO.	DRAWING NO.	REV.
B	//EECAD... C0979S1 Rev -.sch	C0979B01	-
SCALE	SHOWN ON		SHEET 2 OF 12
NONE			

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C

B

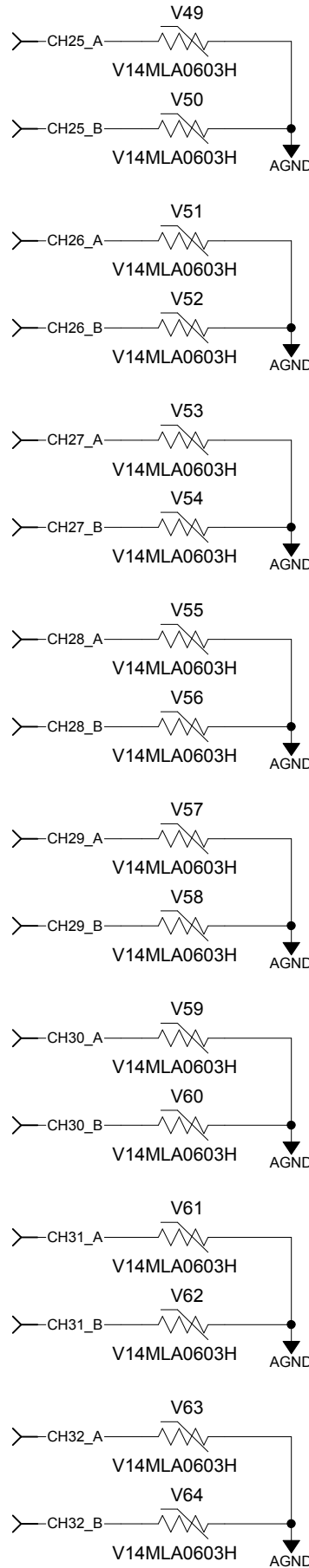
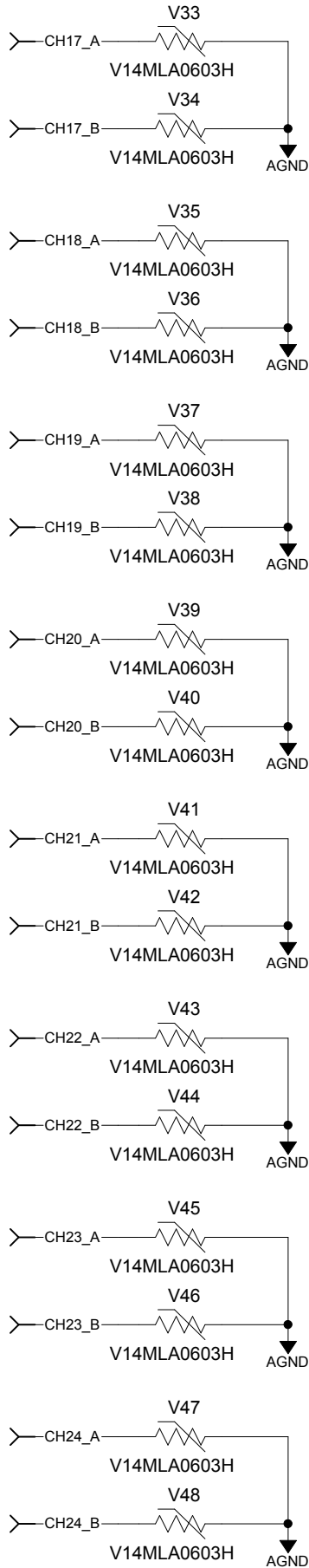
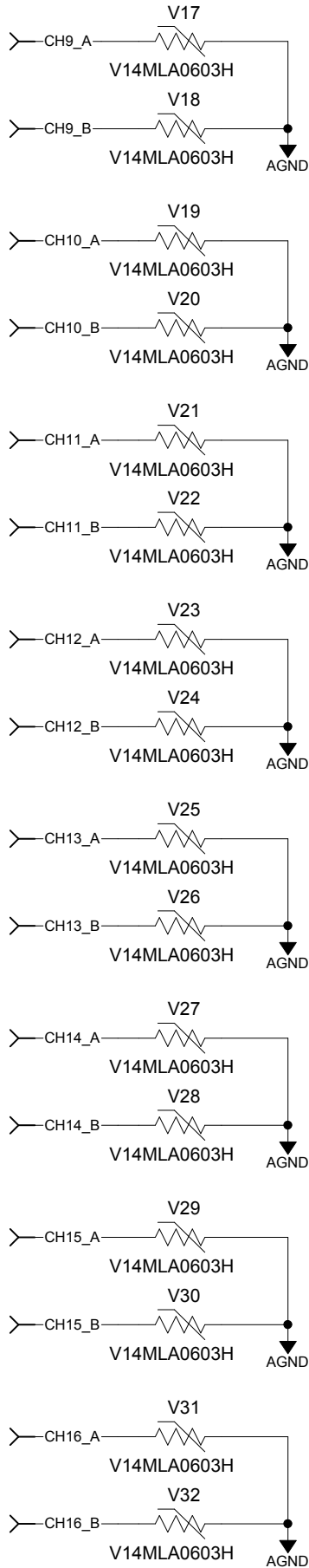
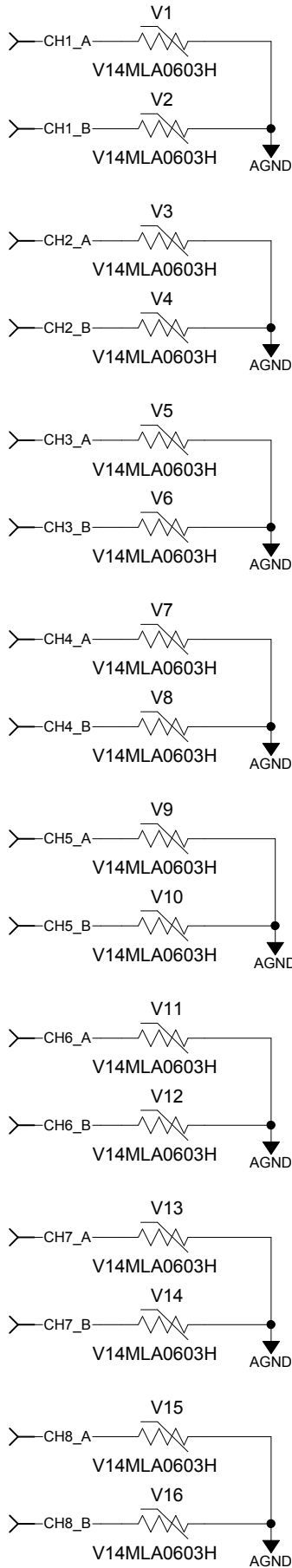
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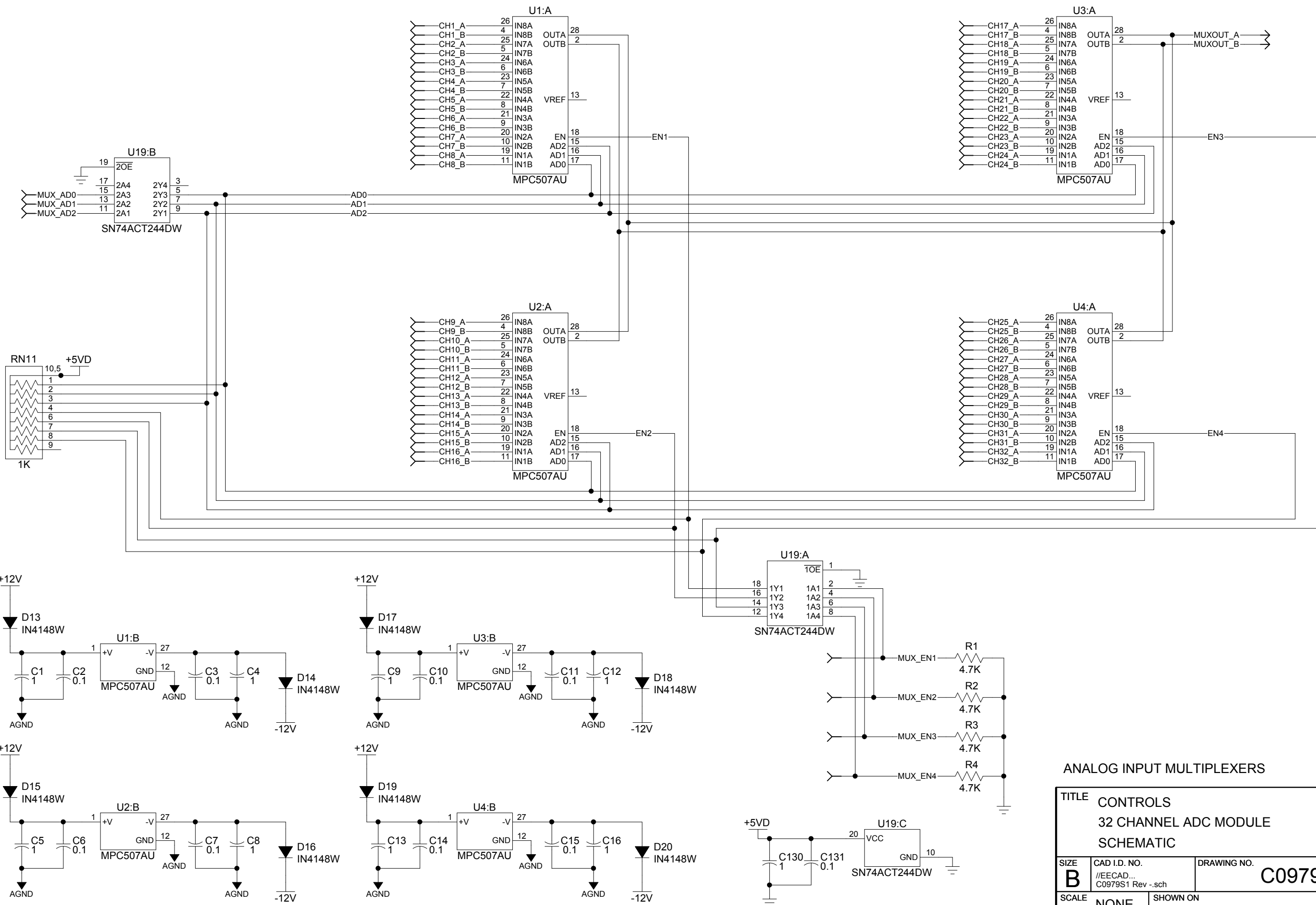
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INPUT PROTECTION

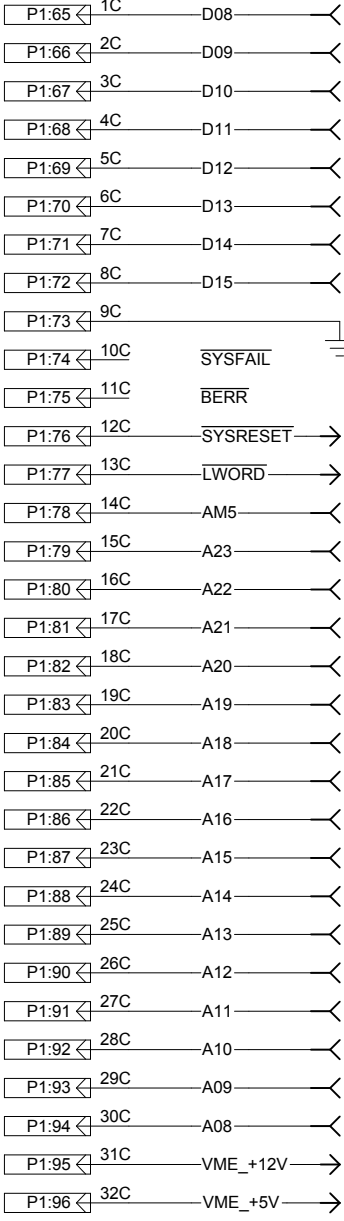
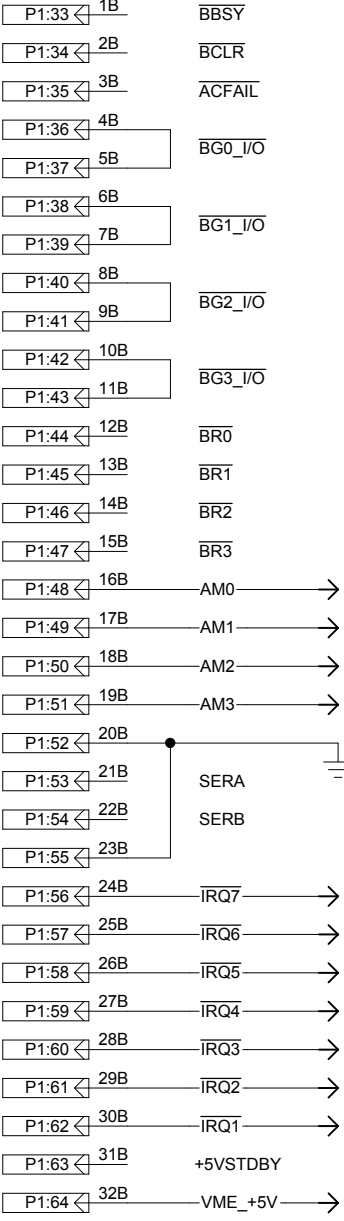
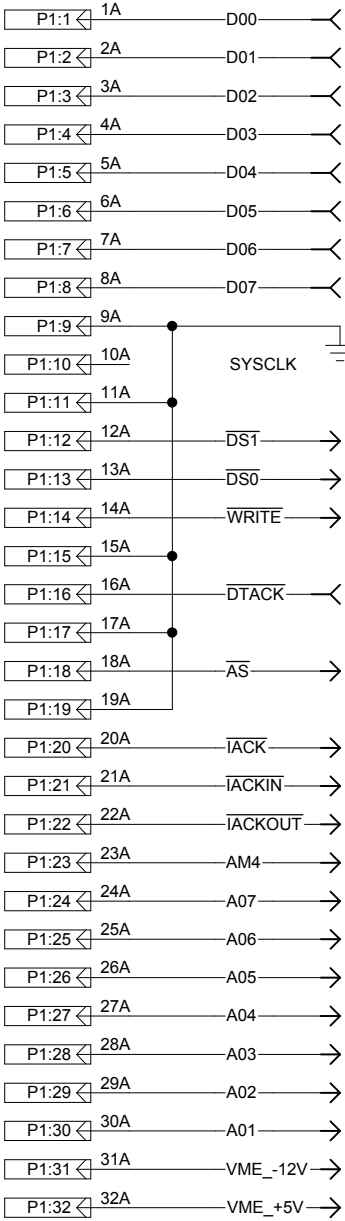
TITLE CONTROLS 32 CHANNEL ADC MODULE SCHEMATIC			
SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON _____	SHEET 3 OF 12	



ANALOG INPUT MULTIPLEXERS

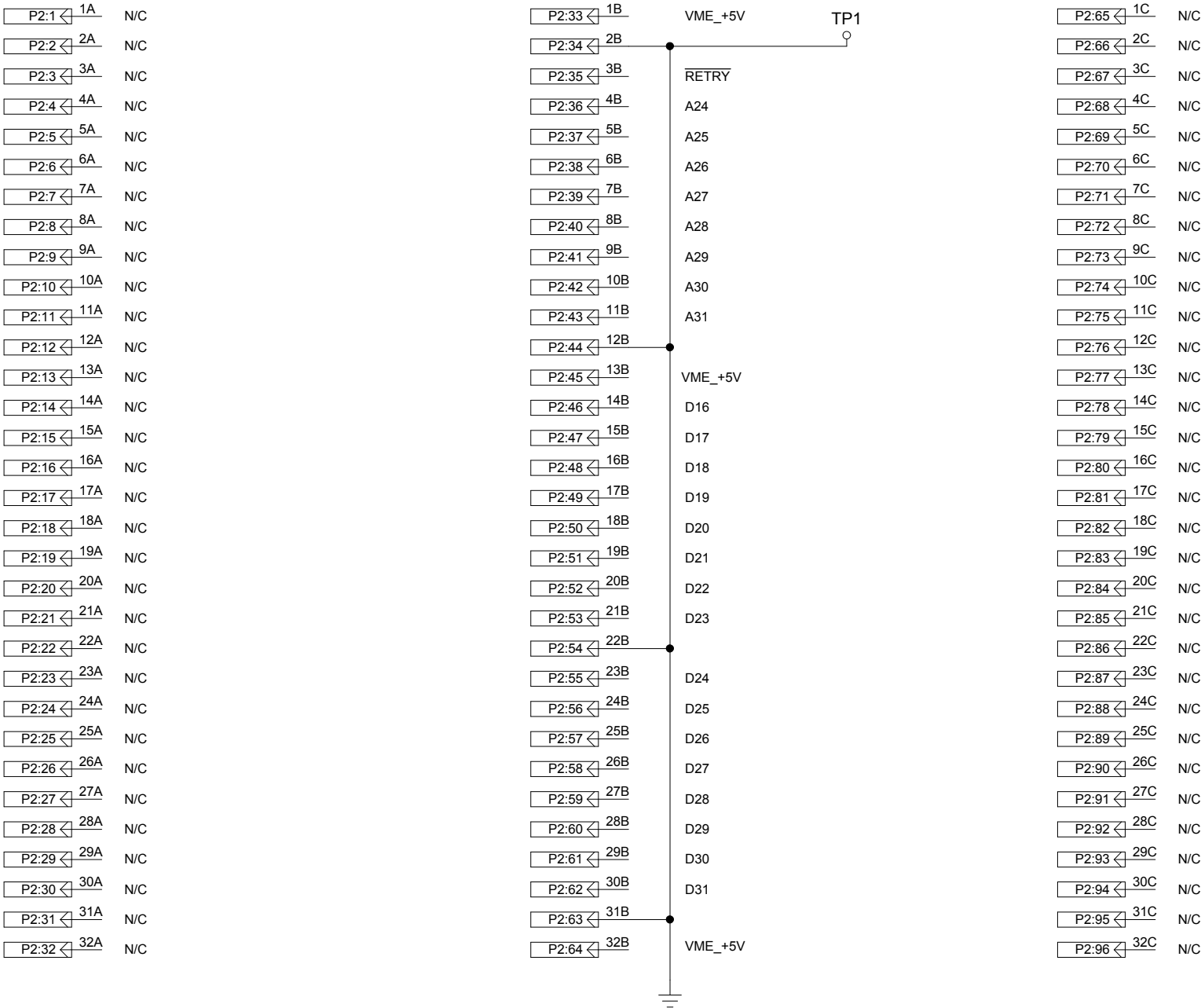
TITLE
CONTROLS
32 CHANNEL ADC MODULE
SCHEMATIC

SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON	SHEET 4	OF 12



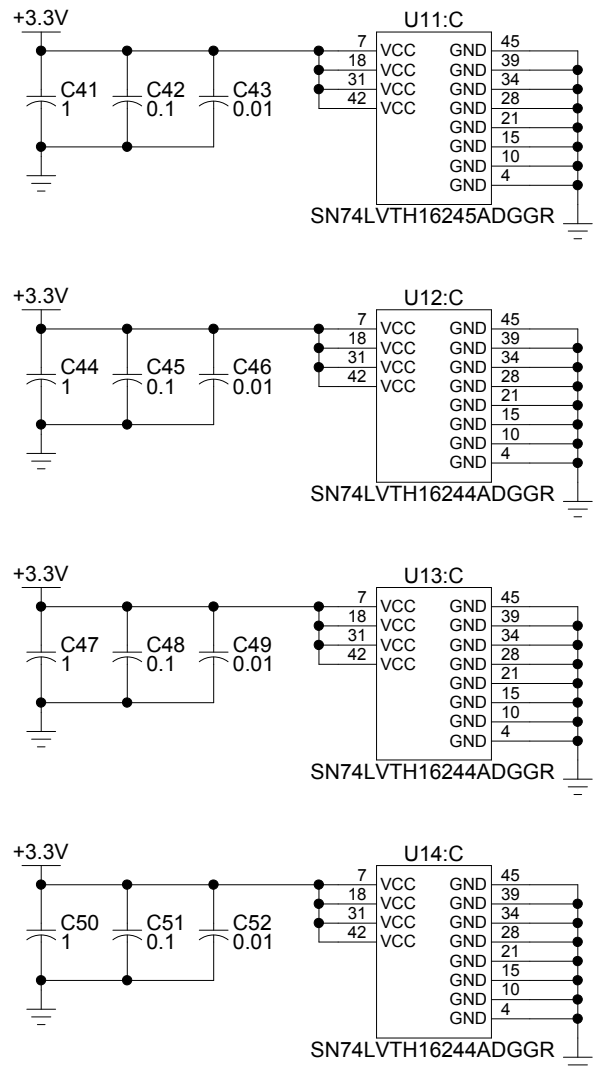
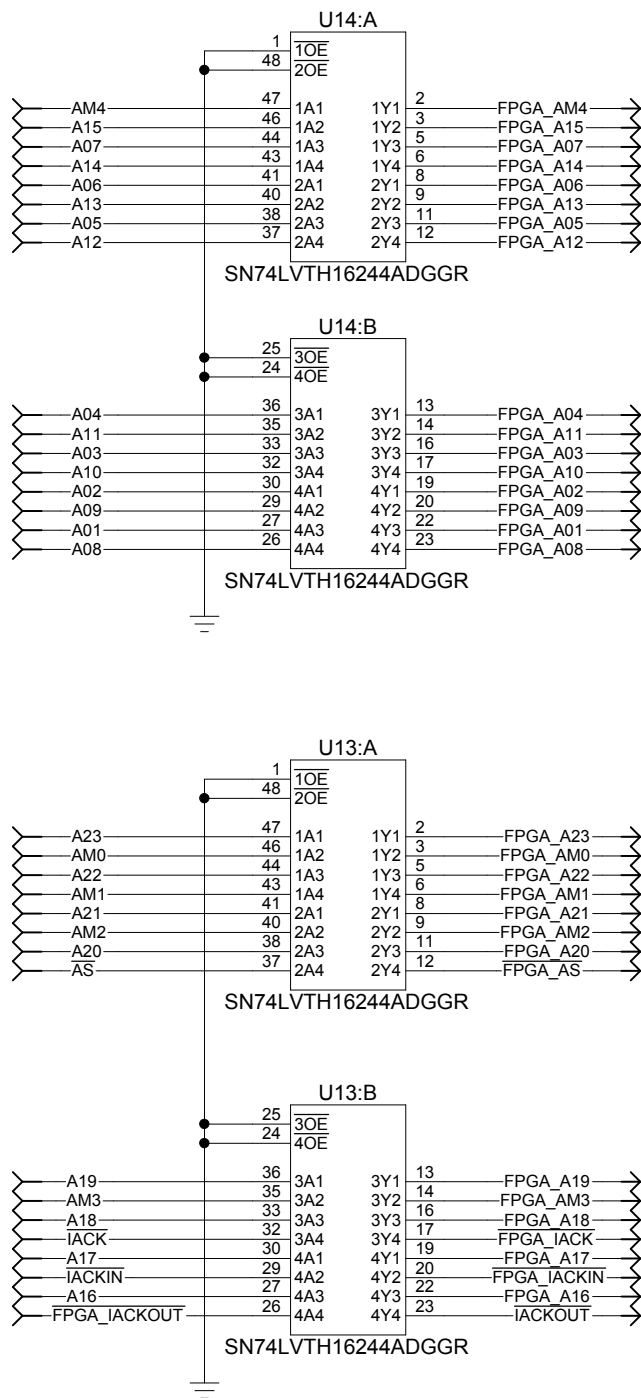
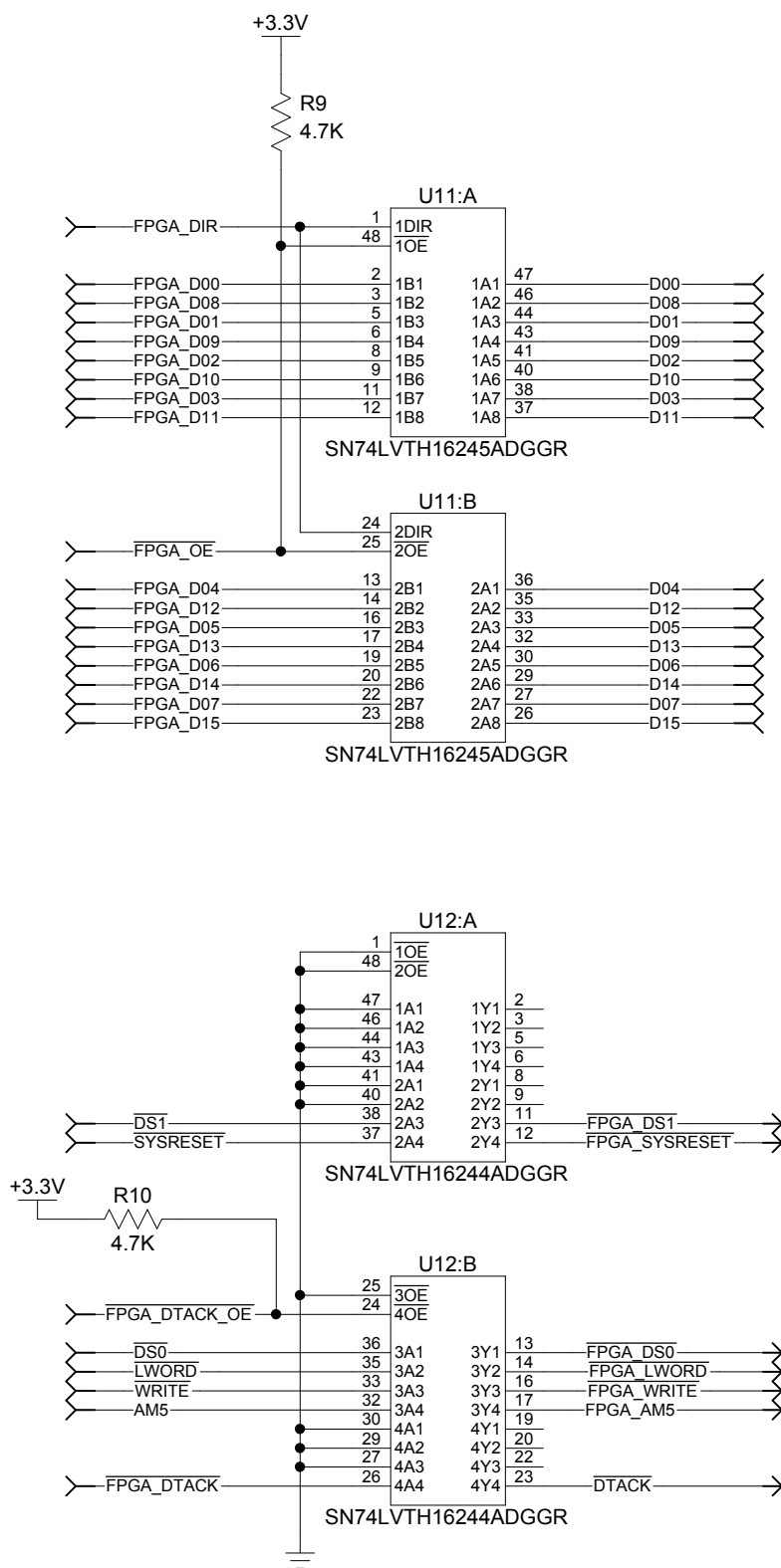
VME P1 CONNECTOR

TITLE CONTROLS 32 CHANNEL ADC MODULE SCHEMATIC			
SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON _____	SHEET 6 OF 12	



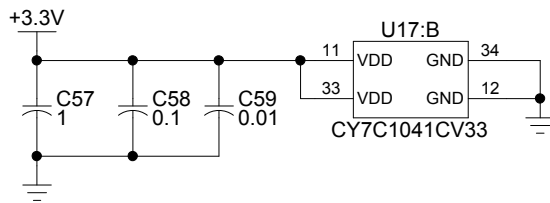
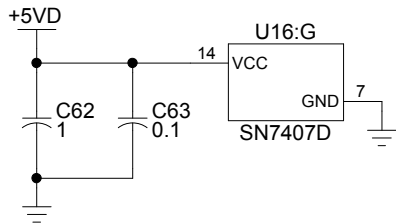
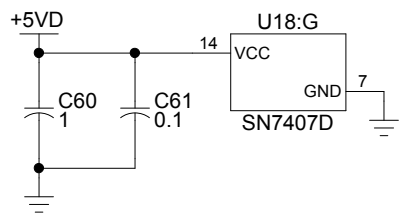
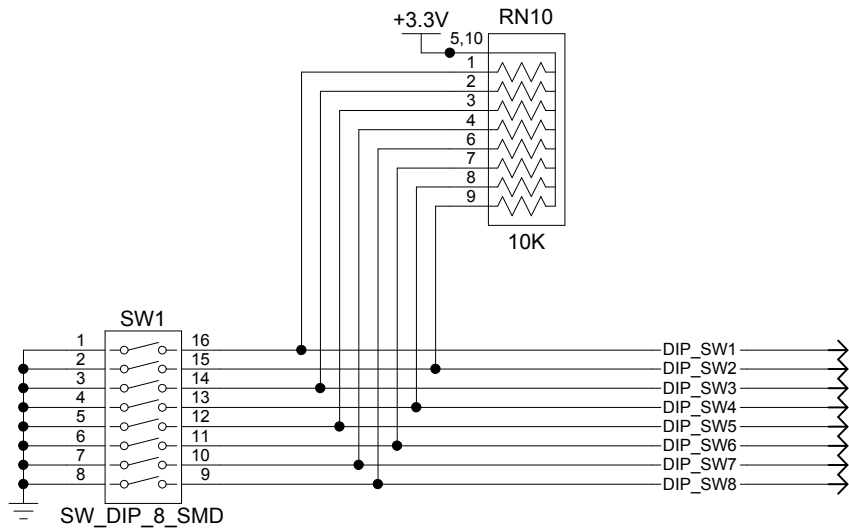
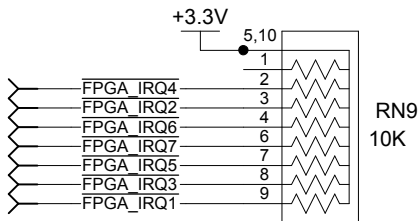
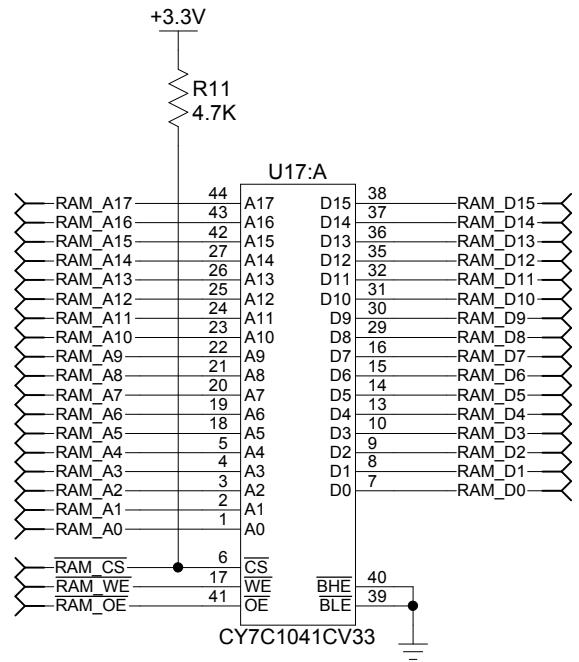
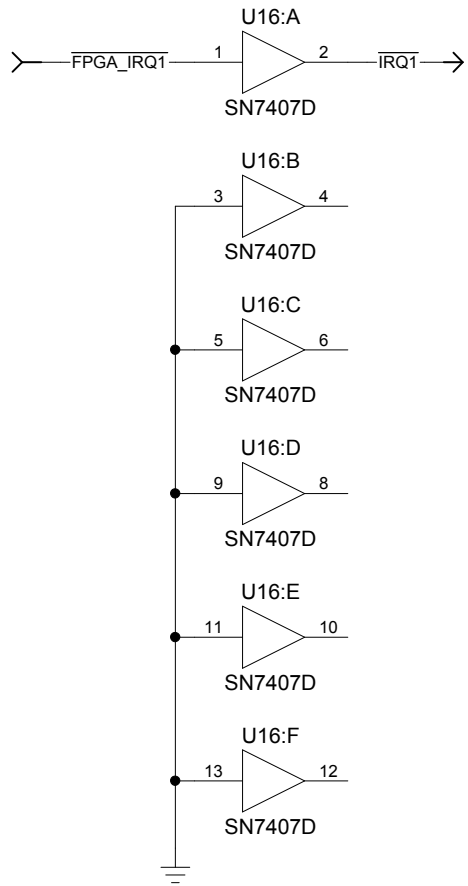
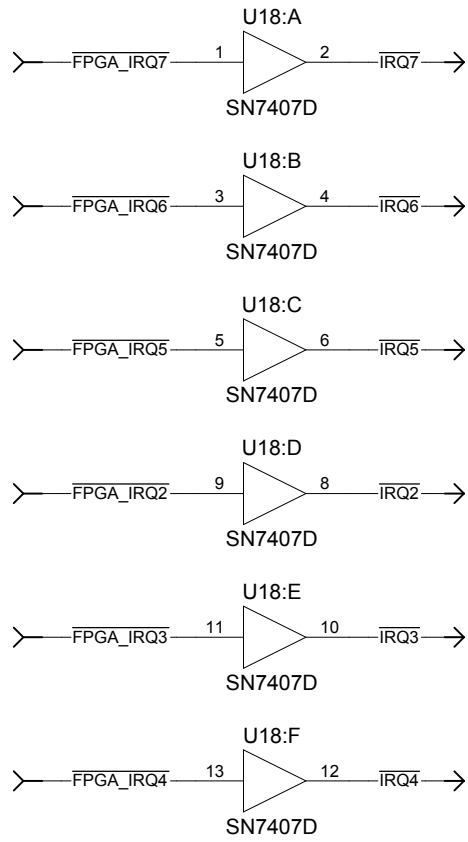
VME P2 CONNECTOR

TITLE				CONTROLS	
				32 CHANNEL ADC MODULE	
				SCHEMATIC	
SIZE	CAD I.D. NO.		DRAWING NO.		REV.
B	//EECAD... C0979S1 Rev -.sch		C0979B01		-
SCALE	NONE		SHOWN ON		SHEET 7 OF 12



VME INTERFACE BUFFERS

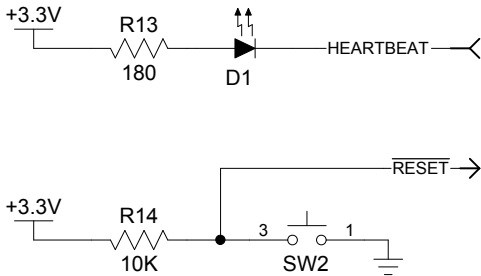
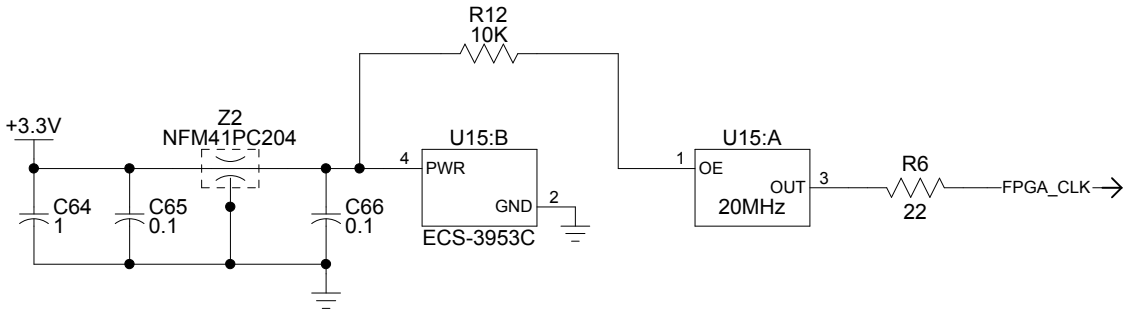
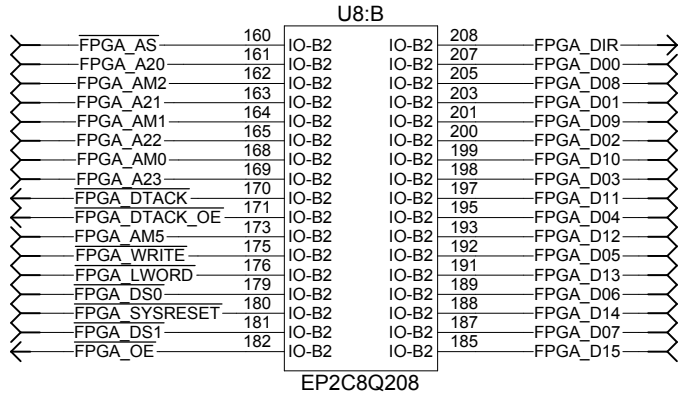
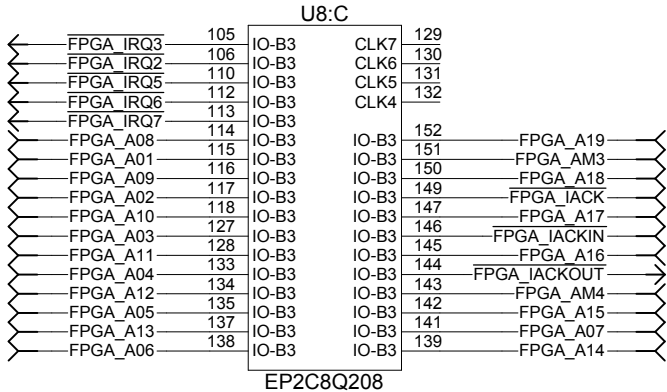
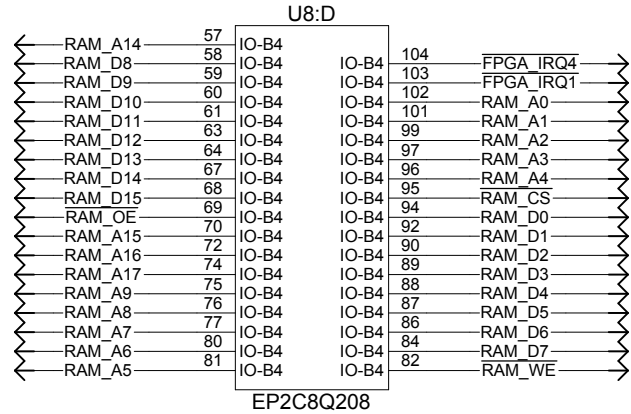
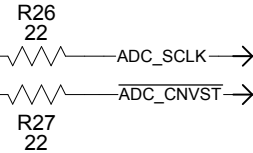
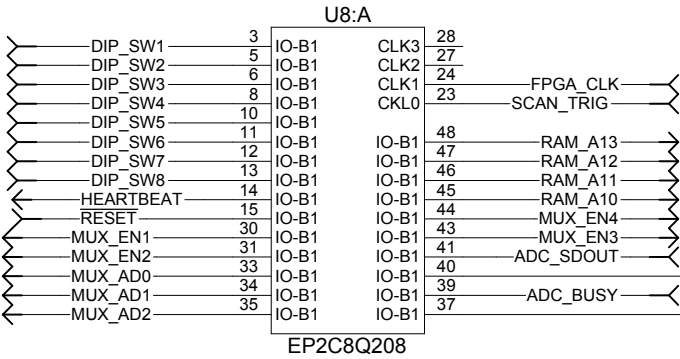
TITLE CONTROLS 32 CHANNEL ADC MODULE SCHEMATIC			
SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON _____	SHEET 8 OF 12	



VME IRQ BUFFERS, RAM & DIP SWITCH

TITLE
CONTROLS
32 CHANNEL ADC MODULE
SCHEMATIC

SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON	SHEET 9 OF 12	



FPGA I/O

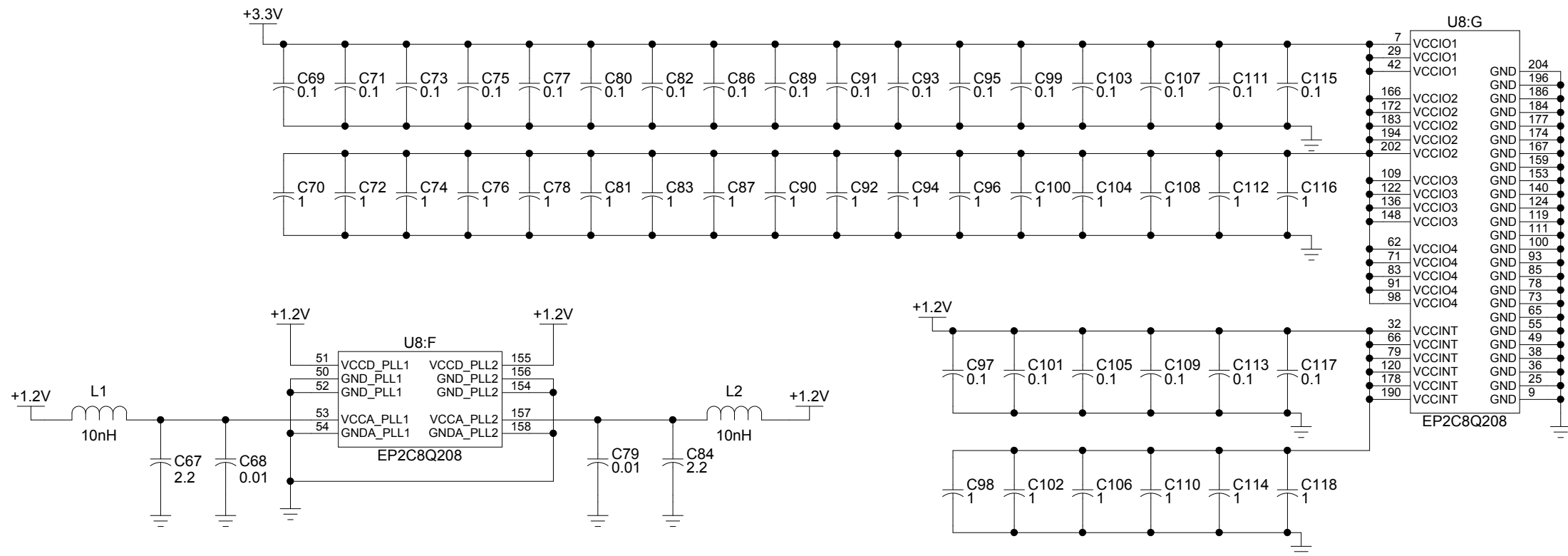
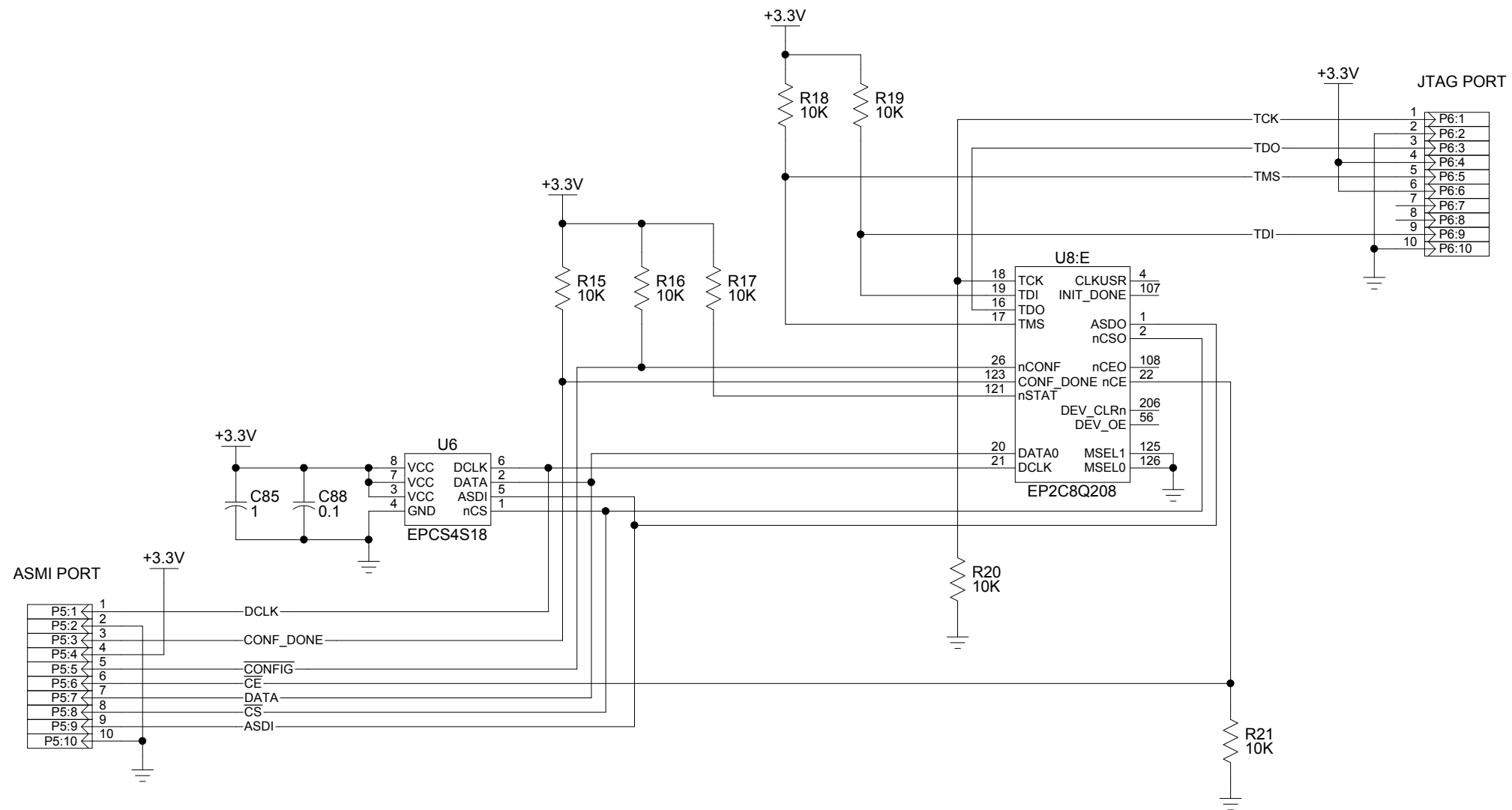
TITLE

CONTROLS

32 CHANNEL ADC MODULE

SCHEMATIC

SIZE	CAD I.D. NO.	DRAWING NO.	REV.
B	//EECAD... C0979S1 Rev -.sch	C0979B01	-
SCALE	SHOWN ON	SHEET 10 OF 12	
NONE			



FPGA POWER & CONFIGURATION

TITLE
CONTROLS
32 CHANNEL ADC MODULE
SCHEMATIC

SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON _____	SHEET 11 OF 12	

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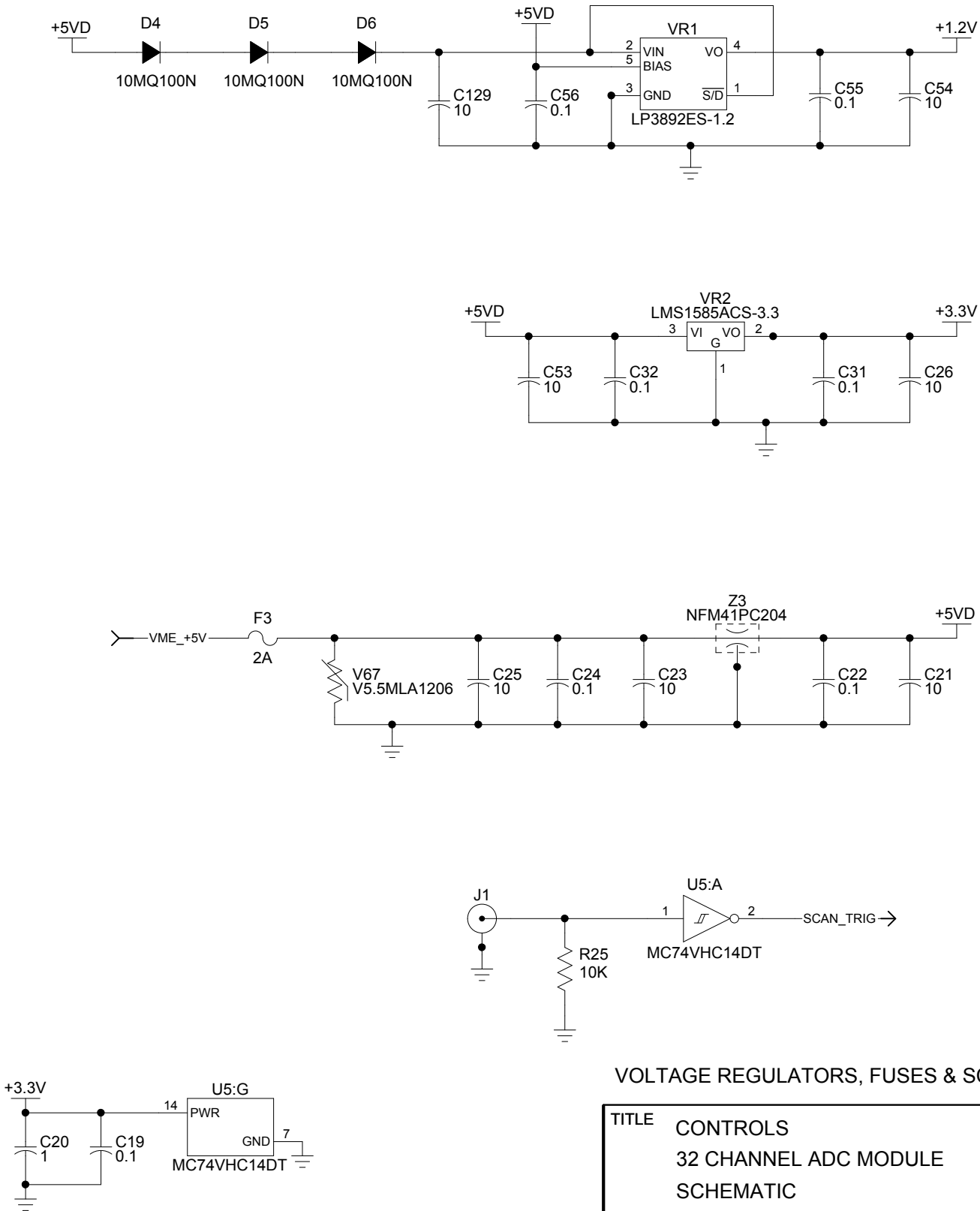
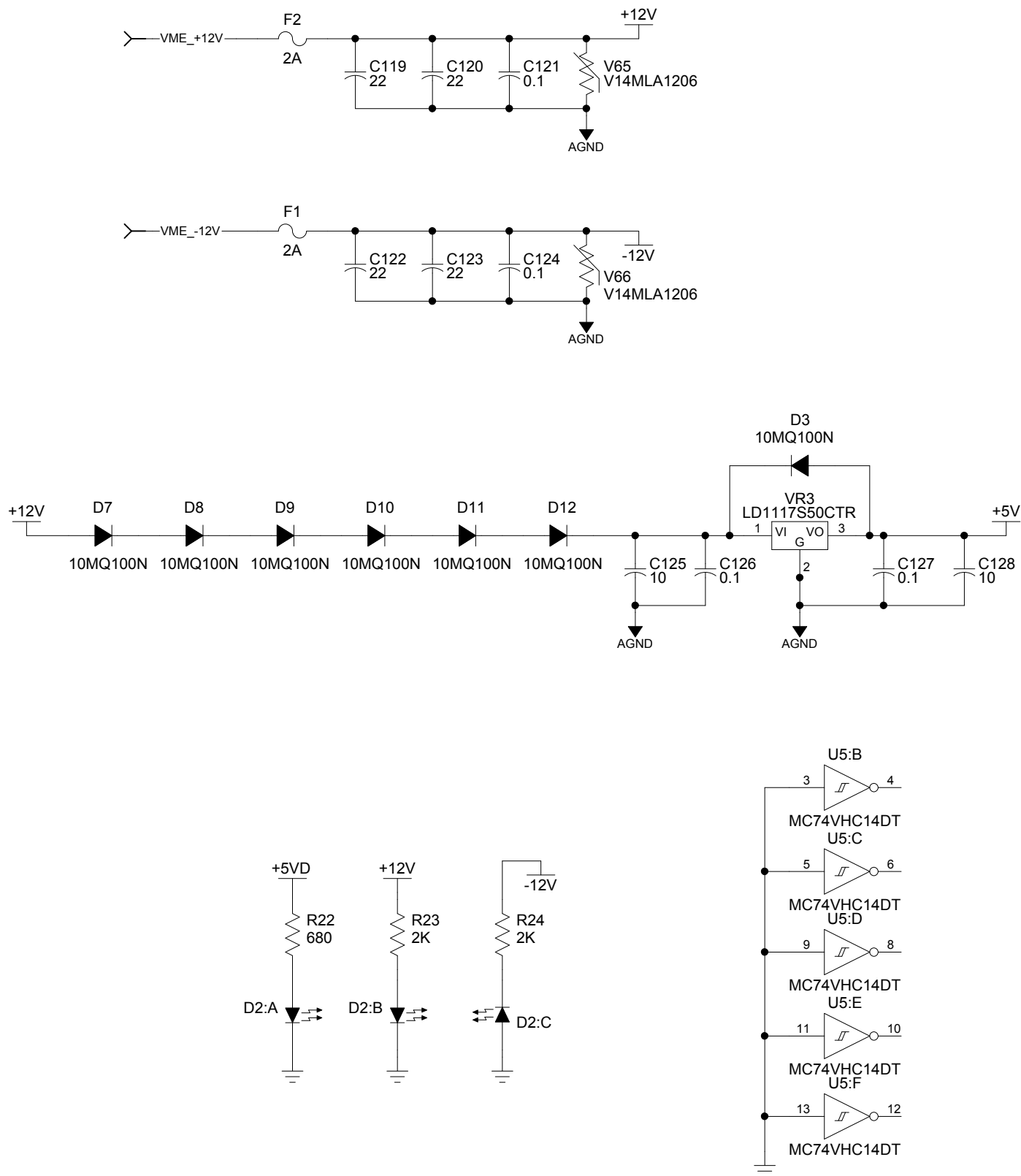
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VOLTAGE REGULATORS, FUSES & SCAN TRIGGER INPUT

TITLE CONTROLS 32 CHANNEL ADC MODULE SCHEMATIC			
SIZE B	CAD I.D. NO. //EECAD... C0979S1 Rev -.sch	DRAWING NO. C0979B01	REV. -
SCALE NONE	SHOWN ON _____	SHEET 12 OF 12	