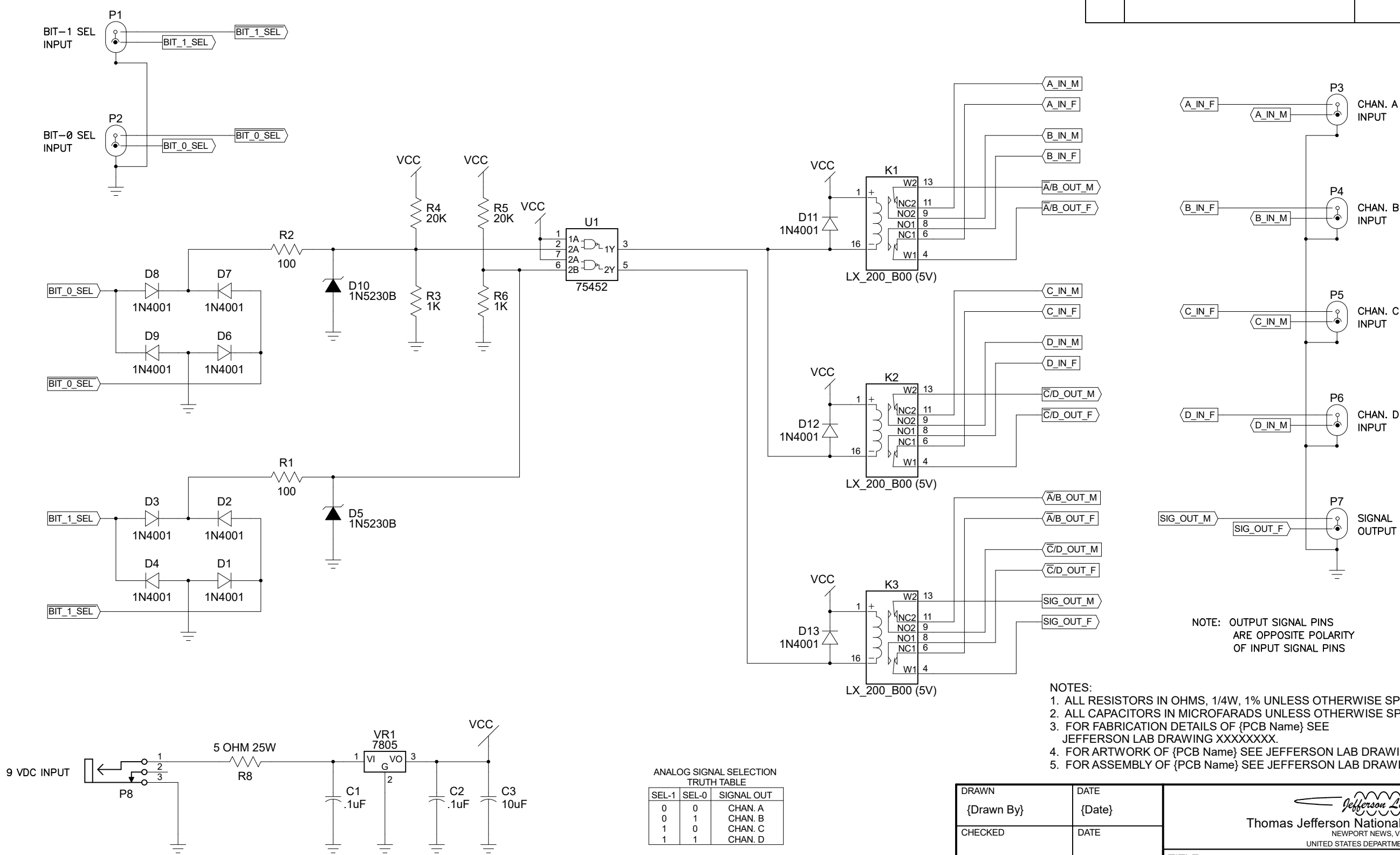


REVISIONS				
REV.	REMARKS	DRAWN	DATE	APPROVED



- NOTES:
1. ALL RESISTORS IN OHMS, 1/4W, 1% UNLESS OTHERWISE SPECIFIED.
 2. ALL CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
 3. FOR FABRICATION DETAILS OF {PCB Name} SEE JEFFERSON LAB DRAWING XXXXXXXX.
 4. FOR ARTWORK OF {PCB Name} SEE JEFFERSON LAB DRAWING XXXXXXXX.
 5. FOR ASSEMBLY OF {PCB Name} SEE JEFFERSON LAB DRAWING XXXXXXXX.

ANALOG SIGNAL SELECTION TRUTH TABLE		
SEL-1	SEL-0	SIGNAL OUT
0	0	CHAN. A
0	1	CHAN. B
1	0	CHAN. C
1	1	CHAN. D

DRAWN {Drawn By}	DATE {Date}	 Thomas Jefferson National Accelerator Facility NEWPORT NEWS, VIRGINIA UNITED STATES DEPARTMENT OF ENERGY		
CHECKED	DATE			
APPR/ORIGINATOR	DATE			
APPROVED		TITLE MAJOR SYSTEM SUB-SYSTEM {Title} SCHEMATIC		
ENGINEER {Engineer}		SIZE B	CAD I.D. NO. //EECAD... Fast_feedback.sch	DRAWING NO.
		SCALE NONE	SHOWN ON DRAWING NUMBER	SHEET 1 OF 1

{Drawing Number} {Revision}