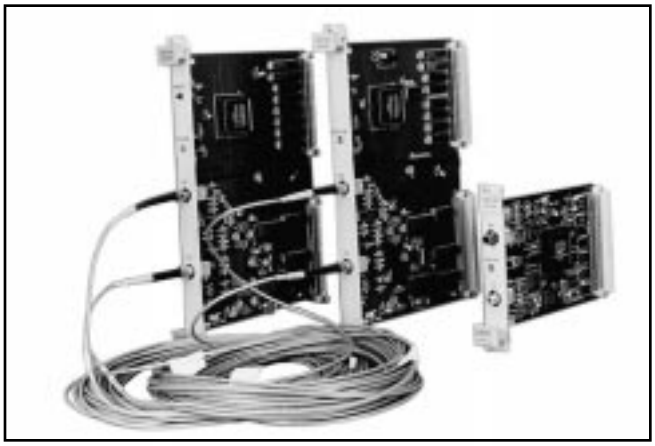
VMIVME-5531L
VMEbus Fiber-Optic Repeater Link

- High-performance, easy-to-use method of linking two or more VMEbus systems together via fiber-optic cable
- Software transparent allows direct communication from primary chassis to secondary chassis with no software overhead (unidirectional link control with bidirectional data transfers)
- Plug-and-play operation
- Supports 8-, 16-, and 32-bit data transfers (6U slave)
- Supports 16-, 24-, and 32-bit addressing (6U slave)
- Supports 8-, 16-bit data, 16-, 24-bit address (3U slave)
- Total electrical isolation between VMEbus systems
- Maximum 6,560 ft (2,000 m) fiber-optic cables
- Advantages over standard repeaters
  - Small cables
  - High-noise immunity
  - High-voltage isolation
  - No EMI generated by cables
- The VME-to-VME link consists of two boards and two fiber-optic cables (master board: 6U, slave board: 6U or 3U)



OVERVIEW — The VMIVME-5531 Link is a high-performance, yet easy-to-use method of linking two or more VMEbus systems together via fiber-optic cable. The Repeater Link is a two-board set which allows VMEbus slave I/O boards residing in one VMEbus chassis to be controlled by a VMEbus master residing in another chassis. The VMEbus chassis in which VMEbus masters reside is referred to as a primary chassis, while the VMEbus slave boards reside in a secondary chassis. The two-board set is configured as shown in Figure 1 with one board designated for the primary chassis while the other board is designated for the secondary chassis. A master VMEbus chassis can communicate with several secondary chassis by using multiple REPEATER Links in a star configuration as shown in Figure 2.

The link is software transparent. Any VMEbus master in the primary chassis may access (read or write) to any slave board in the secondary chassis. Only noninterrupter slave boards are allowed in the secondary chassis.

The link between the primary chassis and secondary chassis is automatically established when a VMEbus master (typically a CPU board) addresses any board in the secondary chassis.

Any time a master in the primary chassis issues a VMEbus read/write cycle it will be repeated to the secondary chassis. If a slave board in any secondary chassis responds to that address, the data transfer (read or write) will occur between the chassis and a Data Transfer Acknowledge (DTACK) will be generated to the master (in primary chassis) to complete the cycle.

A link consists of two boards (VMIVME-5531M and VMIVME-5531S) and two cables which enable a VMEbus system to be expanded beyond a single chassis. Refer to Figures 3, 4, and 5 for a block diagram of each board.

Ordering Options

Dec. 21, 1995 800-005531-000 C		A	B	C	–	D	E	F
VMIVME-5531L	–				–			

A =
0 = 6U Slave
1 = 3U Slave

BC =

00 = Not Used	09 = 1,000 ft (304.8 m)
01 = 5 ft (1.5 m)	10 = 1,500 ft (457.3 m)
02 = 25 ft (7.6 m)	11 = 2,000 ft (609.7 m)
03 = 50 ft (15.2 m)	12 = 2,460 ft (750.0 m)
04 = 100 ft (30.4 m)	13 = 3,280 ft (1,000 m)
05 = 150 ft (45.7 m)	14 = 4,100 ft (1,250 m)
06 = 200 ft (60.9 m)	15 = 4,920 ft (1,500 m)
07 = 350 ft (106.7 m)	16 = 5,740 ft (1,750 m)
08 = 500 ft (152.4 m)	17 = 6,560 ft (2,000 m)

Boards Only		A	B	C	–	D	E	F
VMIVME-5531M	–	0	0	0	–			

ABC = 000 (Options reserved for future use)

		A	B	C	–	D	E	F
VMIVME-5531S	–		0	0	–			

A =
0 = 5531S (6U Form Factor)
1 = 5531S (3U) (3U Form Factor)

BC = 00 (Options reserved for future use)

Note

Link: Includes one VMIVME-5531M Board, one VMIVME-5531S Board, and two cable assemblies.

Connector Data

Compatible Connector	*ST Connector*
PC Board Fiber-Optic Connector	Fiber-Optic Receiver HFBR-2416 (Hewlett-Packard) Fiber-Optic Transmitter HFBR-1414 (Hewlett-Packard)

For Ordering Information, Call:
1-800-322-3616 or 1-256-880-0444 • FAX (256) 882-0859
E-mail: info@vmic.com Web Address: www.vmic.com

VMEbus SPECIFICATIONS

VMEbus Compliance[1]:
Complies with VMEbus specification revision C.1
A32, A24, A16: D32, D16, D08 (EO)
VMIVME-5531M is DTB slave (for use in Primary Chassis)
VMIVME-5531S is DTB master (for use in Secondary Chassis)
Form factors - double height, single slot: 5531M
- double height, single slot: 5531S
- single height, single slot: 5531S (3U)

VMEbus Signals Repeated to Secondary Chassis[1,2]: A1 to A31, D0 to D31, DS0*, DS1*, WRITE*, SYSRESET*, AM2, AM4, AM5, and LWORD*

VMEbus Signals Returned to Primary Chassis: (D0 to D31, DTACK*)[3] (D0 to D15, DTACK*)[4]

Address Modifiers: (09, 0D)[3], 29, 2D, 39, 3D

Read Cycle Overhead: 4.0 µs maximum

Write Cycle Overhead: 4.0 µs maximum

Transfer Rate (Maximum) (10-foot Cable Length): Assuming a slave board in the secondary chassis responds in 250 ns:
1 Mbyte/s minimum (D32)[3]

CABLE SPECIFICATIONS[5]

Mode: Multi

Length: 5 to 6,560 ft (1.5 to 2,000 m)

Fiber Size: 62.5 micron

Clad Size: 125 micron

Buffer Size: 900 micron

Maximum Attenuation at 1,300 nm: 9.0 dB (including connectors)

Bandwidth at 1,300 nm: 500 MHz-km

Boot Length: 20 to 60 mm

U.L./NEC Rating: OFNP

Connector Style: ST, 2.5 mm Bayonet, both ends

Quantity Required: 2 per link

PHYSICAL/ENVIRONMENTAL

Temperature: 0 to 65 °C, operating
-20 to 85 °C, storage

Humidity: 20 to 80 percent, relative noncondensing

Altitude: Operation to 10,000 ft

Cooling: Forced air convection

Dimensions: Double height (6U) board 160 x 233.35 mm or single height (3U) board 160 x 100.0 mm (Slave only)

POWER REQUIREMENTS

VMIVME-5531S: 2 A typical at 5 VDC
2.5 A maximum

VMIVME-5531M: 2 A typical at 5 VDC
2.5 A maximum

TRADEMARKS

1. 3U Slave repeats A1 to A23, D0 to D15, DS0*, DS1*, WRITE*, SYSRESET*, AM2, AM4, AM5, and LWORD*.
2. The following signals are regenerated at the secondary chassis instead of being sent over the fiber-optic cable: SYSCLK, AM0, AM1, AM3, and IACK*.
3. Applies to 6U.
4. Applies to 3U.
5. Cables conforming to this specification are supplied with VMIVME-5531L. To order cables separately, please refer to specification number VMICBL-000-F3-xxx.

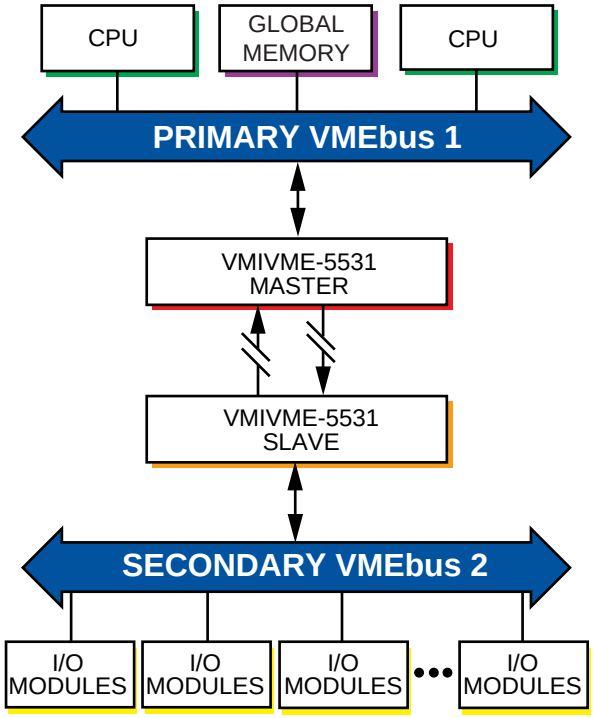


Figure 1. Single Link Application Diagram

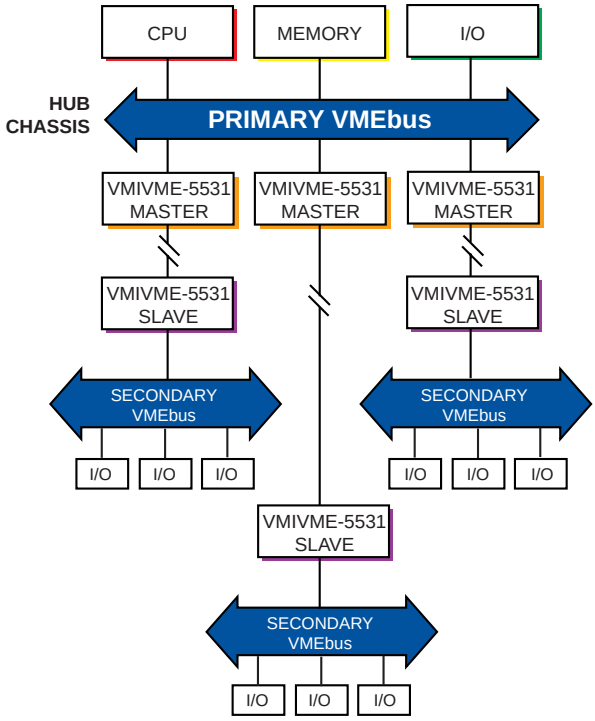


Figure 2. Star Configuration

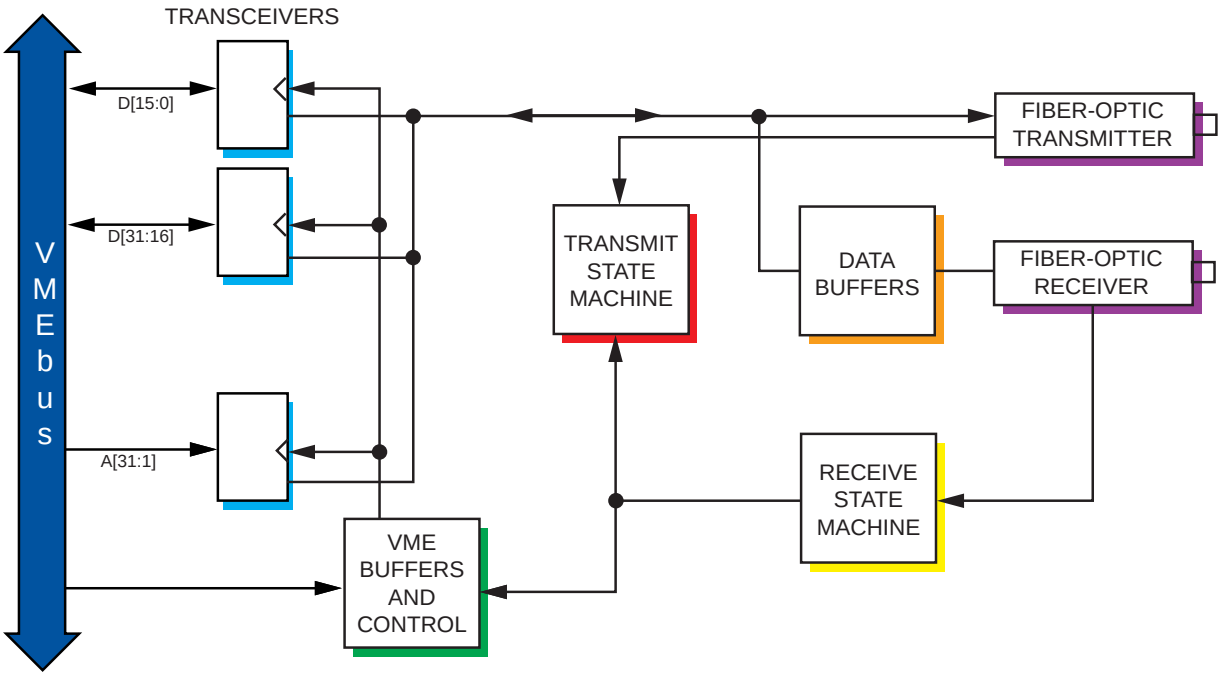


Figure 3. Block Diagram of 5531M Primary Chassis Repeater Link Board

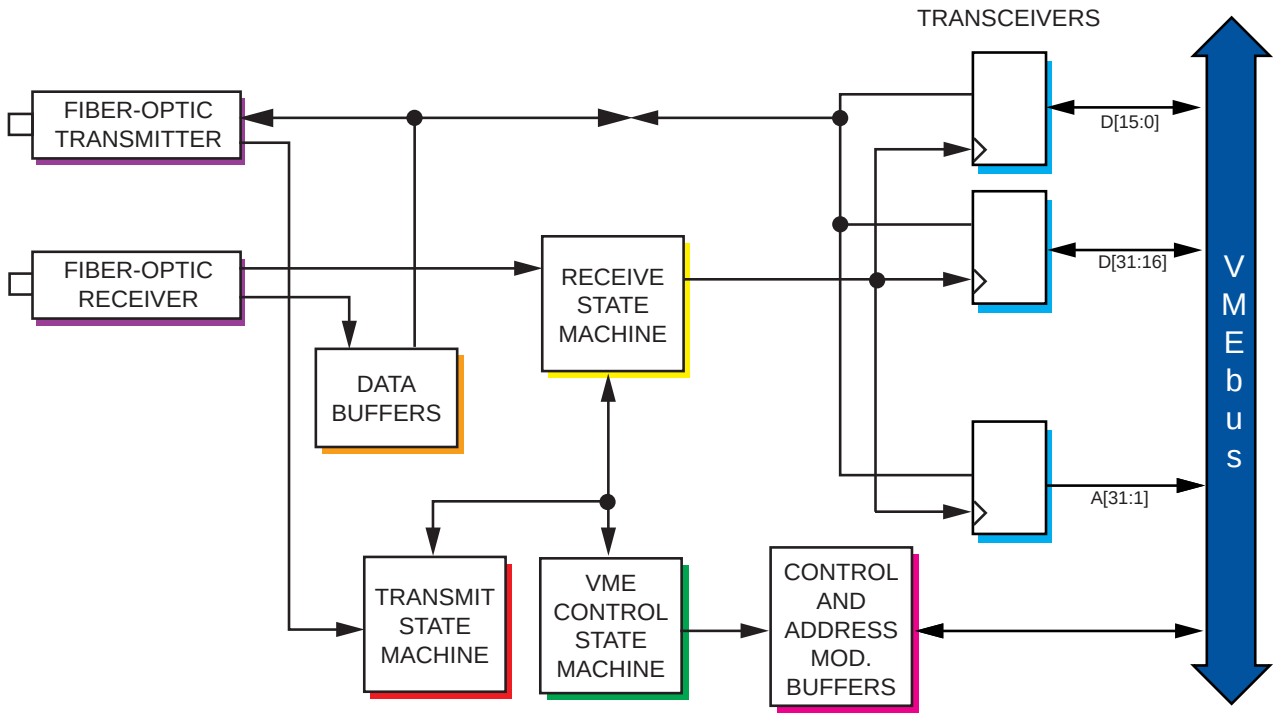


Figure 4. Block Diagram of 5531S Secondary Chassis Repeater Link Board (6U)

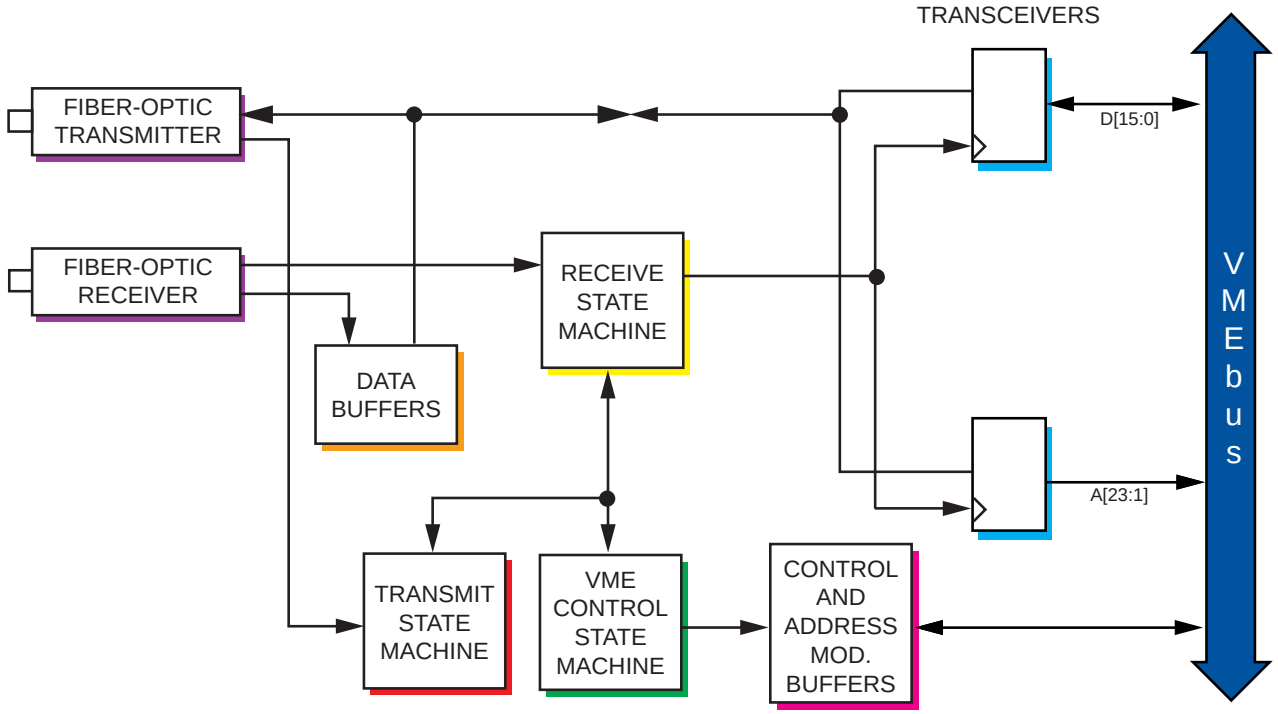


Figure 5. Block Diagram of 5531S Secondary Chassis Repeater Link Board (3U)