



Dual 64-pin Transition Panel with Bussed Ground

- Converts two 64-pin DIN connectors to lift clamp terminal blocks
- Directly compatible with
- VMIVME-2532
- VMIVME-3110
- VMIVME-4100
- VMIVME-4101
- VMIVME-4105
- VMIVME-4116
- VMIVME-4120VMIVMF-4500
- EIA standard RS-310C 19-inch rack mountable in 1U space (1.75 inch)

INTRODUCTION — The VMIACC-BT02 provides a compact, cost-effective transition between field wiring and VMIC I/O boards. Lift clamp style terminal blocks are provided for attachment of field wiring while two 64-pin DIN connectors are provided for connection to I/O boards. Mass-terminated flat cables may be used to connect between the transition panel and the I/O boards. Figure 1 is a dimensional outline drawing of the VMIACC-BT02 while Figure 2 is a functional block diagram of the product.

PHYSICAL CHARACTERISTICS

Width: 19 inch

Height: 1.75 inch (1U)

Depth: 1.25 inch

Weight: .9 lb

TERMINAL BLOCK

Body: Noryl SE 100, light grey similar RAL 7035

Clamp: Steel, galvanized, and chromated

Screw: M2.6 steel

Maximum Wire Diameter: Solid wired up to 4 mm² (12 to 22 AWG). Fine stranded wired up to 2.5 mm² (14 to 22 AWG), multicore cable end up to 2.5 mm².

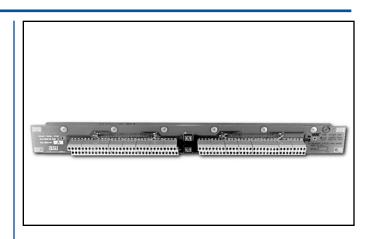
ELECTRICAL DATA

The ampacity of the transition panels is limited by the DIN connectors of 1.25 A per pin.

The ampacity of the transition panels printed circuit copper is 6 A AC/DC.

The powerpacity of the connecting terminals must not exceed 250 W per circuit.

The voltage limit of the transition panels printed circuits is 48 V AC/DC not to exceed 250 W per circuit.



CONNECTOR DATA

Compatible Connector: Panduit No. 120-964-435E

Strain Relief: Panduit No. 100-000-032

PC Board Connector: Panduit No. 120-964-033A

TRADEMARKS

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Ordering Options								
March 2, 1994 800-000140-000 A		Α	В	С	-	D	Е	F
VMIACC-BT02	-	0	0	0	-			
ABC = 000 (Options reserved for future use)								

For Ordering Information, Call:
1-800-322-3616 or 1-256-880-0444 • FAX (256) 882-0859
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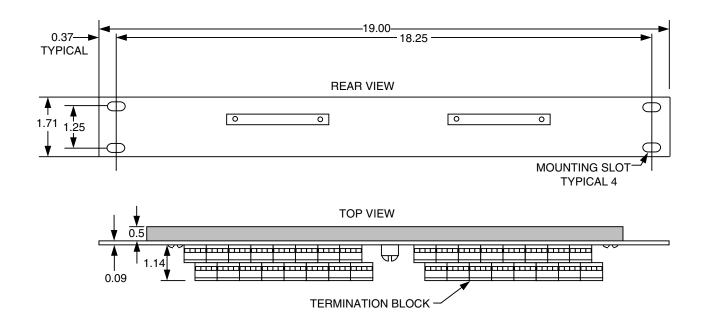


Figure 1. VMIACC-BT02 Dual 64-pin Transition Panel with Bussed Ground

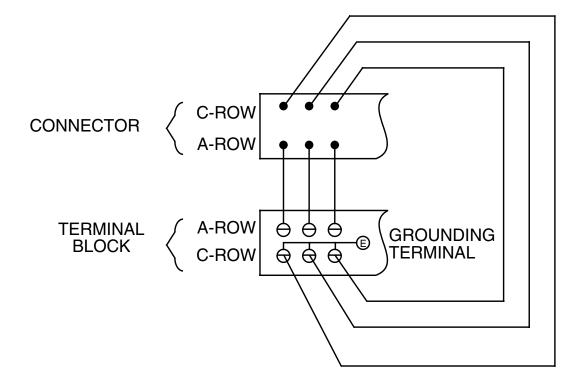


Figure 2. Functional Block Diagram