

- Converts two 96-pin DIN connectors to lift clamp terminal blocks
- Directly compatible with all VMIC products which use 96-pin I/O connectors
- EIA standard RS-310C 19-inch rack mountable in 2U space

INTRODUCTION — The VMIACC-BT04 provides a compact, cost-effective transition between field wiring and VMIC I/O boards. Lift clamp style terminal blocks are provided for attachment of field wiring while two 96-pin DIN connectors are provided for connection to I/O boards. Mass-terminated flat cables may be used to connect between the transition panel and the I/O boards. Figure 1 is a dimensional outline drawing of the VMIACC-BT04 while Figure 2 is a functional block diagram of the product.

FUNCTIONAL CHARACTERISTICS

TERMINAL BLOCK MATERIALS

Body: Noryl SE 100, light grey similar RAL 7035

Clamp: Steel, galvanized, and chromated

Screw: Steel, galvanized, and chromated

Wire Protection: CuZn, brass, prenickled, and 5 μ tin-plated

MECHANICAL DATA

Pitch: .197 inch/5.0 mm

Screw: M2.6

Maximum Wire Diameter: Solid wired up to 4 mm² (12 to 22 AWG). Fine stranded wired up to 2.5 mm² (14 to 22 AWG), multicore cable end up to 2.5 mm².

ELECTRICAL DATA

Maximum Current: 1.25 A

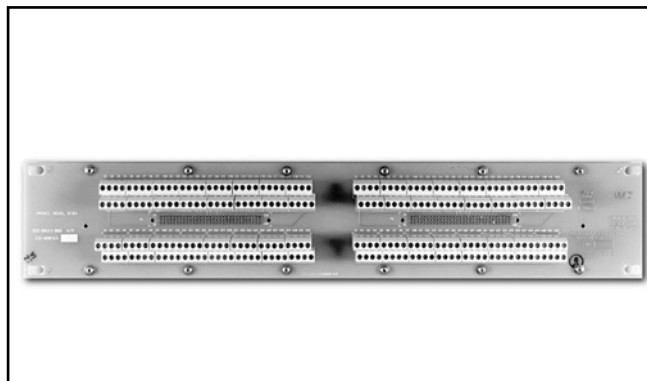
Maximum Voltage: 250 VAC

CONNECTOR DATA

Compatible Connector: ERNI No. 913.031

Strain Relief Housing: Harting No. 09030960501

PC Board Connector: ERNI No. 913.216



TRADEMARKS

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Ordering Options		
October 11, 1994 800-000163-000 A		
VMIACC-BT04		
Connector Data		
Style	Recommended Connecting Component	I/O Connectors
96-pin IDC	Mating Connector (96-pin Mass Connector)	ERNI No. 913.031
	0.033-inch Ribbon Cable (96-pin Mass Connector)	ERNI No. 913-049
	PC Board Connector Part Number	ERNI No. 913-216
For Ordering Information, Call: 1-800-322-3616 or 1-256-880-0444 • FAX (256) 882-0859 E-mail: info@vmic.com Web Address: www.vmic.com Copyright © February 1994 by VMIC Specifications subject to change without notice.		

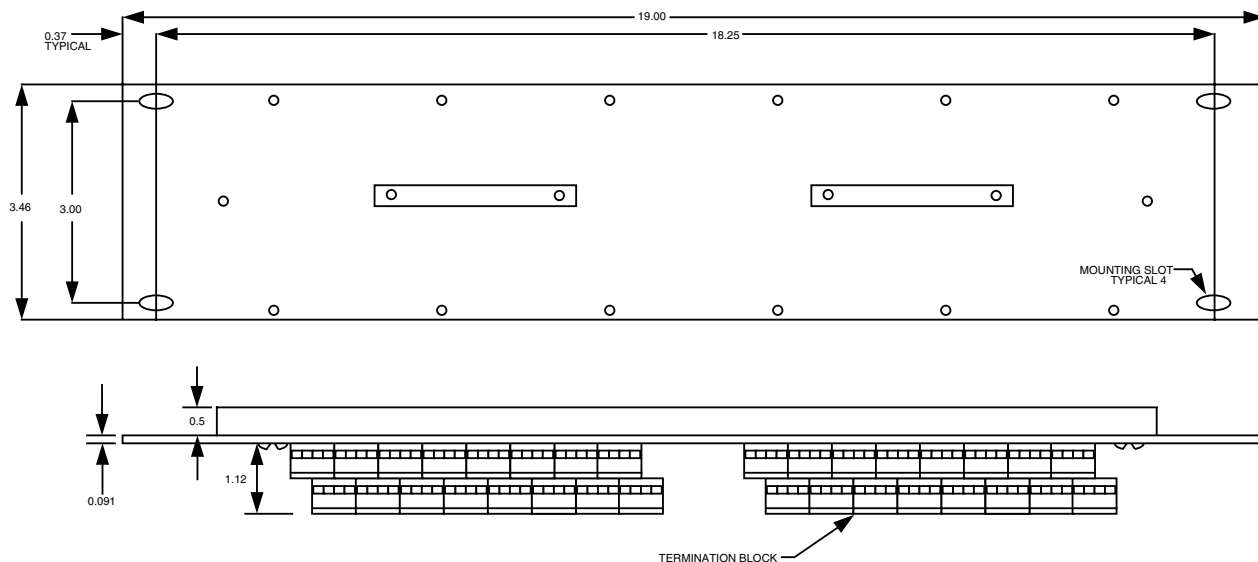


Figure 1. VMIACC-BT04 Dual 96-pin Transition Panel with Bussed Ground

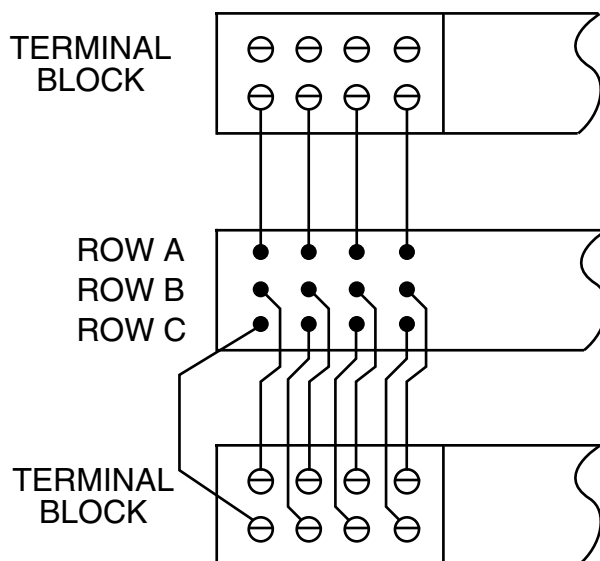


Figure 2. Functional Block Diagram