OPERATIONS MANUAL Motherboards/Cardcages

WinSystems reserves the right to make changes in the circuitry and specifications at any time without notice.

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TABLE OF CONTENTS

Motherboards

<u>Drawings</u>

Warranty and Repair Information

MOTHERBOARDS

1. Power And Signal Connections For All WinSystems Motherboards:

Pin 1 = +5 VDC

Pin 2 = Ground

Pin 3 = VBAT

Pin 4 = -12 VDC

Pin 5 = Ground

Pin 6 = -5 VDC or / PROTECT

Pin 7 = +12 VDC

Pin 8 = Auxiliary Ground

Pin 9 = /PBREŠET (Manual or Push Button Reset)

2. Motherboard And Cable Pin Out:

3. Plug And Receptacle Molex Part Numbers:

Molex Plug Part Number 03-09-2092

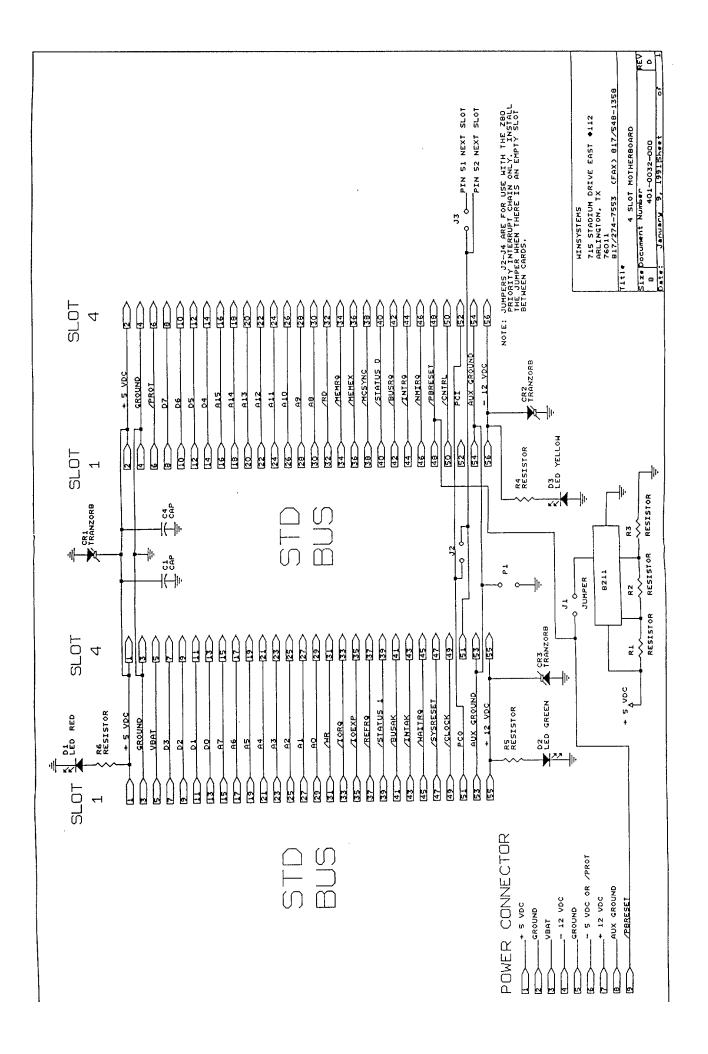
Molex Receptacle Part Number 03-09-1094

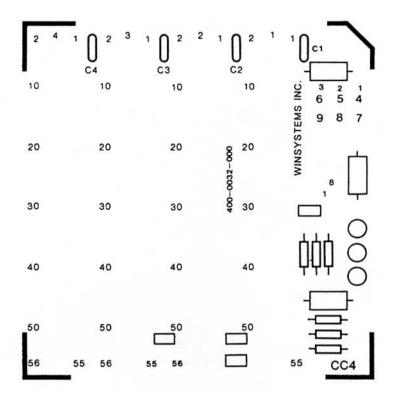
4. Interrupt Priority For Use With Cards That Utilize The Z80 Mode 2 Interrupt Structure:

Highest Priority = Slot 1

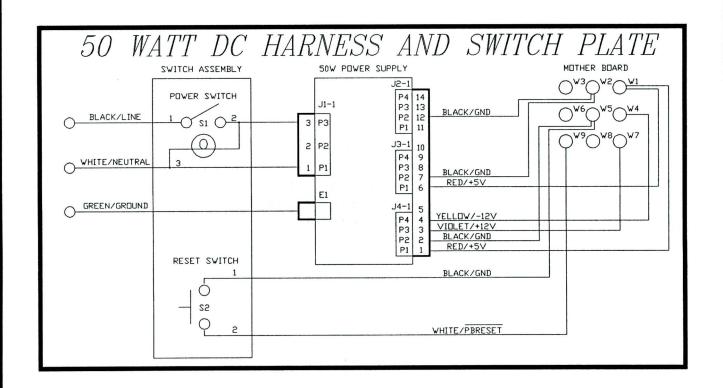
Lowest Priority = Slot n (Highest Number)

- 5. Jumpers J2 Jn Are Used For Shorting PCI to PCO For An Unused Card Slot When Z80 Mode 2 Interrupts Are Used.
- If The Optional Power Fail Circuit Is Installed, J1 Enables,
 Disables This Option. When Enabled, The Power Fail Reset
 Will Pull The /PBRESET Line On The STD BUS Low When +5 VDC
 Fails.



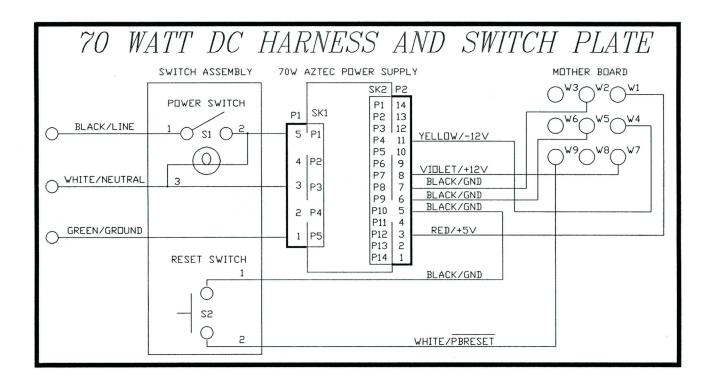


REV.	DESCRIPTION	DATE:	ALT. BY	APROVED
Α	CONVERTED TO CAD.	5-10-90	K. THOMAS	

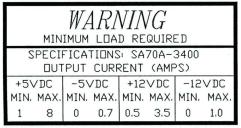


WinSystems, Inc.					
TITLE					
50W DC H	ARNESS	& S1	WITCH	<i>PLATE</i>	
DRW. BY: DATE:		APP. BY:	DA	TE:	
K. THOMAS	05-10-90				
ACAD ID:	SCALE:		TOL:		
50WDIAG.DWG	NOTED				
FSCM. NO:	DRW. NO:		REV:	SHT. OF	
			A	1 1	

REV.	DESCRIPTION	DATE:	ALT. BY	APROVED
Α	CONVERTED TO CAD.	4-11-90	K. THOMAS	

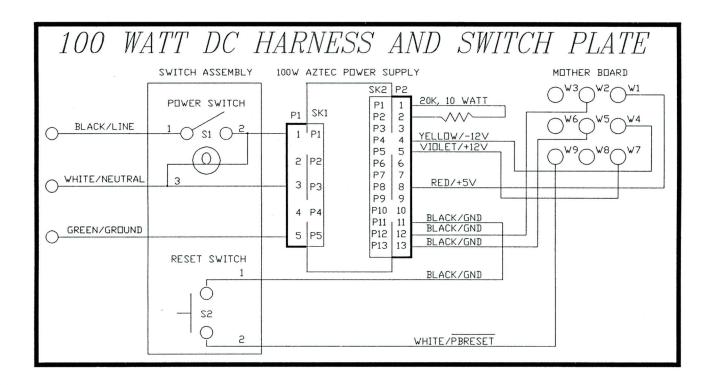


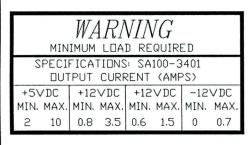
TAT'



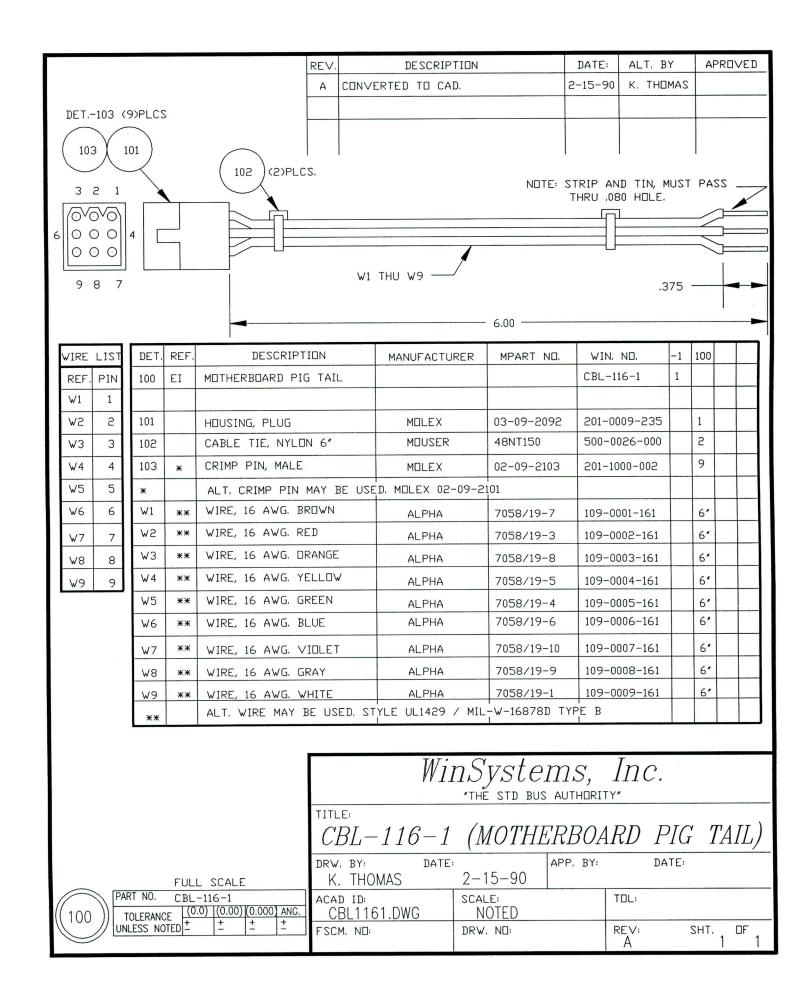
WinSystems, Inc.						
	"THE STD BUS	AUTHORI	TY"			
TITLE:						
70W DC H	ARNESS	& SV	WITCH	PLATE		
DRW. BY: DATE K. THOMAS	04-11-90	APP. BY:	DA	TE:		
ACAD ID: 70WDIAG.DWG	scale: NOTED		TOL:			
FSCM. N□:	DRW. NO:		REV: A	SHT. OF 1 1		

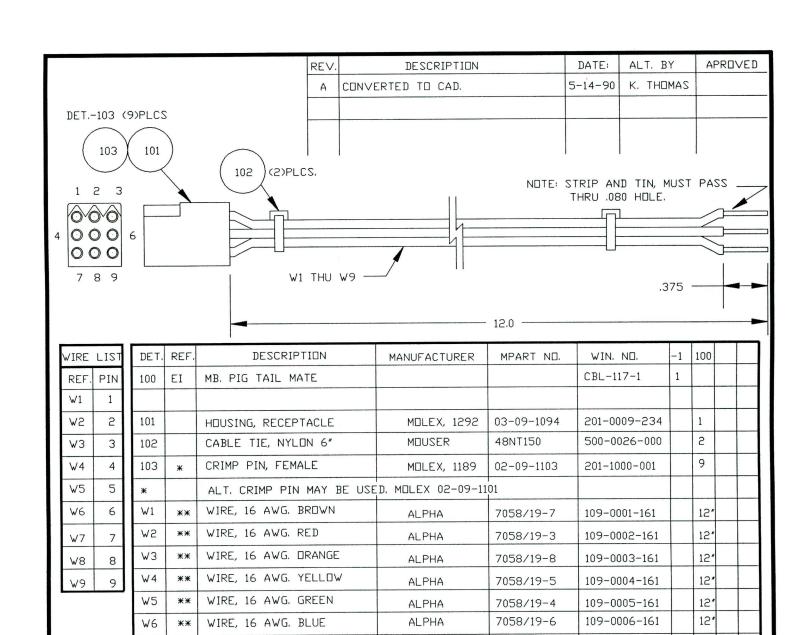
REV.	DESCRIPTION	DATE:	ALT. BY	APROVED
Α	CONVERTED TO CAD.	5-10-90	K. THOMAS	





WinSystems, Inc.				
100W DC I	HARNESS	5 & 5	SWITCH	PLATE
DRW. BY: DATE:	05-10-90	APP. BY:	DATE	
ACAD ID: SCALE: NOT		ED	TOL:	
FSCM. N□:	DRW. NO:		REV: A	SHT. OF 1





ALPHA

ALPHA

ALPHA

ALT. WIRE MAY BE USED. STYLE UL1429 / MIL-W-16878D TYPE B

WinSystems, Inc. "THE STD BUS AUTHORITY" CBL-117-1 (MB. PIG TAIL MATE) APP. BY: DRW. BY: DATE: K. THOMAS 5-14-90 SCALE: ACAD ID: TOL: CBL1171.DWG NOTED SHT. DF FSCM. NO: DRW. NO: REV:

7058/19-10

7058/19-9

7058/19-1

12"

12

12"

109-0007-161

109-0008-161

109-0009-161

FULL SCALE

PART NO. CBL-117-1

TOLERANCE (0.0) (0.00) (0.000) ANG.
UNLESS NOTED + + + +

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W7

W8

W9

WIRE, 16 AWG. VIOLET

WIRE, 16 AWG. GRAY

WIRE, 16 AWG. WHITE

