

Technical Information Manual

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MOD. A1932A

*48 CHANNEL 3 kV/500 μ A
DISTRIBUTOR BOARD*

NPO:
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Disposal of the Product

The product must never be dumped in the Municipal Waste. Please check your local regulations for disposal of electronics products.



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1. Introduction

1.1 The CAEN Universal Multichannel Power Supply System

The SY 1527 system is the fully equipped, large scale experiment version of the latest CAEN Universal Multichannel Power Supply System. This system outlines a completely new approach to power generation and distribution by allowing to house, in the same mainframe, a wide range of boards with different functions, such as High/Low Voltage boards, generic I/O boards (temperature, pressure monitors, etc.) and branch controllers, where the latter are used to control other remote generators and distributors.

Modularity, flexibility and reliability are the key-points of its design, enabling this module to meet the requirements of a wide range of experimental conditions. The latter range from those of LHC experiments, in which the model's features find prior application, to those of other less challenging, but still demanding, High Energy Physics experiments.

The system is housed in a 19"-wide, 8U-high euro-mechanics rack and hosts four main sections:

- the *Board Section*, with 16 slots to house boards, distributors and branch controllers;
- the *Fan Tray Section*, housing 6 fans disposed on two rows;
- the *Power Supply Section*, which consists of the primary power supply and up to 3 power supply units;
- the *CPU and Front Panel Section* which includes all interface facilities.

The User interface features the usual friendliness of the previous CAEN systems which now also includes a 7.7" colour LCD. A wide choice of interfaces provides full communication compatibility with the previous systems and the possibility of controlling heterogeneous external devices.

Modularity has been one of the leading criteria in the design and development of the system: both the *Power Supply Section* and the *Board Section* are completely modular. The *Power Supply Section* allows different configurations with up to 3 power supply units per mainframe (up to 2250 W), while the *Board Section* can house up to 16 boards able to fulfil different functions. A new line of boards and distributors, analogous with those available for the SY 527 system, and a set of branch controllers has been specially developed for this new system. The minimum system configuration consists of the primary power supply, one Power Supply Unit and one board.

The extreme flexibility of the system, which allows to mix freely, inside the same mainframe, boards with different functions, is further enhanced by the possibility of developing *ad-hoc* boards and even complete custom peripheral systems. The latter, actually, can be designed specifically for on-detector installation. All the custom electronics can be anyway remotely controlled by single boards which are inserted in the SY 1527 mainframe and act as branch controllers.

Fast, accurate set-up and monitoring of system parameters (14-bit resolution on Voltages and Currents with standard boards) is available for each branch controller thanks to the

use of one microprocessor per slot. All the operational parameters are stored in a non-volatile memory (EEPROM) to be still available after Power-Off. The parameters can be controlled either via CAEN traditional built-in links (RS232, H.S. CAENET) or via CERN-approved Fieldbuses or via Ethernet (TCP/IP). Programmable handling of parameters and errors is available as well.

Channel trip control on other crates is performed via four external differential trip lines. A sophisticated trip handling via software allows to control and correlate trip conditions on the channels of the crate as well as of other crates connected to it.

Live insertion and extraction of the boards, which reduces the global down time, and easy access to the computing core and peripherals completes the system's flexibility.

Easy interfacing is another key-point of the SY 1527 system. Thanks to the H.S. CAENET interface, the system ensures full communication compatibility with the previous models. Besides the RS232 interface and Ethernet (TCP/IP) provided with the standard version of the system, CAN-bus can be furnished on request, as well as special boards featuring optical links for remote communications. The Power Supply Section and Board Section can be externally synchronised via front panel connectors.

Secure access to the system via Intranet is foreseen together with a multilevel management of custom User's profiles. In particular, three different access levels have been implemented: *Guest*, *User* and *Administrator*; the three of them are password protected.

Handy maintenance and upgrading, which constitute a major issue in the reliability of a system, are further guaranteed by the possibility of accessing and servicing the system via network facilities. Actually, Telnet access allows remote debugging and technical support of the system, including firmware upgrading.

For a detailed description of the SY 1527 Universal Multichannel Power Supply System please refer to the *SY 1527 User's Manual* .

1.2 Technical Specifications Table of the SY 1527 system

Table 1.1 – Technical specifications of the SY1527 mainframe: general

| | |
|--|---|
| Packaging | - 19"-wide, 8U-high Euro-mechanics rack; - Depth: 720 mm. |
| Weight | -Mainframe (*): 24 kg -Mod. A1532: 3.2 kg |
| Power requirements | <i>Voltage range:</i> 100/230 Vac <i>Frequency:</i> 50/60 Hz <i>Power:</i> 3400 W |
| Max. number of boards per crate | 16 |
| Max. number of power supply units per crate | 3 |
| Primary power supply output (Mod. A 1531) | ± 12 V, 8 A +5 V, 20 A |
| Power supply unit output (Mod. A 1532) | +48 V, 15.6 A |
| Max. output power | 2250 W |
| Operating temperature | From 0°C (dry atmosphere) to +40°C |
| Storage temperature | From -20°C (dry atmosphere) to +50°C |

(*). One Primary Power Supply (Mod. A 1531) and one Power Supply Unit (Mod. A 1532) are included; boards are not included.

2. Mod. A1932A Floating Distributor Board

2.1 Functional description

The Model A1932A, High Voltage Positive (Mod. A1932AP) or Negative (Mod. A1932AN) Floating Distributor Power Supply Board for the SY1527 System, is a 48 Channel board with a maximum output voltage of 3 kV. The board is double-width.

The Board houses a Primary High Voltage Channel and 48 Distributed Output Channels, directly supplied by the Primary Channel.

The internal Primary HV Channel has a complete set of parameters that can be programmed, such as high voltage, current limit, voltage RAMP-UP and RAMP-DOWN.

The 48 Output Channels are organised into six 8-channel Groups. Most functional parameters can be programmed individually for each output channel, others, such as safety limits, can be set over a group or over the primary channel.

The HV RAMP-UP and RAMP-DOWN rates may be selected independently for each channel in the range 1÷500 V/s in 1 V/s steps.

The Primary Channel and each group feature independent ON/OFF switching (i.e. if the *groupX* is ON and the primary channel is OFF, at the primary channel's switching ON, the channels in the *groupX* will automatically ramp up), see § 4.1 for details.

The output channels share a common floating ground (i.e. the output voltages are not referred to the crate's ground reference). Both the floating ground (FAGND) and the crate ground (AGND) are available on front panel connectors.

The Primary Channel current is monitored with a 10 µA resolution; if a current larger than the programmed limit ISET is drawn, OVERCURRENT condition is signalled; the board controller detects this state as a fault and reacts turning off the channel: the maximum current that the primary channel can draw is 30 mA (see § 4.1.1). OVERCURRENT is monitored over each group as well: if one group tries to draw more than 10 mA, it trips (see § 4.1.3); this value is hardware-fixed, thus not programmable.

The board hosts also a temperature sensor located on the PCB near the HV channels: the temperature values measured by this sensor are used to signal Over Temperature condition on the SY 1527.

The voltage on each Distributed Output Channel can be independently programmed in a range of 100÷900 V drop from the Primary Channel voltage setting, with a maximum current of 0.5 mA.

If the user tries to set an outside-drop value via software, the SY1527 returns an error message (either OVV=overvoltage or UNV=undervoltage). Moreover if the Primary Channel's voltage value is updated, the output channels must be updated as well, in order to remain within the allowed limits.

Actually when the Primary Channel voltage is updated, the Distributed Output Channels which are no longer within the drop, are somehow “automatically updated”, but their operating condition is not proper (for example ripple may increase and accuracy may decrease, with respect to the values given in § 2.2), until their Vset parameter is updated by the User.

EXAMPLE: the Primary Channel is set at 2000 V and Channel X is at 1450 V; if the Primary Channel is brought at 2500 V, Channel X Vset must be updated at 1600 V ¹ at least.

The output voltages are provided via a Radiall 52-pin connector.

The multipin output connector has two pins dedicated to realize the safety board interlock (see § 4.2). This protection allows to disable the primary HV generation when the A1932 A outputs are not connected to their loads.

¹ Channel X will be actually brought up automatically at 1600 V in this case, but its Vset must be updated anyway by the User for correct operation, otherwise UNV is signalled and ripple might increase.

2.2 Technical Characteristics Table

Table 2.1 – Technical characteristics of the Mod. A1932A Distributor Board

| | |
|---|---|
| Polarity: | Positive / Negative depending on purchased version |
| Primary Voltage: | 0÷3.1 kV |
| Max Primary Current:² | 30 mA (programmable) |
| Max Group Current:² | 10 mA (fixed) |
| Output Channel Voltage: | 0÷3.0 kV |
| Max Output Channel Current: | 0.5 mA |
| Primary-Output drop: | 100÷900 V |
| Voltage set / Monitor Resolution: | 200 mV (Primary; Output) |
| Current set / Monitor Resolution | 20 µA (Primary) |
| VMAX software: | 3.1 kV (Primary) |
| VMAX software resolution: | 1 V |
| Ramp Down: | 1÷500 Volt/sec, 1 Volt/sec step |
| Ramp Up: | 1÷500 Volt/sec, 1 Volt/sec step |
| Voltage Ripple:³ | < 30 mV pp |
| Voltage Monitor vs (Output; Primary) Voltage Accuracy:⁴ | ± 1 V ± 0.1% of reading (Output) ± 5 V ± 1% of reading (Primary) |
| Voltage Set vs Voltage Monitor Accuracy:⁴ | ± 1 V ± 0.1% of setting |
| Current Monitor vs. Primary Current Accuracy:⁴ | ± 50 µA ± 2% of reading |
| Current Set vs. Current Monitor Accuracy:⁴ | ± 50 µA ± 2% of setting (Primary) |

² This is the threshold of the Overcurrent protection intervention

³ From 1 kHz to 15 MHz at full load and Channel Vset programmed within the allowed voltage drop

⁴ From 10% to 90% of Full Scale Range and Channel Vset programmed within the allowed voltage drop

2.3 Front Panel

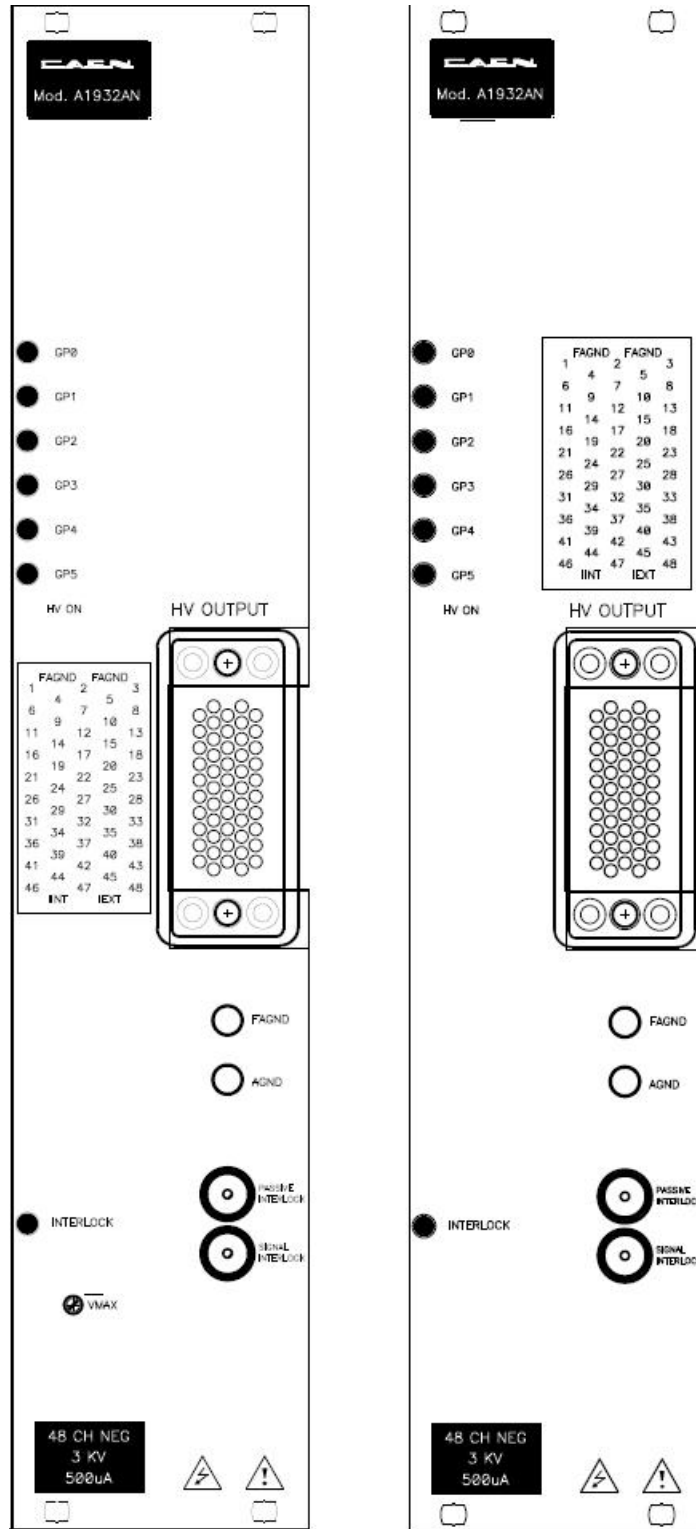


Fig. 2.1 – Mod. A1932A front panel Rev.2 and Rev.1

2.4 Technical Specifications

2.4.1 Packaging

The Mod. A1932A module is housed in a 10 TE-wide, 6U-high mechanics. It occupies two SY1527 slots (double-width).

2.4.2 External connections

The location of all components of the front panel is shown in Fig. 2.1. The function and electro-mechanical specifications of the external connectors are listed in the following subsections.

| | |
|---|--|
| Output Channels (1...48); Floating Ground (FAGND); IINT, IEXT: | Multipin connector Radiall 691803004 type, 52 pin male (to be mated with Radiall 691802002 [<i>SCEM 09.41.34.700.2</i>] type ⁵); see Fig. 2.2 for pin assignment |
| FAGND: | Radiall R921921 socket, Ø 2mm; see § 4.3 |
| AGND: | Radiall R921921 socket, Ø 2mm; see § 4.3 |
| PASSIVE INTERLOCK: | 00-type LEMO connector; see § 4.2 |
| SIGNAL INTERLOCK: | 00-type LEMO connector; see § 4.2 |
| VMAX⁶ | trimmer: it allows to adjust the hardware maximum voltage VMAX common to all the channels. Its value can be read out via software. |

⁵ Requires 52 pins Radiall 691804300 [*SCEM 09.41.33.830.7*] type, to be inserted using the insertion/extraction tool Radiall 282549024 [*SCEM 34.95.17.125.3*] type.

⁶ This trimmer is featured only on models with front panel Rev.2 and greater; see § 2.3

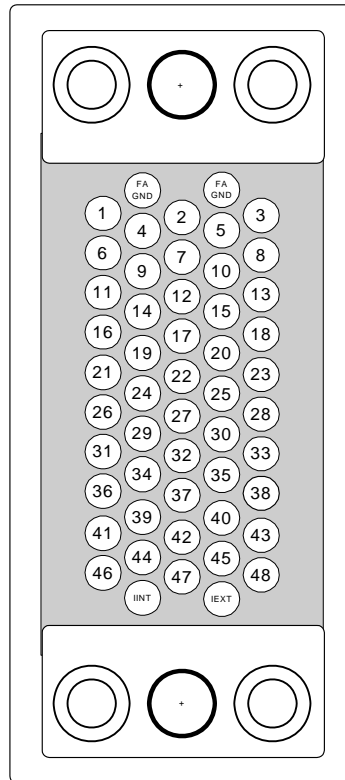


Fig. 2.2 – Mod. A1932A HV Multipin Connector

2.4.3 Displays

GROUP 0..5 LEDs:

Function: they light up as the relevant group is on.

Type: red LEDs for positive polarity version; yellow LEDs for negative polarity version.

INTERLOCK LED:

Function: it lights up as none of the interlocks (connector interlock, passive interlock, signal interlock) is active and the channel can be turned on.

Type: green LED.

2.4.4 Jumpers

Jp1:

Function: if connected, it allows to match the Floating Analog Ground (FAGND) with the ground of the crate (AGND). See § 4.3.

Factory setting: **connected**

Jp2:

Function: if connected, enables the channels switching on, regardless the Passive or the Signal INTERLOCK setting. See § 4.2.

Factory setting: **disconnected**

Jp3:

Function: if connected, enables the channels switching on, regardless the Connector INTERLOCK status. See § 4.2.

Factory setting: **disconnected**

See Fig. 2.3 for jumpers' position.

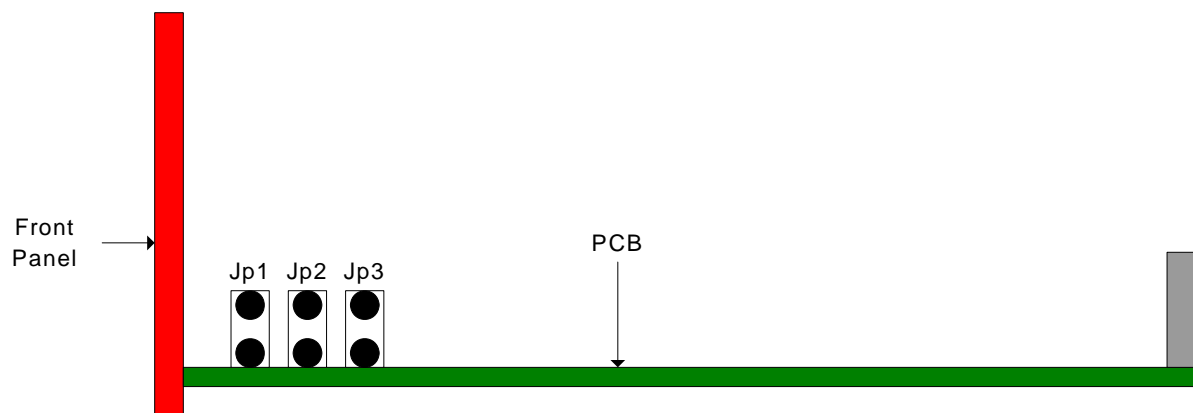


Fig. 2.3 – Mod. A1932A (view from bottom)

3. Safety information and installation requirements

3.1 General safety information

This section contains the fundamental safety rules for the installation and operation of the boards. Read thoroughly this section before starting any procedure of installation or operation of the product.

3.1.1 Injury Precautions

Review the following precautions to avoid injury and prevent damage to this product or any products connected to it. To avoid potential hazards, use the product only as specified. Only qualified personnel should perform service procedures.

Avoid Electric Overload.

To avoid electric shock or fire hazard, do not apply a voltage to a load that is outside the range specified for that load.

Avoid Electric Shock.

To avoid injury or loss of life, do not connect or disconnect cables while they are connected to a voltage source.

Do Not Operate Without Covers.

To avoid electric shock or fire hazard, do not operate this product with covers or panels removed.

Do Not Operate in Wet/Damp Conditions.

To avoid electric shock, do not operate this product in wet or damp conditions.

Do Not Operate in an Explosive Atmosphere.

To avoid injury or fire hazard, do not operate this product in an explosive atmosphere.

Do Not Operate With Suspected Failures.

If you suspect there is damage to this product, have it inspected by qualified service personnel.

3.2 Safety Terms and Symbols on the Product

These terms may appear on the product:

- **DANGER** indicates an injury hazard immediately accessible as you read the marking.
- **WARNING** indicates an injury hazard not immediately accessible as you read the marking.
- **CAUTION** indicates a hazard to property including the product.

The following symbols may appear on the product:



DANGER
High Voltage



ATTENTION
Refer to Manual

3.3 Installation

The Mod. A1932A is a double-width board which occupies two SY 1527 slots. At power ON the SY 1527 system processor will scan all the slots in the crate to find out where the module is plugged and what kind of module it is.

N.B.: the board does not allow “live extraction”; the system must be turned off before removing the board.

4. Operating modes

The Mod. A1932A board can be controlled, either locally or remotely, through the SY 1527 software interface. For details on SY 1527 system operation, please refer to the User's Manual of this product. The following sections contain a description of commands available for the board control and status monitoring.



ATTENTION

THE MOD. A1932A BOARD REQUIRES
SY 1527 FIRMWARE VERSION 1.09.04 OR LATER

4.1 Output control and monitoring

4.1.1 Primary Channel operations

It is possible, through the SY 1527 system, to perform the following operations over the **primary channel**:

- Assign to the channel a symbolic name (NAME)
- Set voltage (VSET)
- Set max. current (ISET)
- Set voltage software limit (SVMAX)
- Set voltage ramp-up speed (RAMPUP)
- Set voltage ramp-down speed (RAMPDOWN)
- Switch channel ON/OFF (Pw)
- Monitor voltage (VMON)
- Monitor current (IMON)
- Monitor board's temperature (TEMP)

The channel is automatically turned off when OVC (threshold: 30 mA) occurs (PDwn)

4.1.2 Output Channel operations

It is possible, through the SY 1527 system, to perform the following operations over each **output channel**:

- Assign to the channel a symbolic name (NAME)
- Set output voltage (VSET)
- Monitor output voltage (VMON)

- Set voltage ramp-up speed (RAMPUP)
- Set voltage ramp-down speed (RAMPDOWN)

4.1.3 Group operations

For each **8-channel group**, it is possible, through the SY 1527 system, to perform the following operations:

- Assign to the group a symbolic name (NAME)
- Switch group ON/OFF (Pw)
- Monitor group status (STATUS)

The group is automatically turned off when OVC (threshold: 10 mA) occurs (PDwn)

4.1.4 SY 1527 messages

The following messages may be returned by the SY 1527 when monitoring the group STATUS:

- OFF (channels turned OFF)
- RUP (channels ramping up)
- RDWN (channels ramping down)
- OVC (channels in OVERCURRENT condition)
- OVV (channels in OVERVOLTAGE condition)
- UNV (channels in UNDERVOLTAGE condition)
- EXTTRIP (channels OFF due to external TRIP line signal)
- INTTRIP (channels OFF due to internal OVERCURRENT condition)
- EXT_DIS (channels disabled by board INTERLOCK protection)

Moreover it is possible to monitor board temperature and to check board status; the following messages may be returned by the SY 1527 when monitoring the board status:

- UNDER_TEMP (board temperature < 5°C)
- OVER_TEMP (board temperature > 65°C)

4.2 HV channels INTERLOCK⁷

The following procedures must be performed in order to enable the channels switch on:

- Providing either the PASSIVE INTERLOCK connector with a 50 Ohm termination or supplying the SIGNAL INTERLOCK connector with a +5 V (3~4 mA) differential signal (see § 2.4.2). These operations are not necessary if the Jp2 jumper (see § 2.4.4) is connected.

⁷ Please refer to § 2.4.3 for the INTERLOCK LED operation

- Connector INTERLOCK: Pin 51 and Pin 52 (IINT and IEXT) of the HV Multipin Connector have to be short circuited in order to allow the channels turning on; this is a safety feature (see § 2.4.2).

4.3 Grounding specifications

The Mod. A1932A channels share a common floating ground (FAGND, see § 2.4.2), available on the front panel both on the multipin connector and on a LEMO connector, which does not coincide with the crate ground (AGND, see § 2.4.2), which is available as front panel LEMO connector. This feature allows on-detector grounding, thus avoiding loops which may increase noise level. FAGND and AGND may be coupled in several ways, according to the environment requirements.

4.4 A1932A OPC Board control

This chapter describes the Items which are available for the control the Mod. A1932A.

A read access to the **Model** Item returns a string with the board model.

A read access to the **Description** Item returns a string with the board synthetic description.

A read access to the **Fmw Release** item returns a string with the board firmware release.

A read access to the **SerNum** item returns the board serial number.

A read access to the **NrOfCh** item returns the number of board's channels.

A read access to the **BdStatus** item returns the status of generic board's parameters, namely:

bit 0: PowerFail; if 1, it indicates a failure in the channels local power supply

bit 1: Firmware Checksum Error; if 1, it indicates an error in the board firmware checksum

bit 2: HVMax Calibration Error; if 1, it indicates that the board HVMax parameter (if present) is not calibrated

bit 3: Temperature Calibration Error; if 1, it indicates that the board temperature sensor (if present) is not calibrated

bit 4: Under Temperature; if 1, it indicates that the board temperature sensor (if present) signals a board temperature < 5 °C

bit 5: Over Temperature; if 1, it indicates that the board temperature sensor (if present) signals a board temperature > 65 °C

bits 6..31: Reserved for future use

A read access to the **HVMax** item returns the voltage hardware limit set by trimmer on the board. This item is featured only on models with front panel Rev.2 and greater; see § 2.3.

A read access to the **HVMax#EU** item returns a string with the HVMax Engineering Units.

A read access to the **HVMax#HighEU** item returns the highest possible HVMax value.

A read access to the **HVMax#LowEU** item returns the lowest possible HVMax value.

A read access to the **Temp** item returns the board's temperature.

A read access to the **Temp#EU** item returns a string with the Temp Engineering Units.

A read access to the **Temp#HighEU** item returns the highest possible Temp value.

A read access to the **Temp#LowEU** item returns the lowest possible Temp value.

Table 4.1 – A1932A Board items

| ItemID | Data Type | Access Rights | Description |
|--------------------------------------|----------------|---------------|------------------------|
| PowerSupplyName.BoardXX.Model | String | R | Board model |
| PowerSupplyName.BoardXX.Description | String | R | Board description |
| PowerSupplyName.BoardXX.Fmw Release | String | R | Board firmware release |
| PowerSupplyName.BoardXX.SerNum | 2-byte integer | R | Board serial number |
| PowerSupplyName.BoardXX.NrOfCh | 2-byte integer | R | Number of channels |
| PowerSupplyName.BoardXX.BdStatus | 2-byte integer | R | Board status |
| PowerSupplyName.BoardXX.HVMax | 4-byte real | R | Hardware voltage limit |
| PowerSupplyName.BoardXX.HVMax#EU | String | R | HVMax EU |
| PowerSupplyName.BoardXX.HVMax#HighEU | 8-byte real | R | HVMax upper limit |
| PowerSupplyName.BoardXX.HVMax#LowEU | 8-byte real | R | HVMax lower limit |
| PowerSupplyName.BoardXX.Temp | 4-byte real | R | Board temperature |
| PowerSupplyName.BoardXX.Temp#EU | String | R | Temperature EU |
| PowerSupplyName.BoardXX.Temp#HighEU | 8-byte real | R | Temp upper limit |
| PowerSupplyName.BoardXX.Temp#LowEU | 8-byte real | R | Temp lower limit |

4.5 Primary Channel OPC control

This chapter describes the items which are available for the control of the primary channel (Channel 0).

The **Name** item allows to assign to the channel a symbolic name.

The **V0set** item allows to set V0.

A read access to the **V0set#EU** item returns a string with the V0set Engineering Units.

A read access to the **V0set#HighEU** item returns the highest possible V0set value.

A read access to the **V0set#LowEU** item returns the lowest possible V0set value.

The **I0set** item allows to set I0.

A read access to the **I0set#EU** item returns a string with the I0set Engineering Units.

A read access to the **I0set#HighEU** item returns the highest possible I0set value.

A read access to the **I0set#LowEU** item returns the lowest possible I0set value.

The **V1set** item allows to set V1.

A read access to the **V1set#EU** item returns a string with the V1set Engineering Units.

A read access to the **V1set#HighEU** item returns the highest possible V1set value.

A read access to the **V1set#LowEU** item returns the lowest possible V1set value.

The **I1set** item allows to set I1.

A read access to the **I1set#EU** item returns a string with the I1set Engineering Units.

A read access to the **I1set#HighEU** item returns the highest possible I1set value.

A read access to the **I1set#LowEU** item returns the lowest possible I1set value.

The **RUp** item allows to program the ramp-up rate.

A read access to the **RUp#EU** item returns a string with the RUp Engineering Units.

A read access to the **RUp#HighEU** item returns the highest possible RUp value.

A read access to the **RUp#LowEU** item returns the lowest possible RUp value.

The **RDWn** item allows to program the ramp-down rate.

A read access to the **RDWn#EU** item returns a string with the RDWn Engineering Units.

A read access to the **RDWn#HighEU** item returns the highest possible RDWn value.

A read access to the **RDWn#LowEU** item returns the lowest possible RDWn value.
 The **SVMMax** item allows to set the software voltage limit.
 A read access to the **SVMMax#EU** item returns a string with the SVMMax Engineering Units.
 A read access to the **SVMMax#HighEU** item returns the highest possible SVMMax value.
 A read access to the **SVMMax#LowEU** item returns the lowest possible SVMMax value.
 The **VMon** item returns back the VMon value.
 A read access to the **VMon#EU** item returns a string with the VMon Engineering Units.
 A read access to the **VMon#HighEU** item returns the highest possible VMon value.
 A read access to the **VMon#LowEU** item returns the lowest possible VMon value.
 The **IMon** item returns back the IMon value.
 A read access to the **IMon#EU** item returns a string with the IMon Engineering Units.
 A read access to the **IMon#HighEU** item returns the highest possible IMon value.
 A read access to the **IMon#LowEU** item returns the lowest possible IMon value.
 A read access to the **Status** item returns back a 16 bit pattern indicating channel status, as follows:

- Bit 0: ON/OFF
- Bit 1: Ramp Up
- Bit 2: Ramp Down
- Bit 3: OverCurrent
- Bit 4: OverVoltage
- Bit 5: UnderVoltage
- Bit 6: External Trip
- Bit 7: Over HVmax
- Bit 8: External Disable
- Bit 9: Internal Trip
- Bit 10: Calibration Error
- Bit 11: don't care
- Bit12: UnderCurrent
- Bit13: OverVoltage Protection
- Bit14: Power Fail
- Bit15: Temperature Error

The **Pw** item allows to switch ON/OFF the channel.
 A read access to the **Pw#CoOpen** returns back the label "Off" associated to Pw=0.
 A read access to the **Pw#CoClose** item back the label "On" associated to Pw=1.
 The **PDwn** item allows to select the power-down option, as follows
 PDwn=1 ⇒ RAMP
 PDwn=0 ⇒ KILL
 A read access to the **PDwn#CoOpen** item returns back the label "Kill" associated to PDwn=0.
 A read access to the **PDwn#CoClose** item returns back the "Ramp" associated to PDwn=1.

Table 4.2 – Primary Channel items

| ItemID | Data Type | Access Type | Description |
|--|-------------|-------------|----------------------|
| PowerSupplyName.BoardXX.ChanYYY.Name | String | R/W | Channel name |
| PowerSupplyName.BoardXX.ChanYYY.V0Set | 4-byte real | R/W | Set V0 voltage limit |
| PowerSupplyName.BoardXX.ChanYYY.V0Set#EU | String | R | V0set EU |
| PowerSupplyName.BoardXX.ChanYYY.V0Set#HighEU | 8-byte real | R | V0set upper limit |

| ItemID | Data Type | Access Type | Description |
|--|----------------|-------------|----------------------------|
| PowerSupplyName.BoardXX.ChanYYY.V0Set#LowEU | 8-byte real | R | V0set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.I0Set | 4-byte real | R/W | Set I0 current limit |
| PowerSupplyName.BoardXX.ChanYYY.I0Set#EU | String | R | I0set EU |
| PowerSupplyName.BoardXX.ChanYYY.I0Set#HighEU | 8-byte real | R | I0set upper limit |
| PowerSupplyName.BoardXX.ChanYYY.I0Set#LowEU | 8-byte real | R | I0set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set | 4-byte real | R/W | Set V1 voltage limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#EU | String | R | V1set EU |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#HighEU | 8-byte real | R | V1set upper limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#LowEU | 8-byte real | R | V1set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.I1Set | 4-byte real | R/W | Set I1 current limit |
| PowerSupplyName.BoardXX.ChanYYY.I1Set#EU | String | R | I1set EU |
| PowerSupplyName.BoardXX.ChanYYY.I1Set#HighEU | 8-byte real | R | I1set upper limit |
| PowerSupplyName.BoardXX.ChanYYY.I1Set#LowEU | 8-byte real | R | I1set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.RUp | 4-byte real | R/W | Set ramp-up rate |
| PowerSupplyName.BoardXX.ChanYYY.RUp #EU | String | R | Ramp up rate EU |
| PowerSupplyName.BoardXX.ChanYYY.RUp #HighEU | 8-byte real | R | Rup upper limit |
| PowerSupplyName.BoardXX.ChanYYY.RUp #LowEU | 8-byte real | R | RUp lower limit |
| PowerSupplyName.BoardXX.ChanYYY.RDwn | 4-byte real | R/W | Set ramp-down rate |
| PowerSupplyName.BoardXX.ChanYYY.RDwn #EU | String | R | Ramp down rate EU |
| PowerSupplyName.BoardXX.ChanYYY.RDwn #HighEU | 8-byte real | R | RDwn upper limit |
| PowerSupplyName.BoardXX.ChanYYY.RDwn #LowEU | 8-byte real | R | RDwn lower limit |
| PowerSupplyName.BoardXX.ChanYYY.SVMax | 4-byte real | R/W | Set software voltage limit |
| PowerSupplyName.BoardXX.ChanYYY.SVMax #EU | String | R | SVMax EU |
| PowerSupplyName.BoardXX.ChanYYY.SVMax#HighU | 8-byte real | R | SVMax upper limit |
| PowerSupplyName.BoardXX.ChanYYY.SVMax#LowEU | 8-byte real | R | SVMax lower limit |
| PowerSupplyName.BoardXX.ChanYYY.VMon | 4-byte real | R | VMon |
| PowerSupplyName.BoardXX.ChanYYY.VMon #EU | string | R | VMon EU |
| PowerSupplyName.BoardXX.ChanYYY.VMon#HighU | 8-byte real | R | VMon upper limit |
| PowerSupplyName.BoardXX.ChanYYY.VMon#LowEU | 8-byte real | R | VMon lower limit |
| PowerSupplyName.BoardXX.ChanYYY.IMon | 4-byte real | R | IMon |
| PowerSupplyName.BoardXX.ChanYYY.IMon #EU | string | R | IMon EU |
| PowerSupplyName.BoardXX.ChanYYY.IMon#HighU | 8-byte real | R | IMon upper limit |
| PowerSupplyName.BoardXX.ChanYYY.IMon#LowEU | 8-byte real | R | IMon lower limit |
| PowerSupplyName.BoardXX.ChanYYY.Status | 2-byte integer | R | Channel status |

| ItemID | Data Type | Access Type | Description |
|--|-----------|-------------|--------------------|
| PowerSupplyName.BoardXX.ChanYYY.Pw | boolean | R/W | Power ON/OFF |
| PowerSupplyName.BoardXX.ChanYYY.Pw#CoClose | string | R | Pw close label |
| PowerSupplyName.BoardXX.ChanYYY.Pw#CoOpen | string | R | Pw open label |
| PowerSupplyName.BoardXX.ChanYYY.PDwn | boolean | R/W | Power down options |
| PowerSupplyName.BoardXX.ChanYYY.PDwn#CoClose | string | R | PDwn close label |
| PowerSupplyName.BoardXX.ChanYYY.PDwn#CoOpen | string | R | PDwn open label |

4.6 Output Channel OPC control

This chapter describes the items which are available for the control of the output channel (Channel 1..48).

The **Name** item allows to assign to the channel a symbolic name.

The **V0set** item allows to set V0.

A read access to the **V0set#EU** item returns a string with the V0set Engineering Units.

A read access to the **V0set#HighEU** item returns the highest possible V0set value.

A read access to the **V0set#LowEU** item returns the lowest possible V0set value.

The **V1set** item allows to set V1.

A read access to the **V1set#EU** item returns a string with the V1set Engineering Units.

A read access to the **V1set#HighEU** item returns the highest possible V1set value.

A read access to the **V1set#LowEU** item returns the lowest possible V1set value.

The **RUp** item allows to program the ramp-up rate.

A read access to the **RUp#EU** item returns a string with the RUp Engineering Units.

A read access to the **RUp#HighEU** item returns the highest possible RUp value.

A read access to the **RUp#LowEU** item returns the lowest possible RUp value.

The **RDWn** item allows to program the ramp-down rate.

A read access to the **RDWn#EU** item returns a string with the RDWn Engineering Units.

A read access to the **RDWn#HighEU** item returns the highest possible RDWn value.

A read access to the **RDWn#LowEU** item returns the lowest possible RDWn value.

The **VMon** item returns back the VMon value.

A read access to the **VMon#EU** item returns a string with the VMon Engineering Units.

A read access to the **VMon#HighEU** item returns the highest possible VMon value.

A read access to the **VMon#LowEU** item returns the lowest possible VMon value.

A read access to the **Status** item returns back a 16 bit pattern indicating channel status, as follows:

- Bit 0: ON/OFF
- Bit 1: Ramp Up
- Bit 2: Ramp Down
- Bit 3: OverCurrent
- Bit 4: OverVoltage
- Bit 5: UnderVoltage
- Bit 6: External Trip
- Bit 7: Over HVmax
- Bit 8: External Disable
- Bit 9: Internal Trip
- Bit 10: Calibration Error
- Bit 11: don't care
- Bit 12: UnderCurrent

Bit13: OverVoltage Protection
 Bit14: Power Fail
 Bit15: Temperature Error

The **Pw** item allows to switch ON/OFF the channel.
 A read access to the **Pw#CoOpen** returns back the label "Off" associated to Pw=0.
 A read access to the **Pw#CoClose** item back the label "On" associated to Pw=1.
 The **PDwn** item allows to select the power-down option, as follows

PDwn=1 ⇒ RAMP
 PDwn=0 ⇒ KILL

A read access to the **PDwn#CoOpen** item returns back the label "Kill" associated to PDwn=0.
 A read access to the **PDwn#CoClose** item returns back the "Ramp" associated to PDwn=1.
 The **TripInt** item allows to program the internal trip time.
 A read access to the **TripInt#EU** item returns a string with the TripInt Engineering Units.
 A read access to the **TripInt#HighEU** item returns the highest possible TripInt value.
 A read access to the **TripInt#LowEU** item returns the lowest possible TripInt value.
 The **TripExt** item allows to program the external trip time.
 A read access to the **TripExt#EU** item returns a string with the TripExt Engineering Units.
 A read access to the **TripExt#HighEU** item returns the highest possible TripExt value.
 A read access to the **TripExt#LowEU** item returns the lowest possible TripExt value.

Table 4.3 – Output Channel items

| ItemID | Data Type | Access Type | Description |
|--|-------------|-------------|----------------------|
| PowerSupplyName.BoardXX.ChanYYY.Name | String | R/W | Channel name |
| PowerSupplyName.BoardXX.ChanYYY.V0Set | 4-byte real | R/W | Set V0 voltage limit |
| PowerSupplyName.BoardXX.ChanYYY.V0Set#EU | String | R | V0set EU |
| PowerSupplyName.BoardXX.ChanYYY.V0Set#HighEU | 8-byte real | R | V0set upper limit |
| PowerSupplyName.BoardXX.ChanYYY.V0Set#LowEU | 8-byte real | R | V0set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set | 4-byte real | R/W | Set V1 voltage limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#EU | String | R | V1set EU |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#HighEU | 8-byte real | R | V1set upper limit |
| PowerSupplyName.BoardXX.ChanYYY.V1Set#LowEU | 8-byte real | R | V1set lower limit |
| PowerSupplyName.BoardXX.ChanYYY.RUp | 4-byte real | R/W | Set ramp-up rate |
| PowerSupplyName.BoardXX.ChanYYY.RUp #EU | String | R | Ramp up rate EU |
| PowerSupplyName.BoardXX.ChanYYY.RUp #HighEU | 8-byte real | R | Rup upper limit |
| PowerSupplyName.BoardXX.ChanYYY.RUp #LowEU | 8-byte real | R | RUp lower limit |
| PowerSupplyName.BoardXX.ChanYYY.RDwn | 4-byte real | R/W | Set ramp-down rate |
| PowerSupplyName.BoardXX.ChanYYY.RDwn #EU | String | R | Ramp down rate EU |

| ItemID | Data Type | Access Type | Description |
|---|----------------|-------------|----------------------------|
| PowerSupplyName.BoardXX.ChanYYY.RDwn #HighEU | 8-byte real | R | RDwn upper limit |
| PowerSupplyName.BoardXX.ChanYYY.RDwn #LowEU | 8-byte real | R | RDwn lower limit |
| PowerSupplyName.BoardXX.ChanYYY.VMon | 4-byte real | R | VMon |
| PowerSupplyName.BoardXX.ChanYYY.VMon #EU | string | R | VMon EU |
| PowerSupplyName.BoardXX.ChanYYY.VMon#HighU | 8-byte real | R | VMon upper limit |
| PowerSupplyName.BoardXX.ChanYYY.VMon#LowEU | 8-byte real | R | VMon lower limit |
| PowerSupplyName.BoardXX.ChanYYY.Status | 2-byte integer | R | Channel status |
| PowerSupplyName.BoardXX.ChanYYY.Pw | boolean | R/W | Power ON/OFF |
| PowerSupplyName.BoardXX.ChanYYY.Pw#CoClose | string | R | Pw close label |
| PowerSupplyName.BoardXX.ChanYYY.Pw#CoOpen | string | R | Pw open label |
| PowerSupplyName.BoardXX.ChanYYY.PDwn | boolean | R/W | Power down options |
| PowerSupplyName.BoardXX.ChanYYY.PDwn#CoClose | string | R | PDwn close label |
| PowerSupplyName.BoardXX.ChanYYY.PDwn#CoOpen | string | R | PDwn open label |
| PowerSupplyName.BoardXX.ChanYYY.TripInt | 4-byte real | R/W | Set Internal trip time |
| PowerSupplyName.BoardXX.ChanYYY.TripInt #EU | string | R | Internal Trip time EU |
| PowerSupplyName.BoardXX.ChanYYY.TripInt#HighU | 8-byte real | R | Int. Trip time upper limit |
| PowerSupplyName.BoardXX.ChanYYY.TripInt#LowEU | 8-byte real | R | Int. Trip time lower limit |
| PowerSupplyName.BoardXX.ChanYYY.TripExt | 4-byte real | R/W | Set external trip time |
| PowerSupplyName.BoardXX.ChanYYY.TripExt #EU | string | R | External Trip time EU |
| PowerSupplyName.BoardXX.ChanYYY.TripExt#HighU | 8-byte real | R | Ext. Trip time upper limit |
| PowerSupplyName.BoardXX.ChanYYY.TripExt#LowEU | 8-byte real | R | Ext. Trip time lower limit |