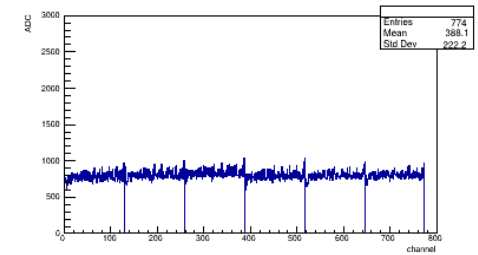
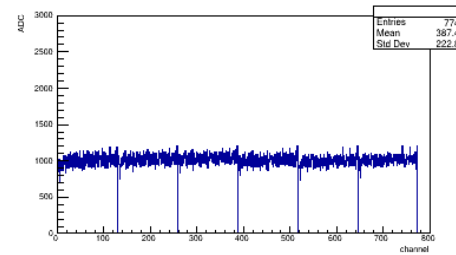
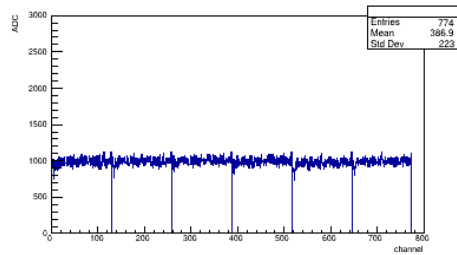
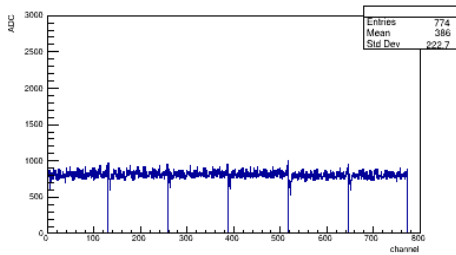
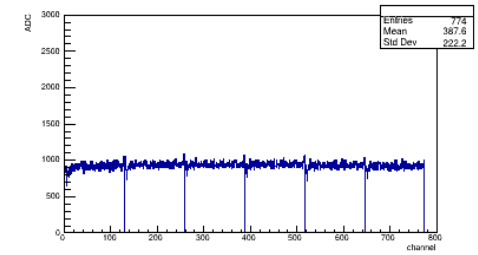
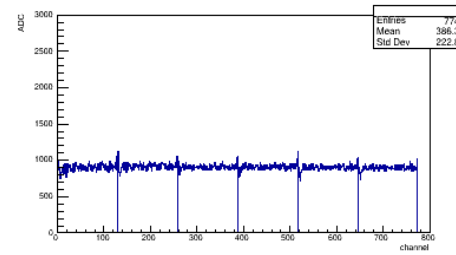
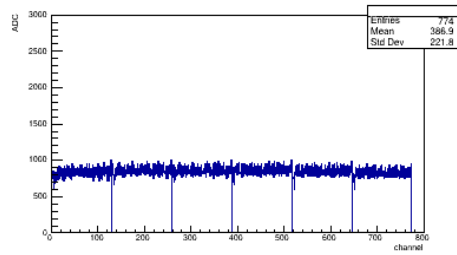
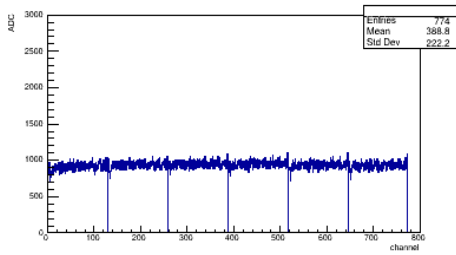
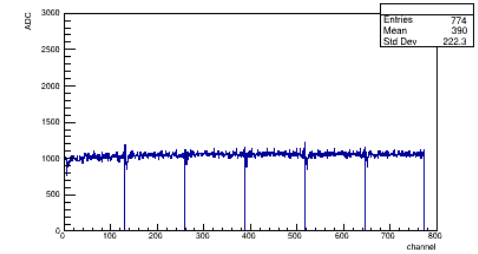
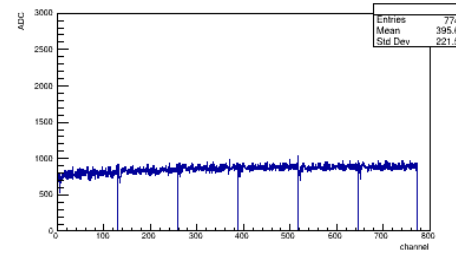
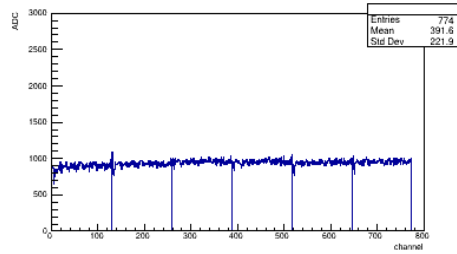
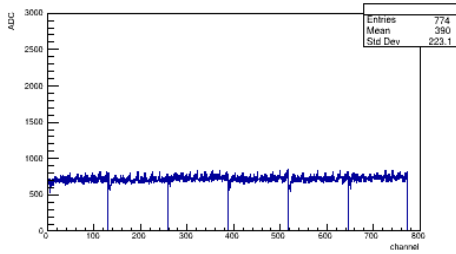


DAQ and Readout

- Readout all four layers on the cosmic stand
- Raw adc pedestals are looking promising except few concerns
- APV adc distributions for Layer 4 are all decent except one
- All faulty apvs will be tested again from tomorrow
- Plan is to start taking data by Friday

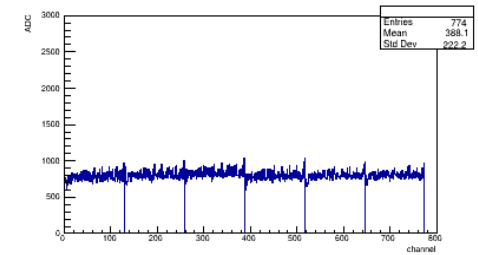
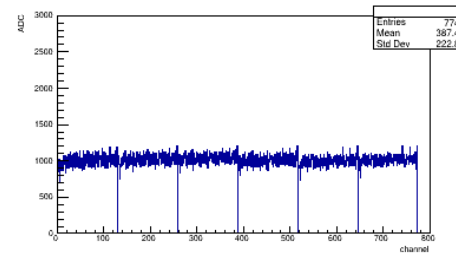
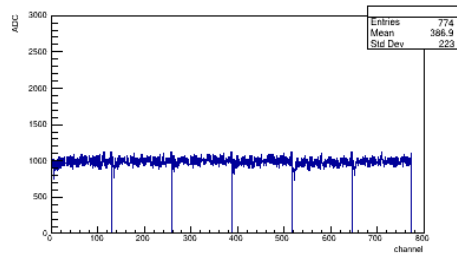
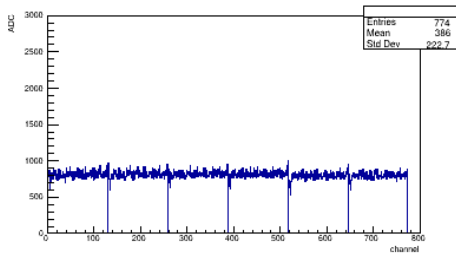
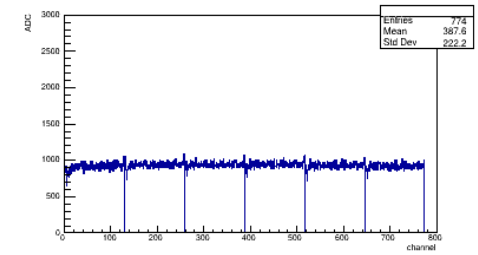
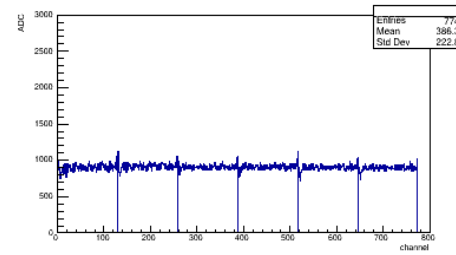
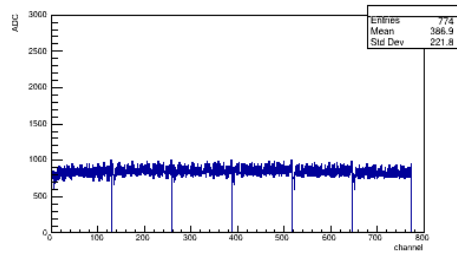
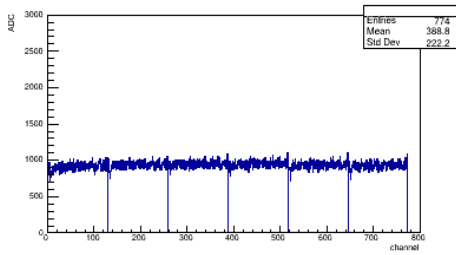
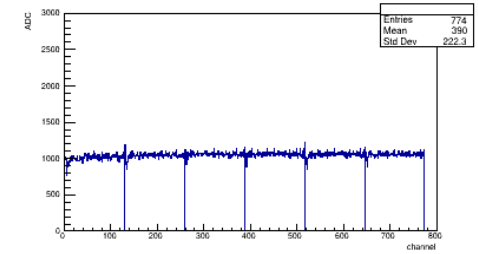
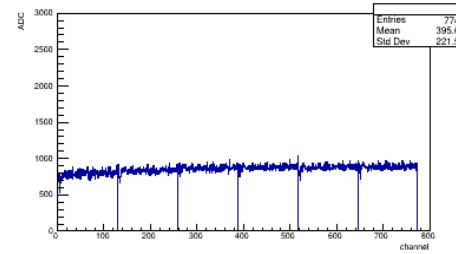
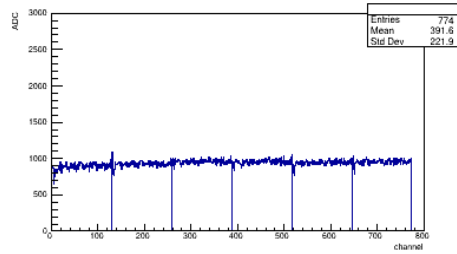
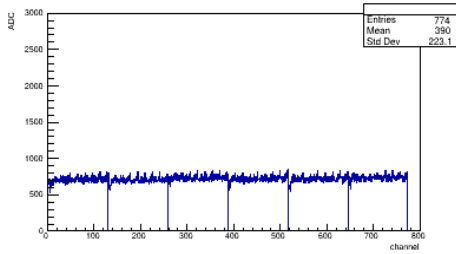
DAQ and Readout

Layer 4: 1st 12 slot back plane



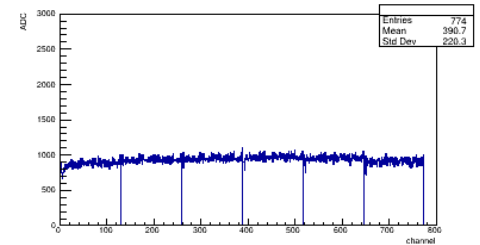
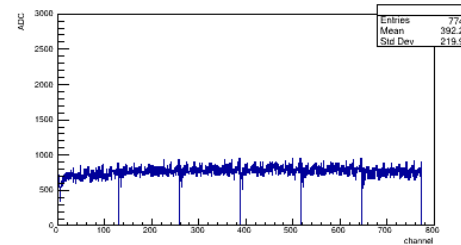
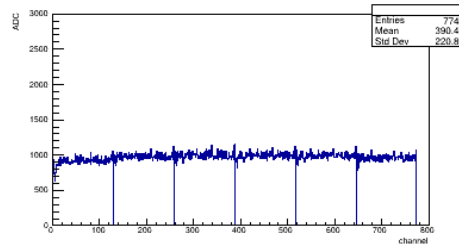
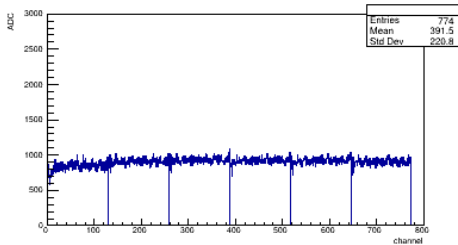
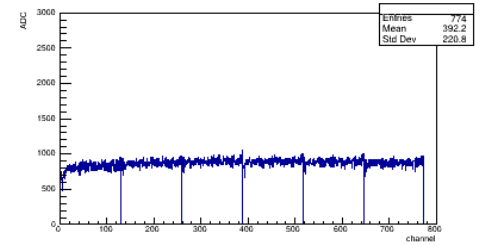
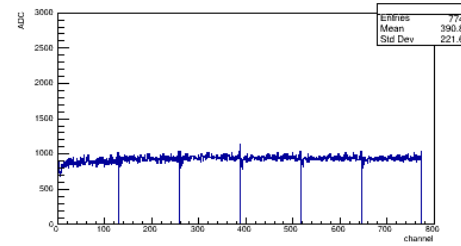
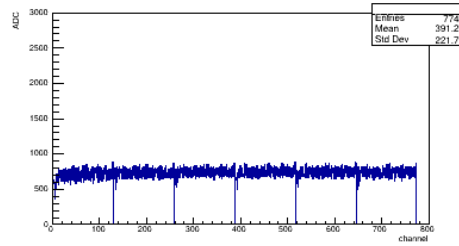
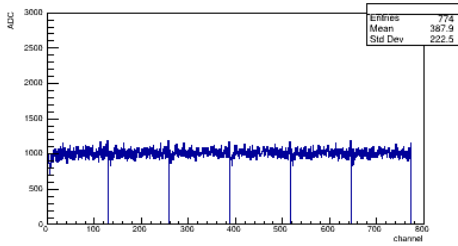
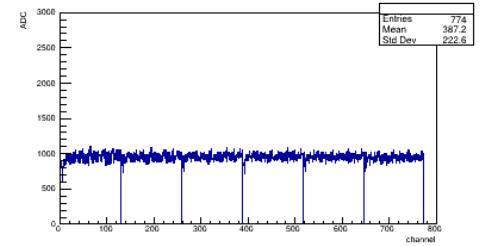
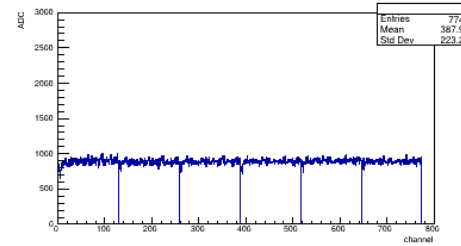
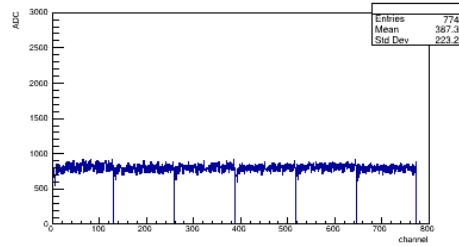
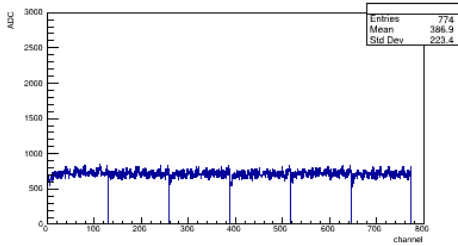
DAQ and Readout

Layer 4: 2nd 12 slot back plane



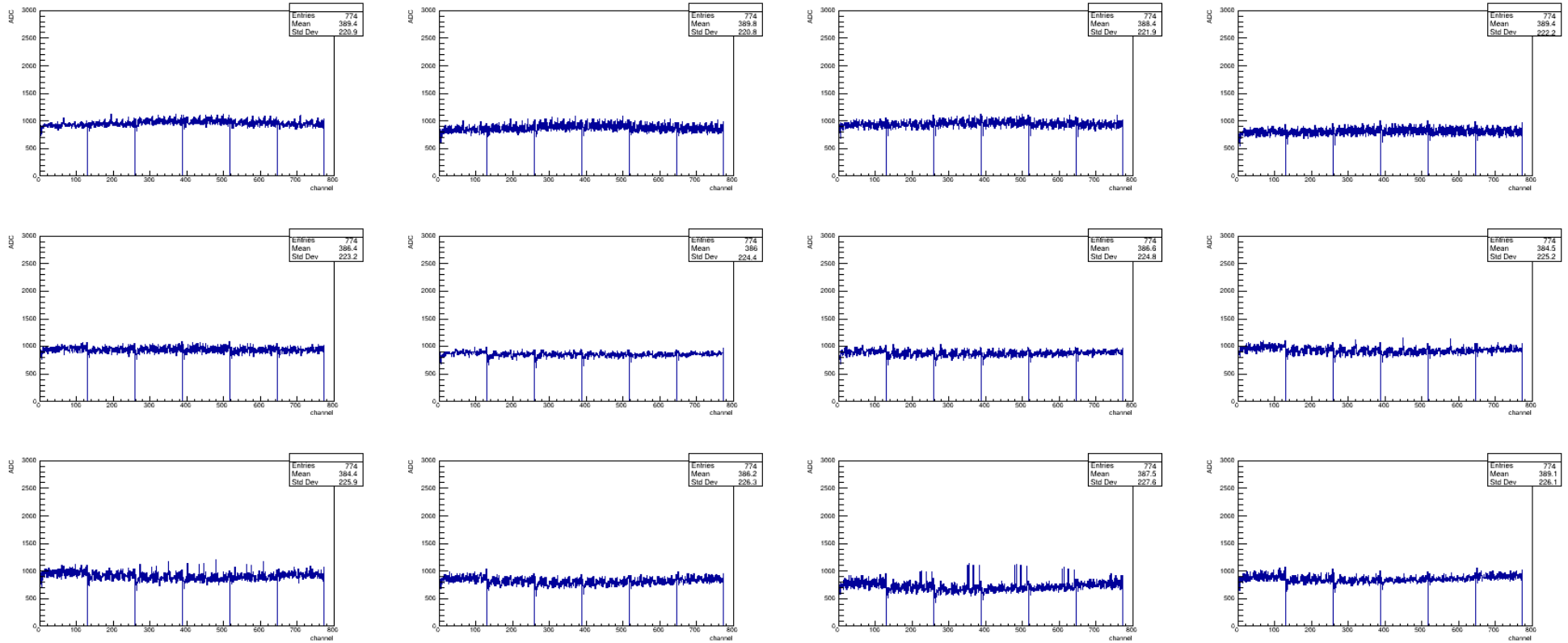
DAQ and Readout

Layer 4: 3rd 12 slot back plane



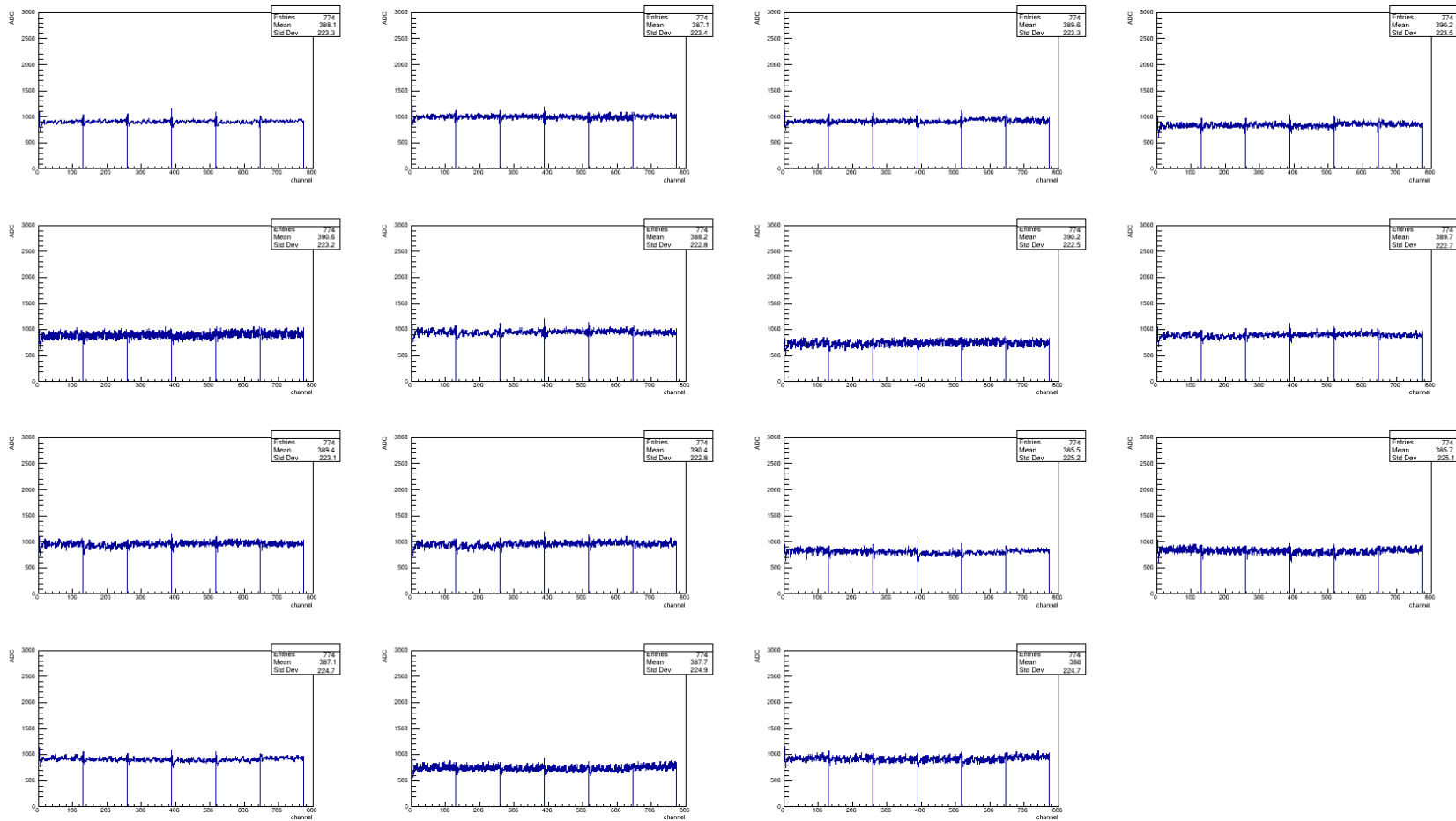
DAQ and Readout

Layer 4: 4rd 12 slot back plane



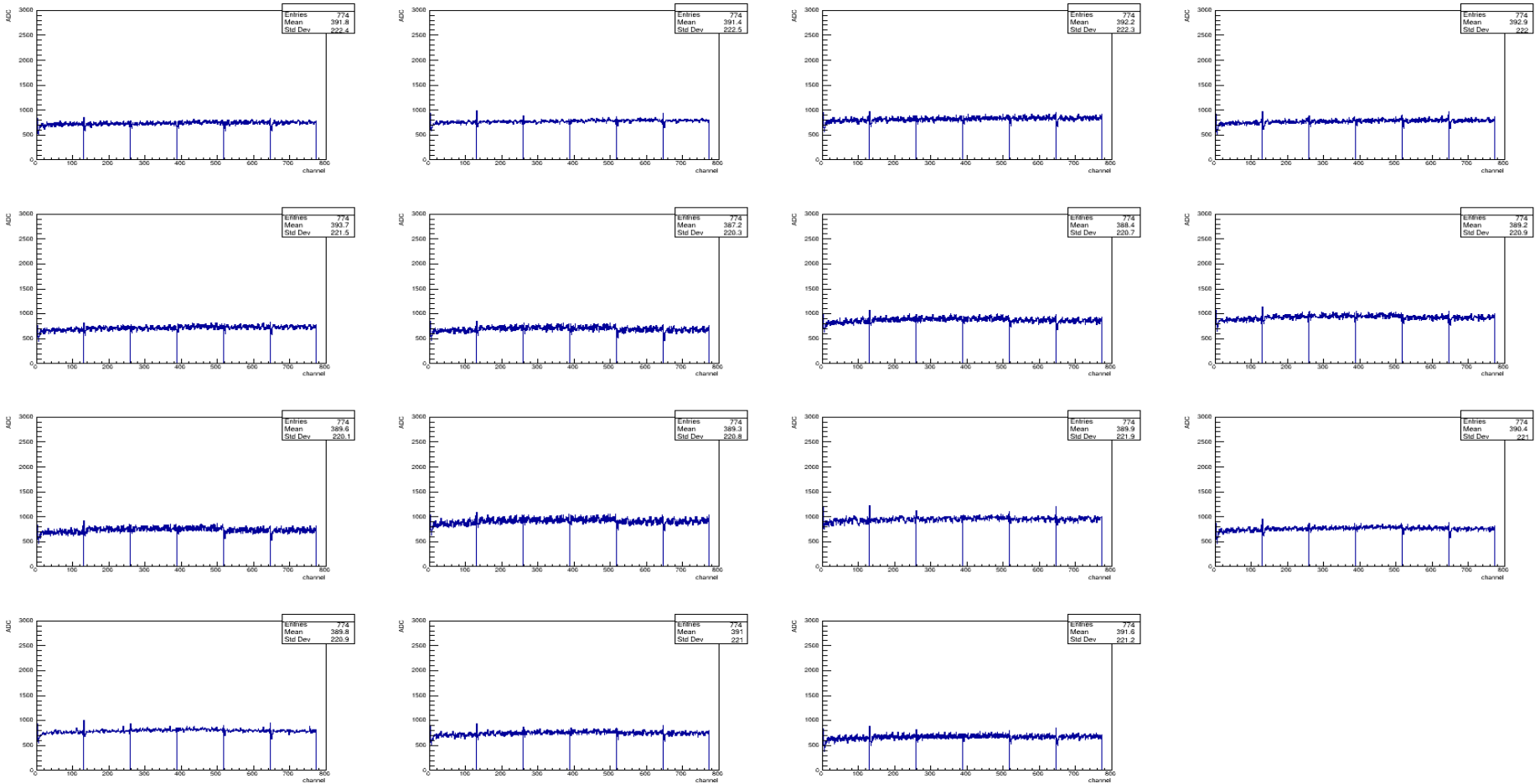
DAQ and Readout

Layer 4: 1st 3x5 slot back plane



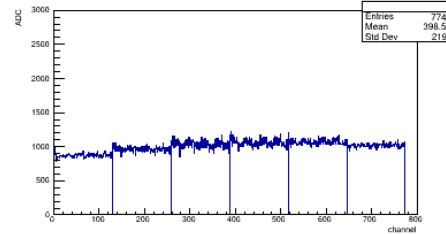
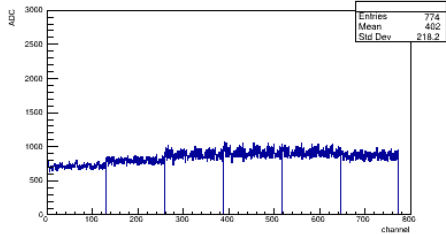
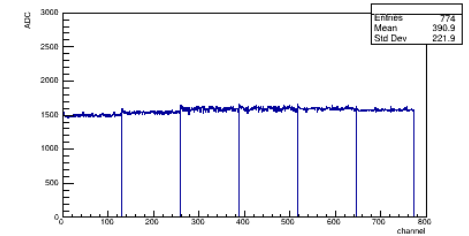
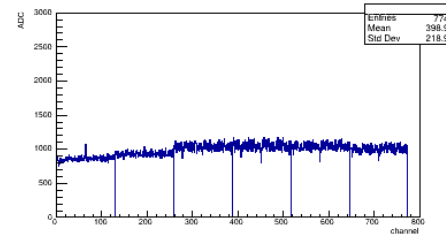
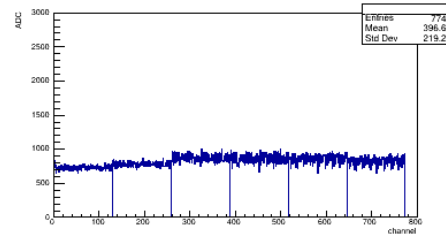
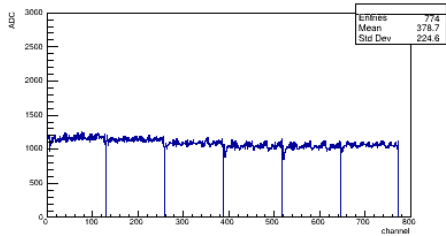
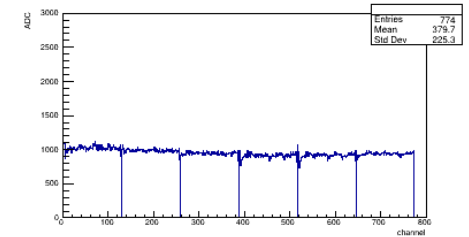
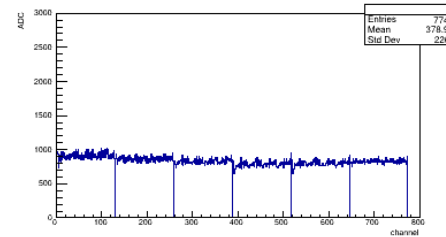
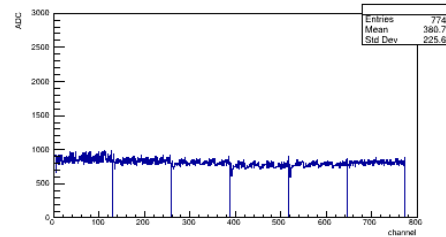
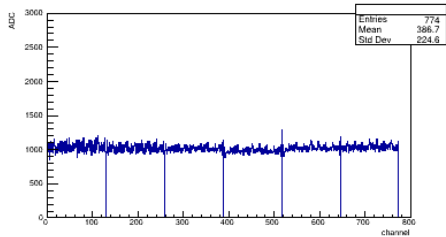
DAQ and Readout

Layer 4: 2nd 3x5 slot back plane



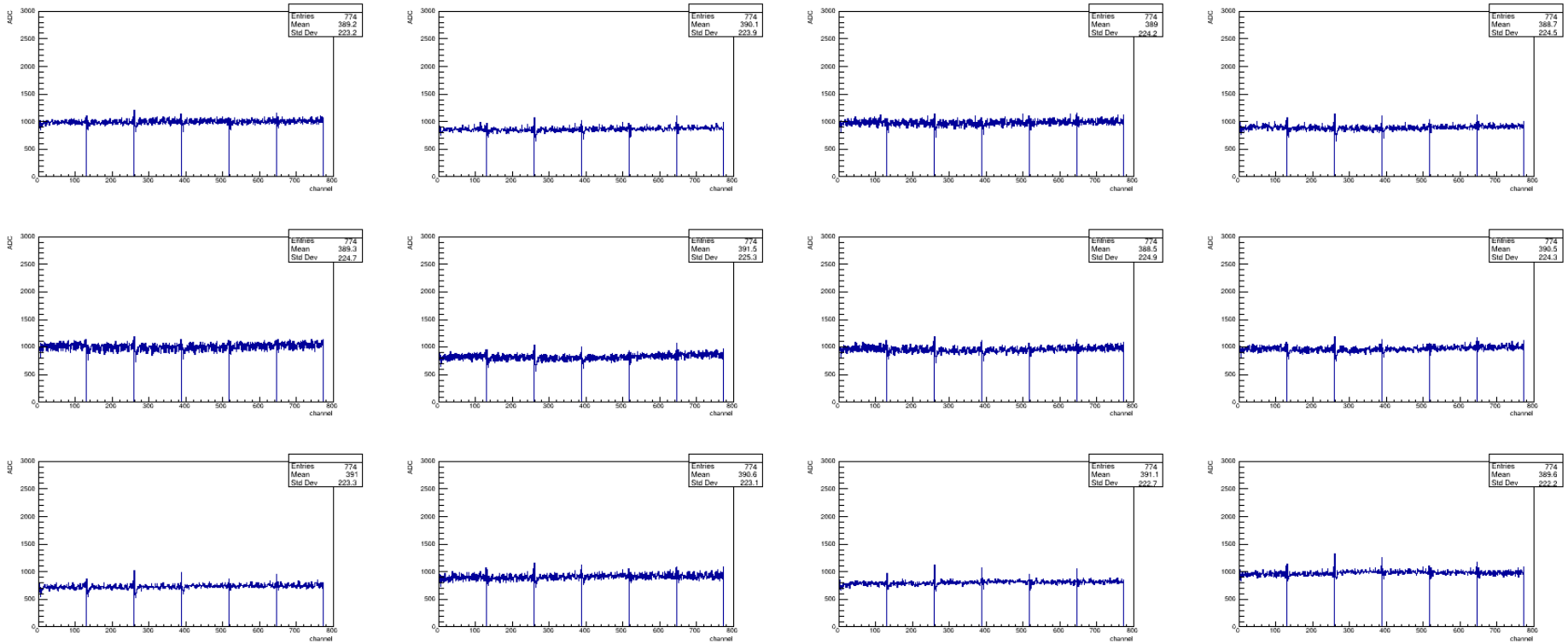
DAQ and Readout

Layer 4: 2x5 slot back plane



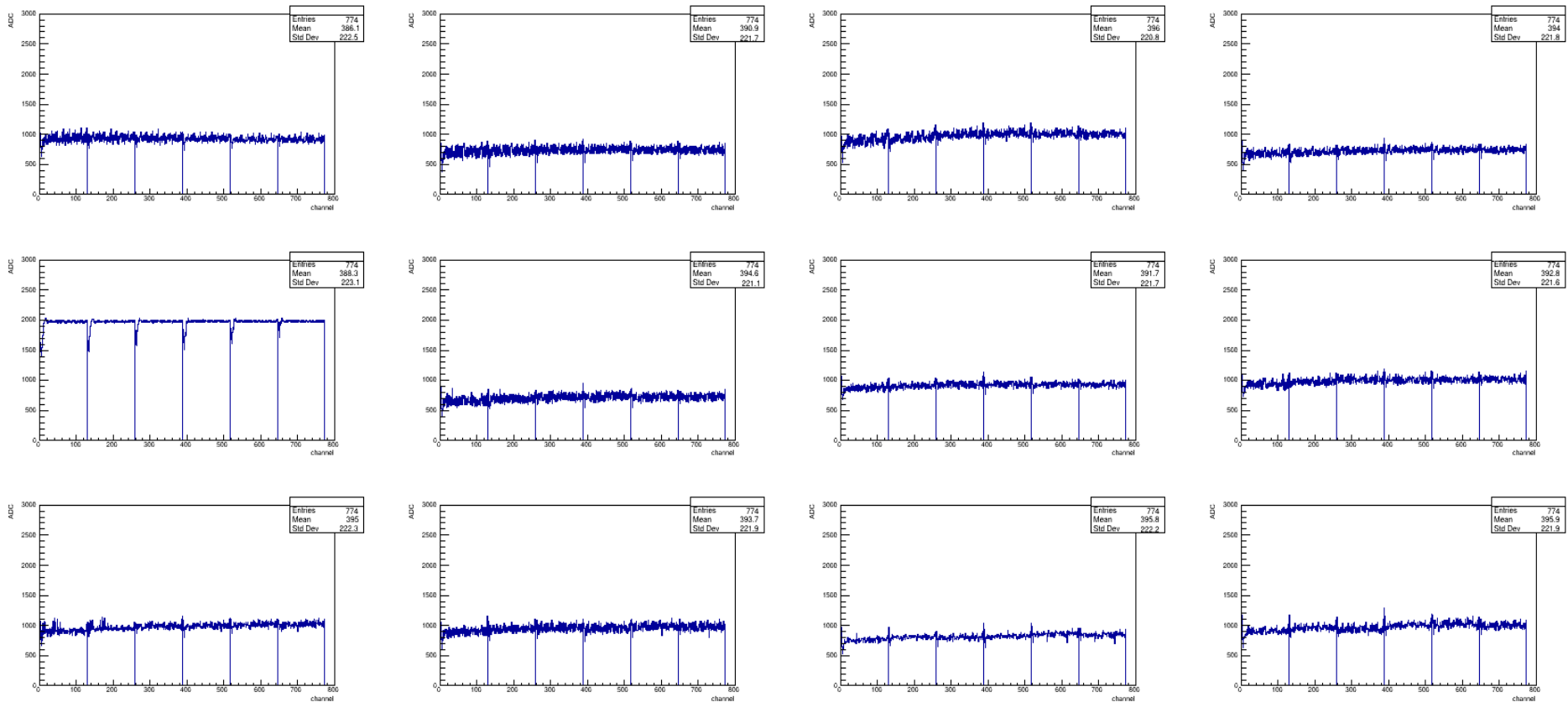
DAQ and Readout

Layer 1: 1st 12 slot back plane



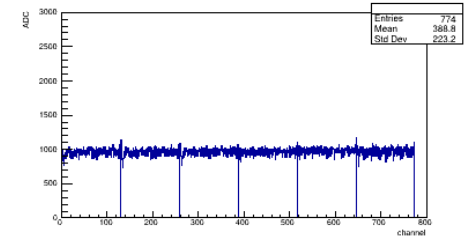
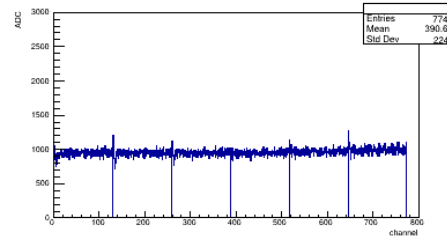
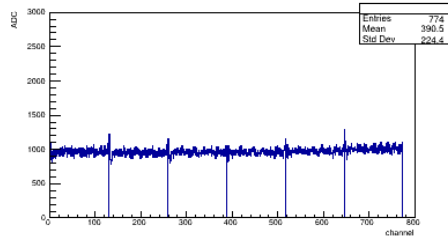
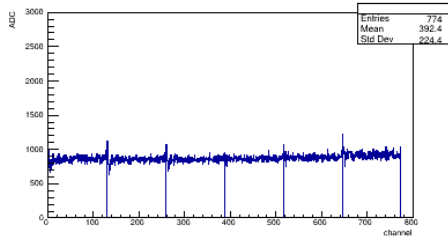
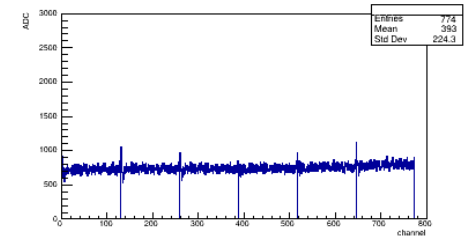
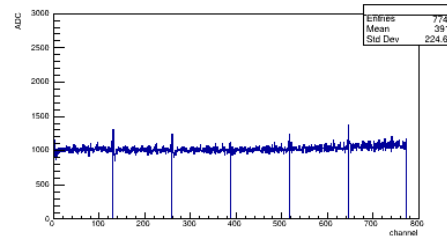
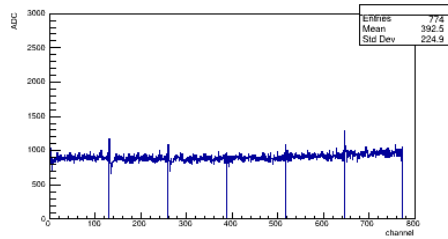
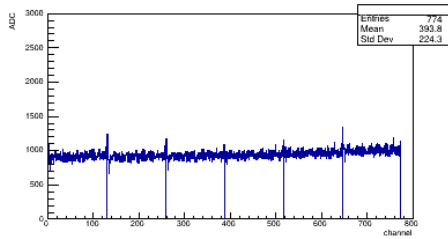
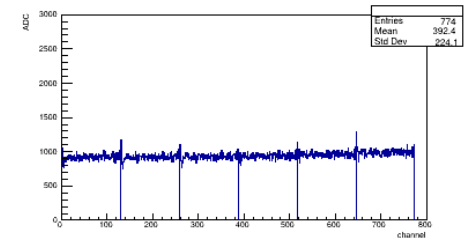
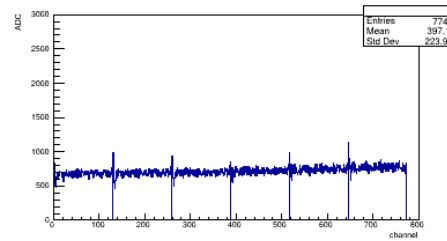
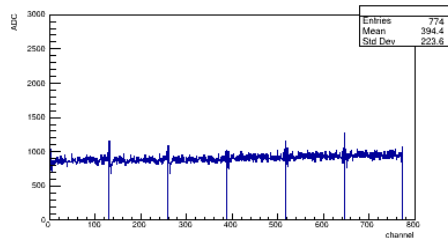
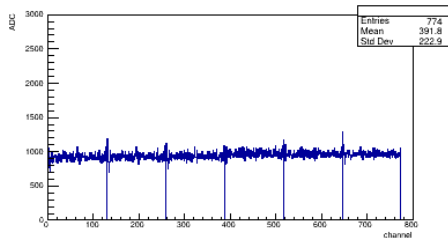
DAQ and Readout

Layer 1: 2nd 12 slot back plane



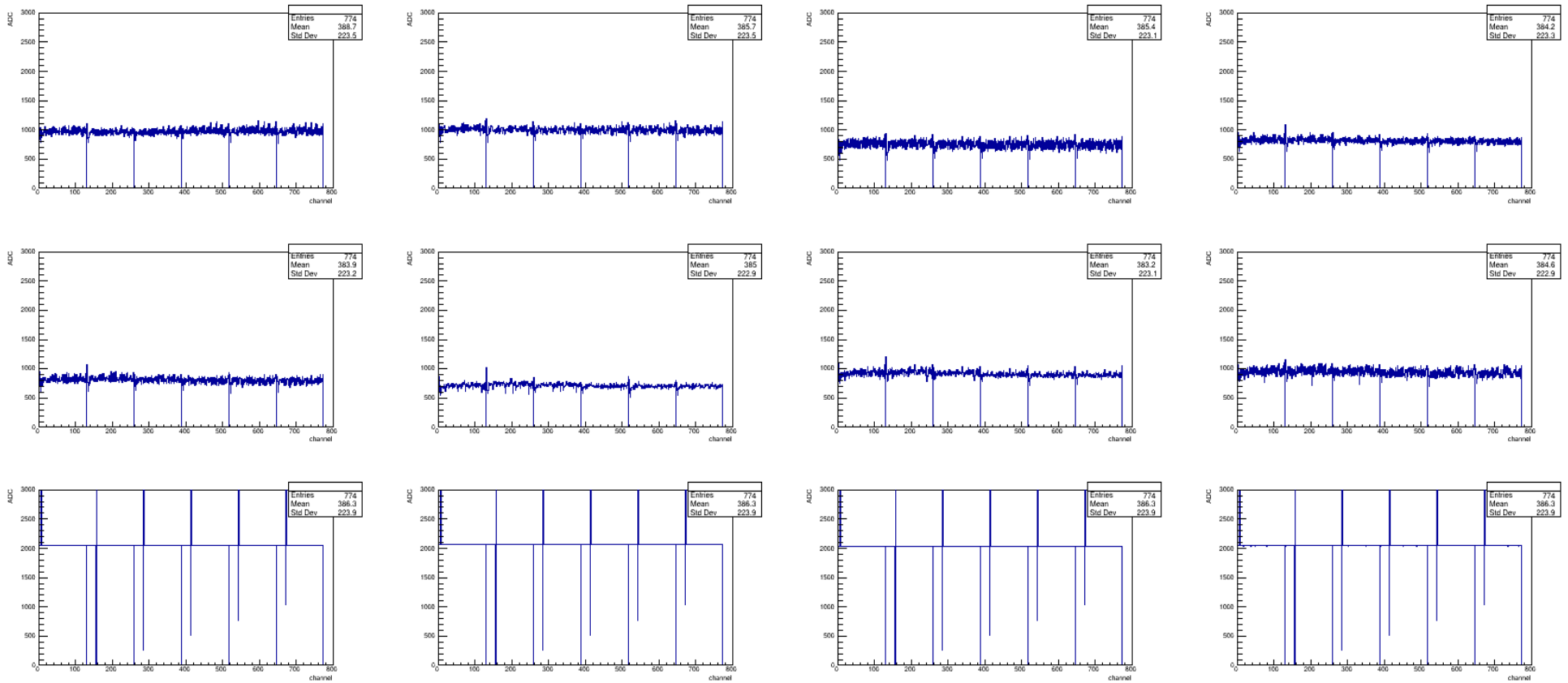
DAQ and Readout

Layer 1: 3rd 12 slot back plane



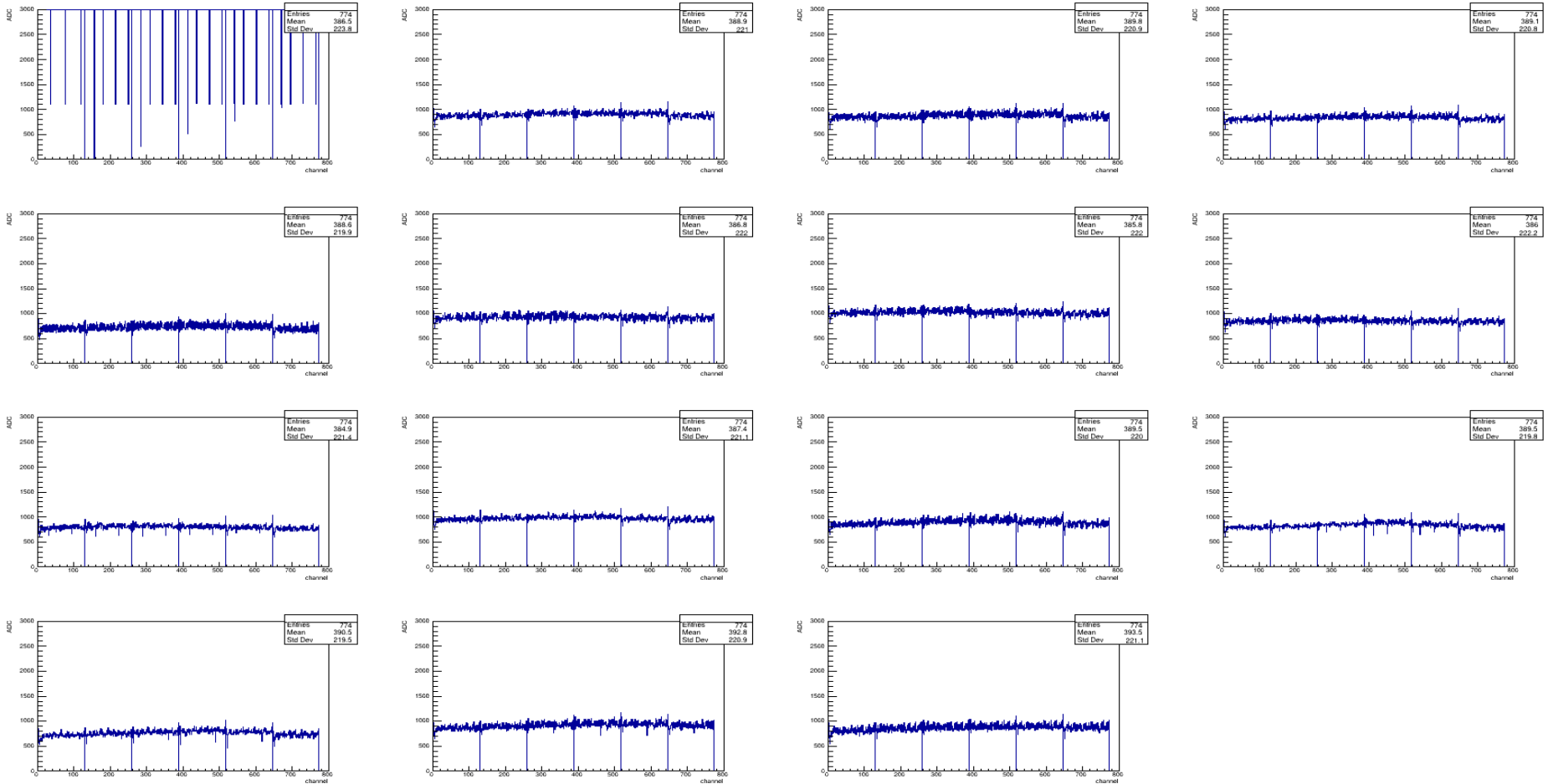
DAQ and Readout

Layer 1: 4th 12 slot back plane



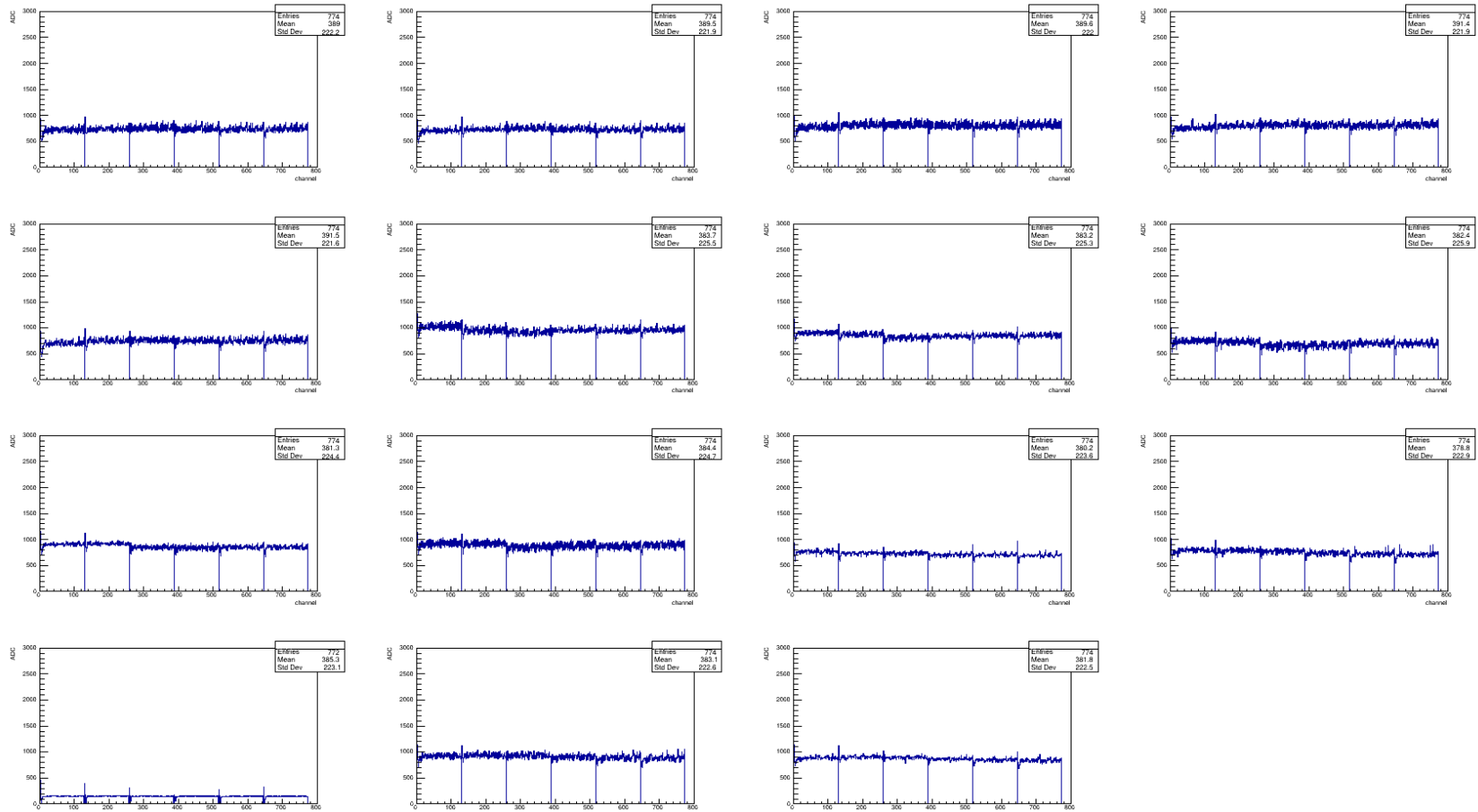
DAQ and Readout

Layer 1: 1st 3x5 slot back plane



DAQ and Readout

Layer 1: 2nd 3x5 slot back plane



DAQ and Readout

Layer 1: 2nd 3x5 slot back plane

