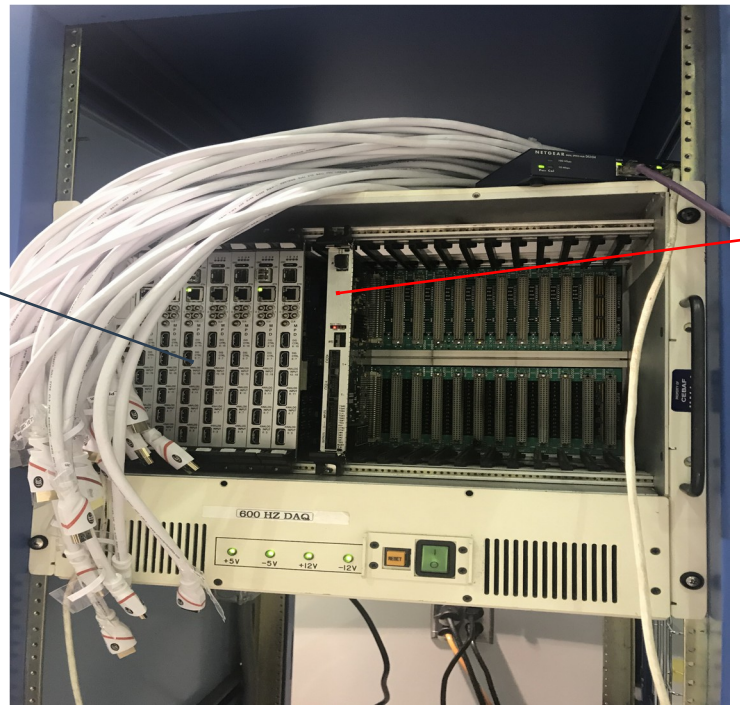
# DAQ setup

→ Installed VME controller (its hostname is hallavme14pc)

→ Installed 7 MPD's and the address switch set to the slot number

→ TI address is set for slot 10




**VME crate**

# DAQ setup

→ Programmed firmware of all MPDs before being attached to the crate

→ Copied programs and libraries we need to run CODA 3.10

This is a version of CODA that is in current development as opposed to the older version currently used in Hall A (CODA 2.6.2)

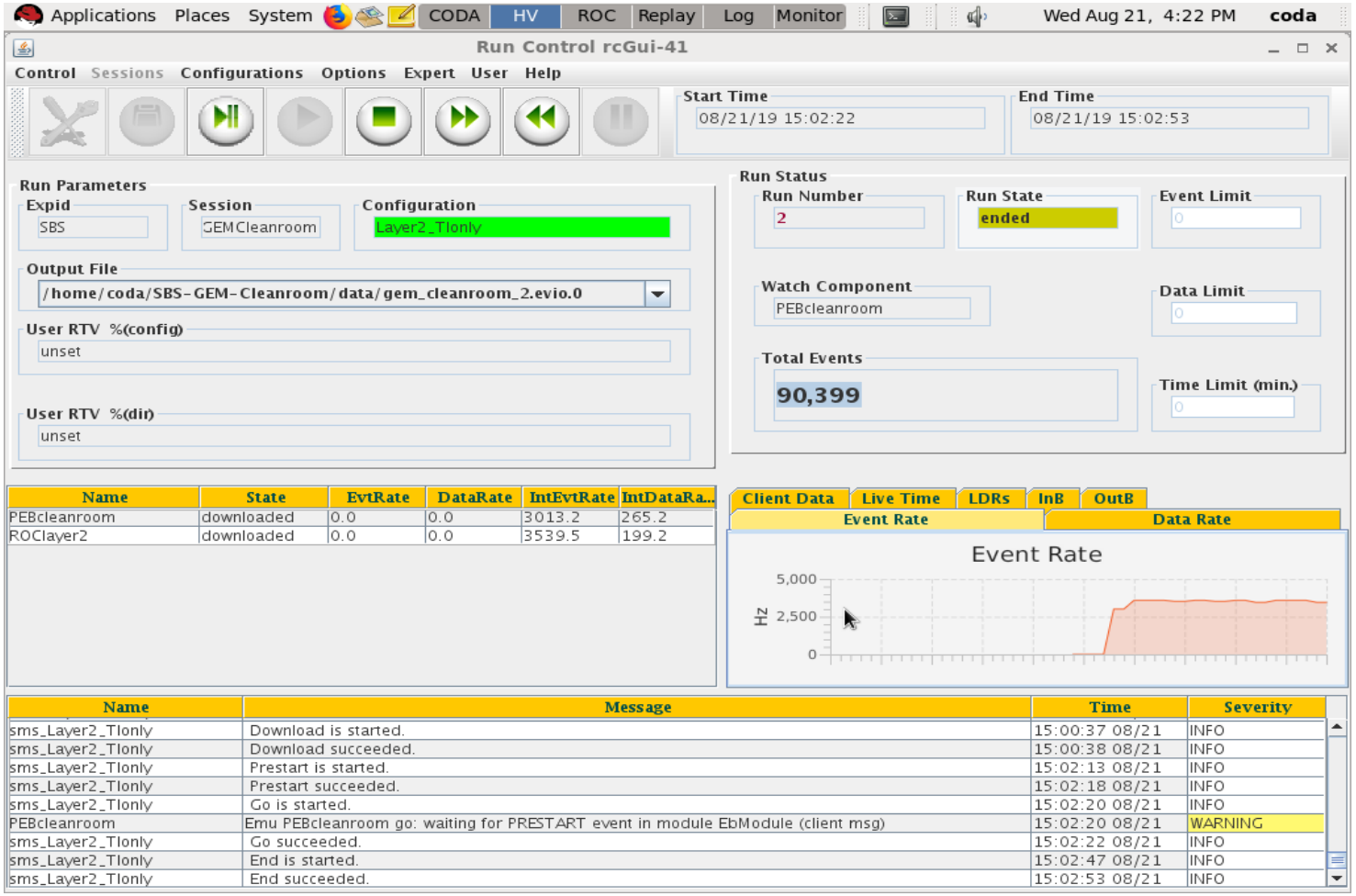
Detail information are posted in wiki:
SBS GEM EEL Cleanroom setup

→ Checked the communication between APV and MPD

→ Configured CODA to read TI and tested

# DAQ setup

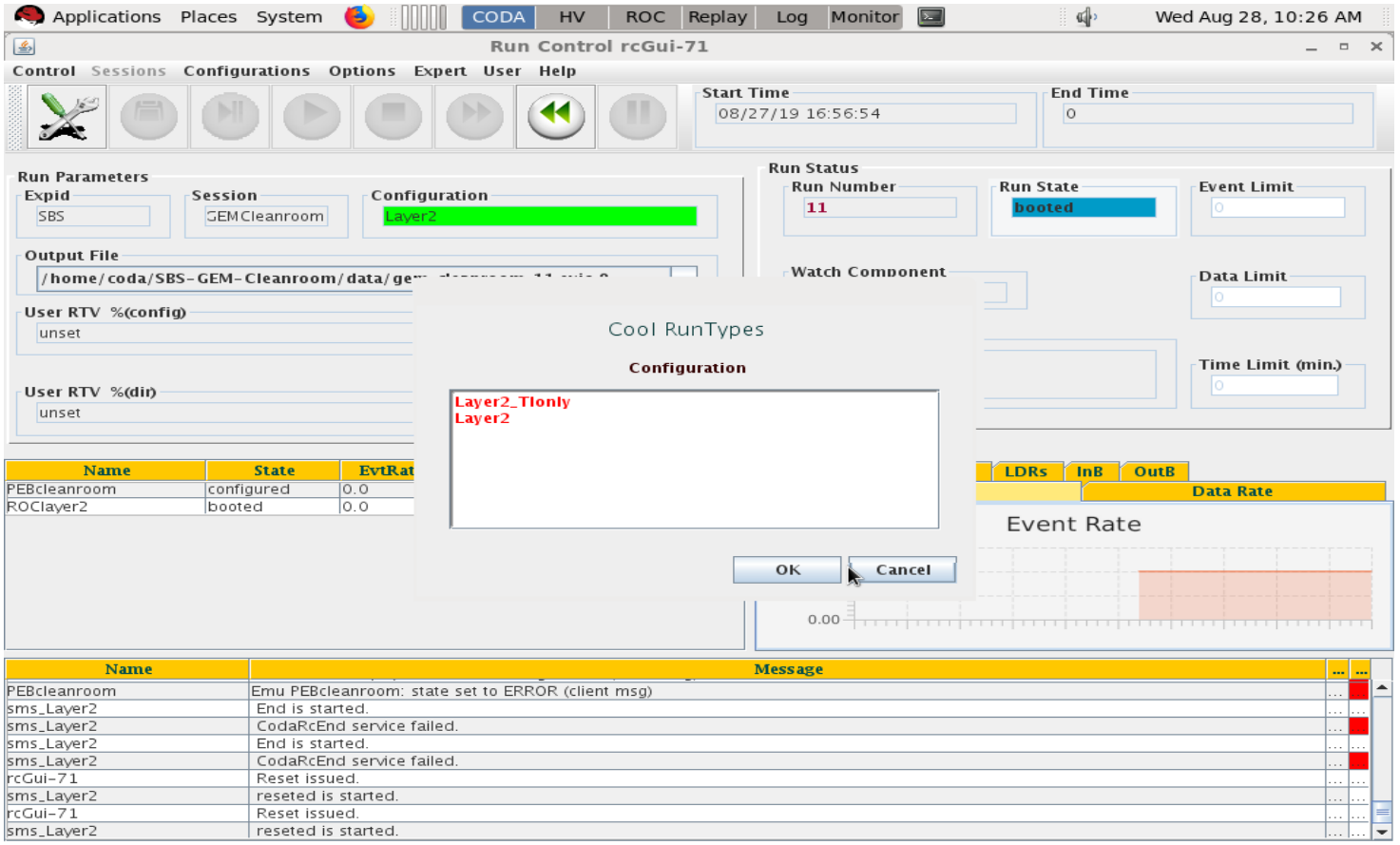
## New Run Control GUI



# DAQ setup

## → Added two configuration

Layer2_Tionly → Configure TI
Layer2 → Configure MPDs



# DAQ setup

→ Found bug in TI firmware 3v9.2 (relates to using the front panel inputs for triggers). Downgraded both TI firmware and library to 3v8.1.

→ Using gate generator as a slow pulser trigger (~few Hz) for debugging.

I2C (MPD -> APVs config)

→ Debugging I2C. Will need to be less strict with i2c to get other MPDs to work.

→ Library development ongoing.

→ Splitting of configuration files from a couple files: config_apv.txt and config_apv_default.txt

→ config_apv.txt now reads multiple textiles prepared for the individual MPD slots

# DAQ setup

**Next steps:**

→ Full Readout of layer2 without SSP

→ Debug decoding of CODA 3.10 data with Analyzer.

→ Add more layers to readout (more crates, more ROCs, use SSP).

→ Consider using VME Controller in MPD crates to configure the MPDs and then use SSP to read them out.