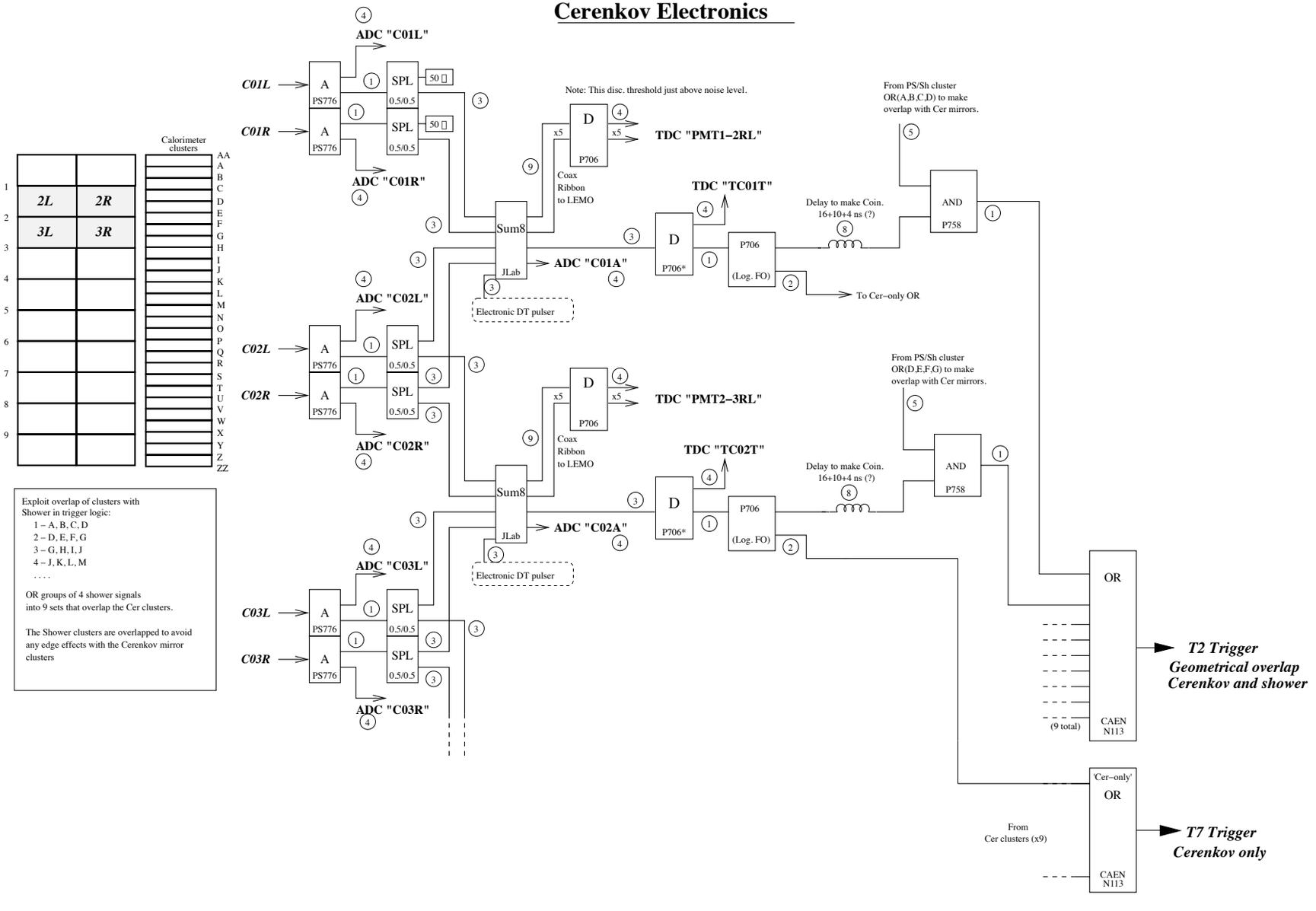
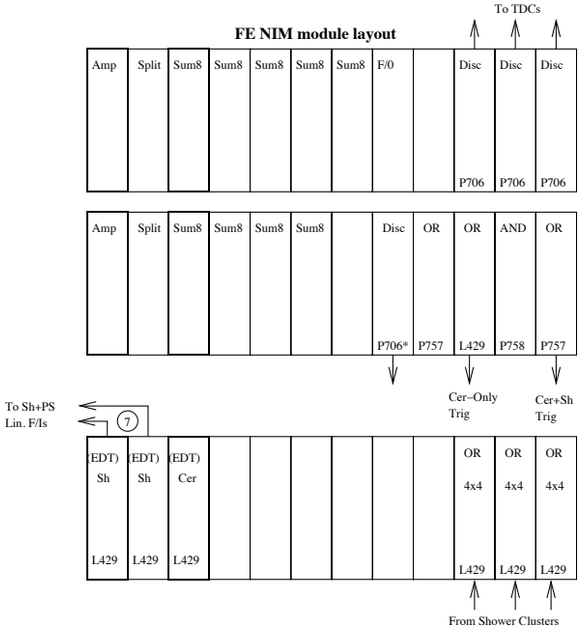
Cerenkov Electronics



FE NIM module layout



Group	Length	amount
1	1ns	38
2	2ns	9
3	4ns	54
4	8ns	83
5	16ns	9
7	16ns	26
8	32ns ?	9
9		9

FE Patch Panel to ADC/TDC

	250ns	
9	SUM TDC out —> Weldment —> NIM–ECL —> TDC input	
45	PMT TDC out —> Weldment —> NIM–ECL —> TDC input	
	250ns	550ns
20	SUM ADC out —> Weldment —> Delay —> ADC input	
9	PMT ADC out —> Weldment —> Delay —> ADC input	