

Summary of last week activities

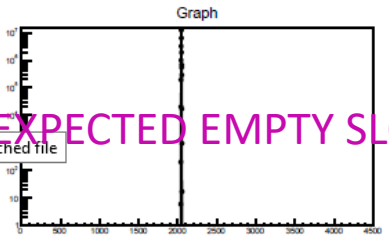
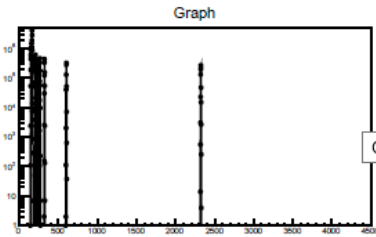
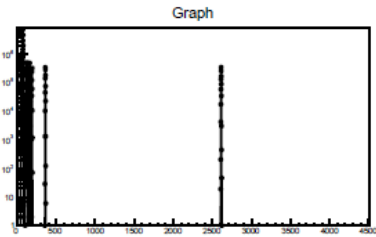
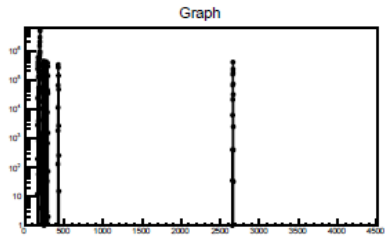
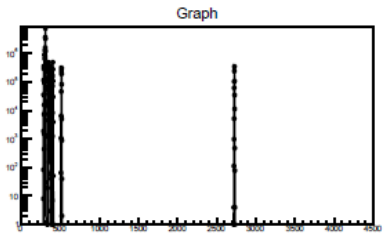
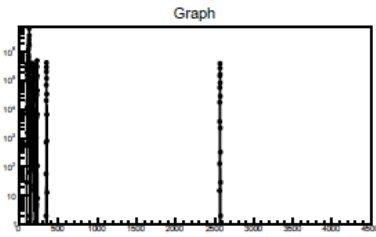
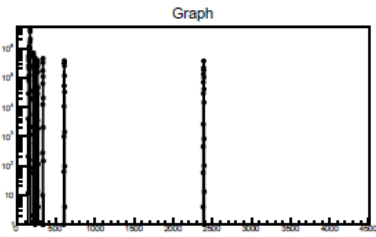
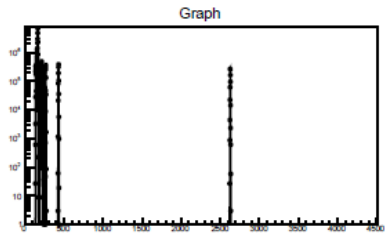
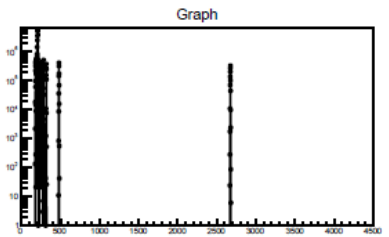
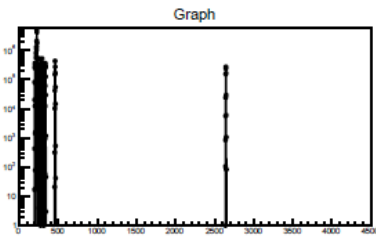
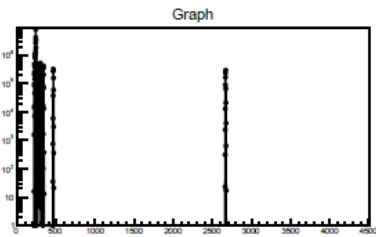
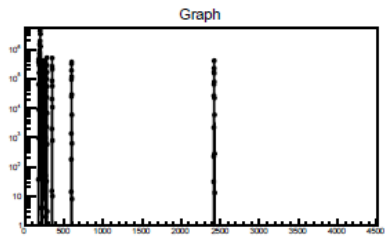
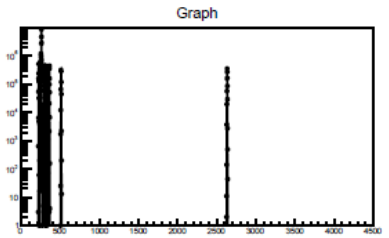
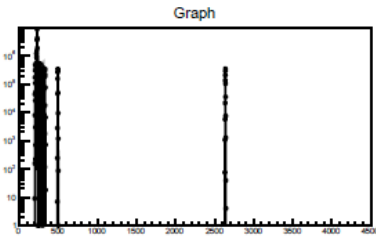
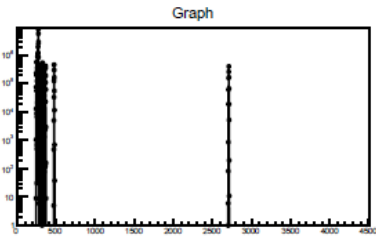
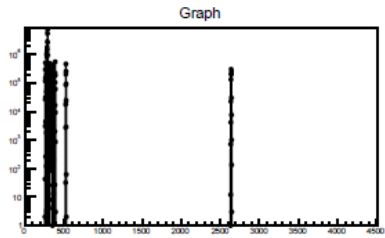
Changes in the cabling since our meeting last Tuesday.

- Replaced all the patch panels for 4 slots back planes by the HDMI adapters
- This release enough long HDMI's to equip all the MPDs without mixing with short cables.
- Cable arrangement at the DAQ crate level (Thanks to Bogdan / Albert)
- Update the mapping & configuration file.
- Run the configuration and histo tests that Evaristo suggested

Currently 12 MPDs on the VME crates to readout all 3 chambers

- Still have 3 MPDs on chamber J0 that are clearly not working (MPD5, MPD6, MPD7). Evaristo suggests one possible cause of the problem, MPD4 is also connected to J0 but is working/configured perfectly).
- 2 APV cards (see red circle MPD8 and MPD13)
- A problem with connection from Patch Panel to the MPD12 (long HDMI) but not on the back planes side because it affects only 4 ADC channels in the row and back planes have 5-slots \Rightarrow Long HDMI or connection or configuration. It should be pretty easy to track and fix
- MPD9: Similar issue for MPD15, but here we can not discount the backplane as potential source
- Inversion of the clock phase pick position for two back planes (here it is clearly a configuration issue, I have to check but probably the wrong phase or just a bit inversion, so a non issue in my view

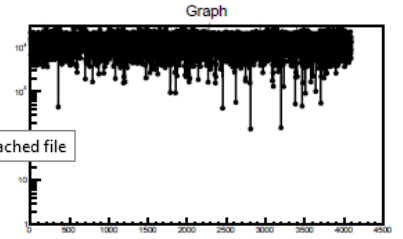
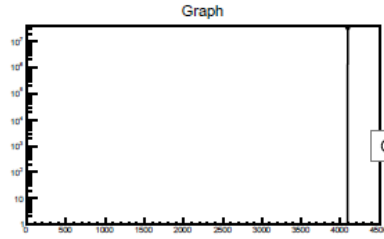
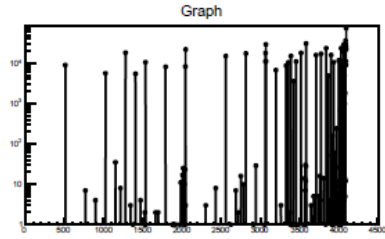
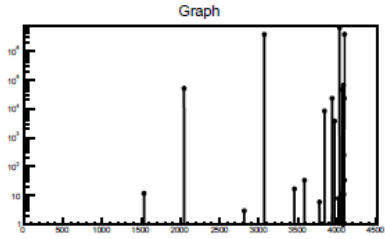
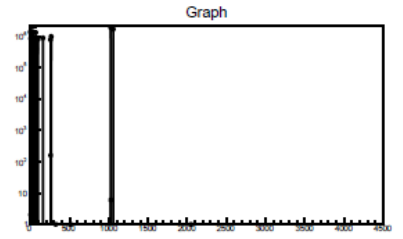
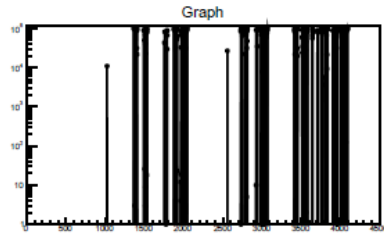
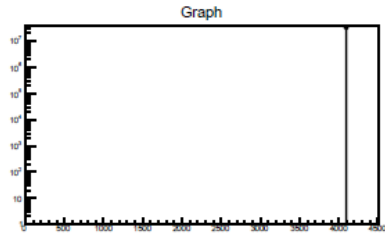
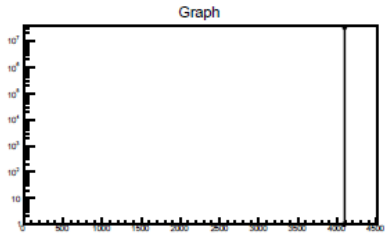
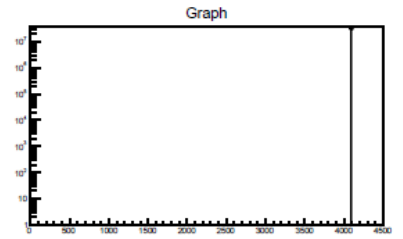
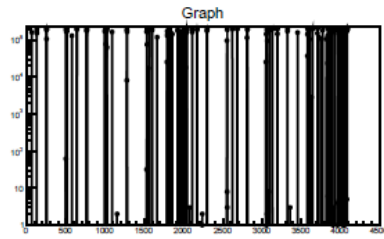
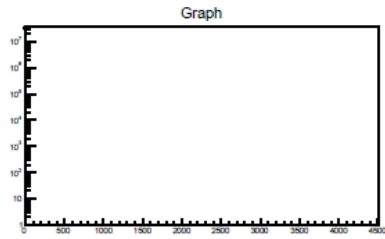
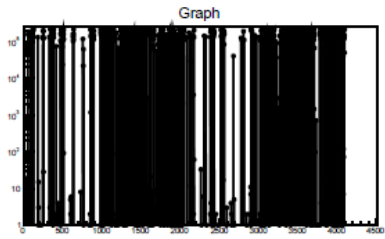
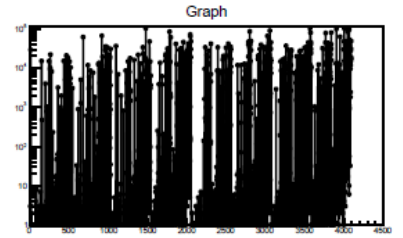
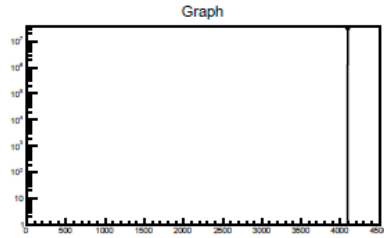
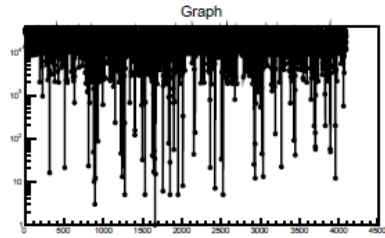
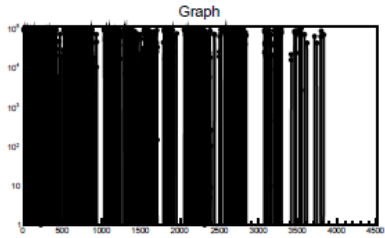
MPD4 [3 × 5-slots BP: short to long HDMIs vi 3-to-4 Patch Panel]



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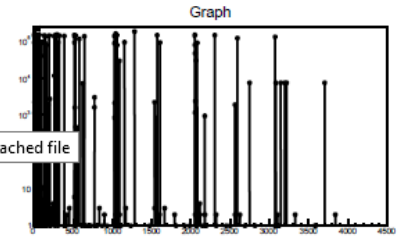
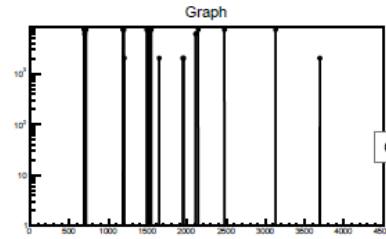
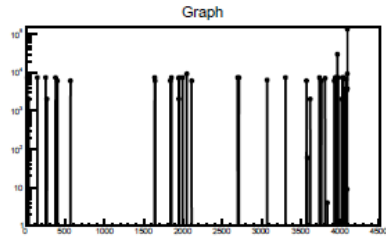
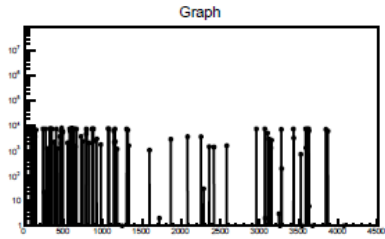
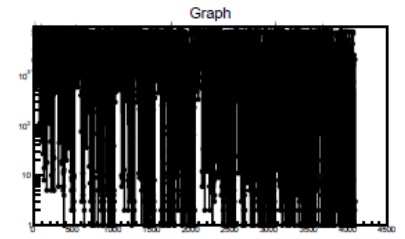
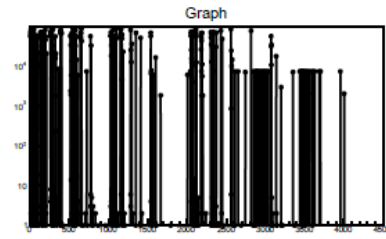
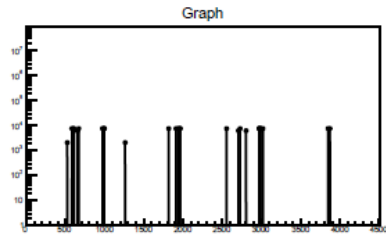
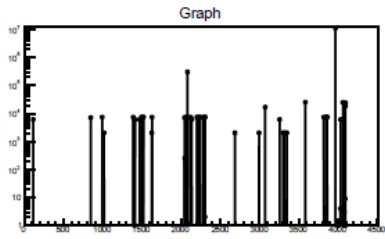
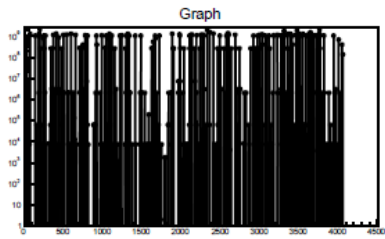
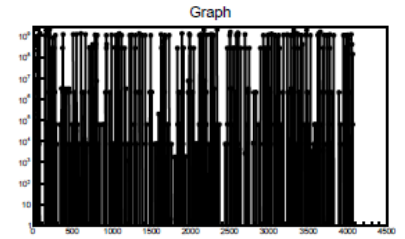
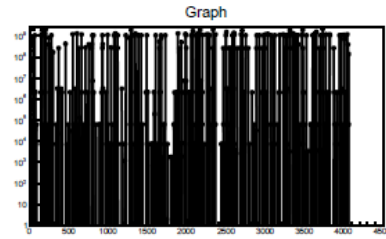
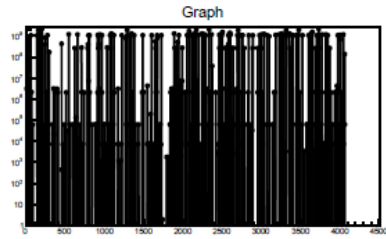
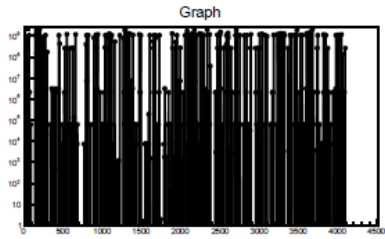
EXPECTED EMPTY SLOT

MPD5 [3 × 4-slots BP short to long HDMIIs via adapter]



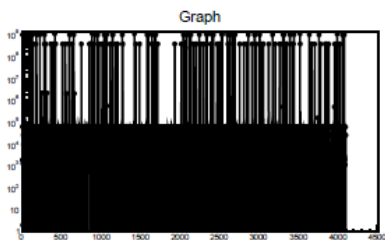
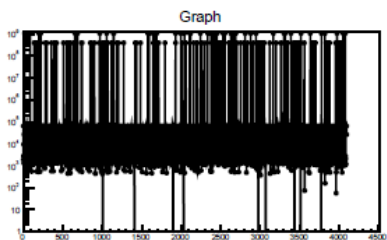
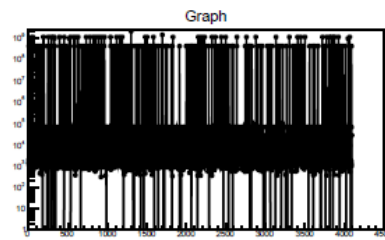
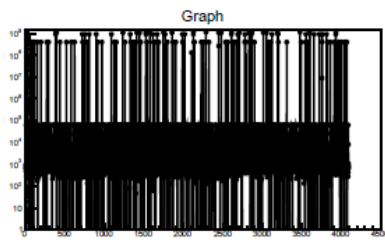
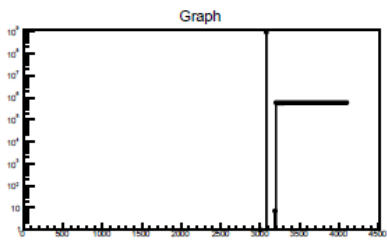
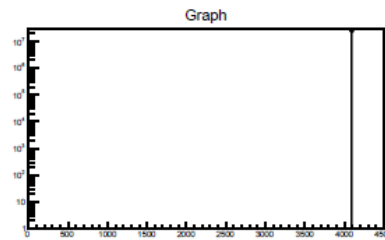
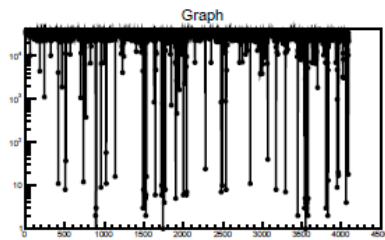
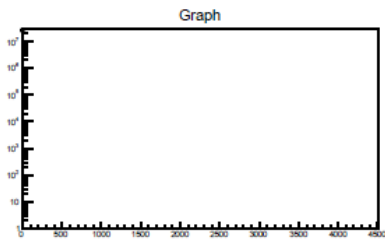
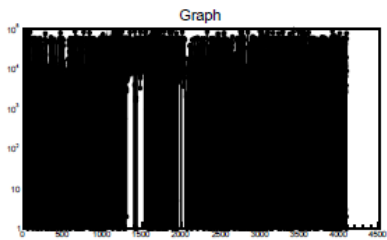
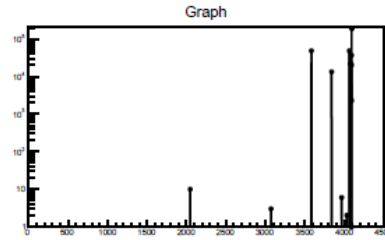
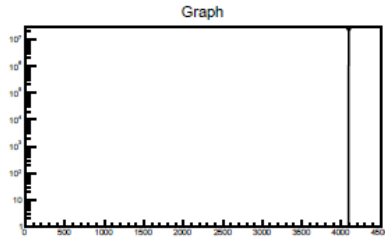
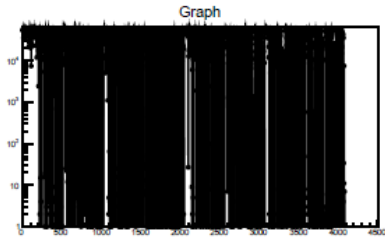
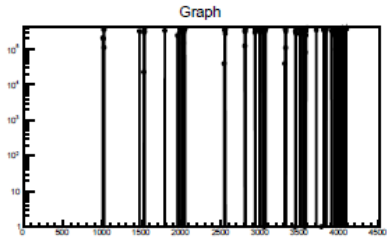
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MPD6 [3 × 5-slots BP: short to long HDMI vi 3-to-4 Patch Panel]



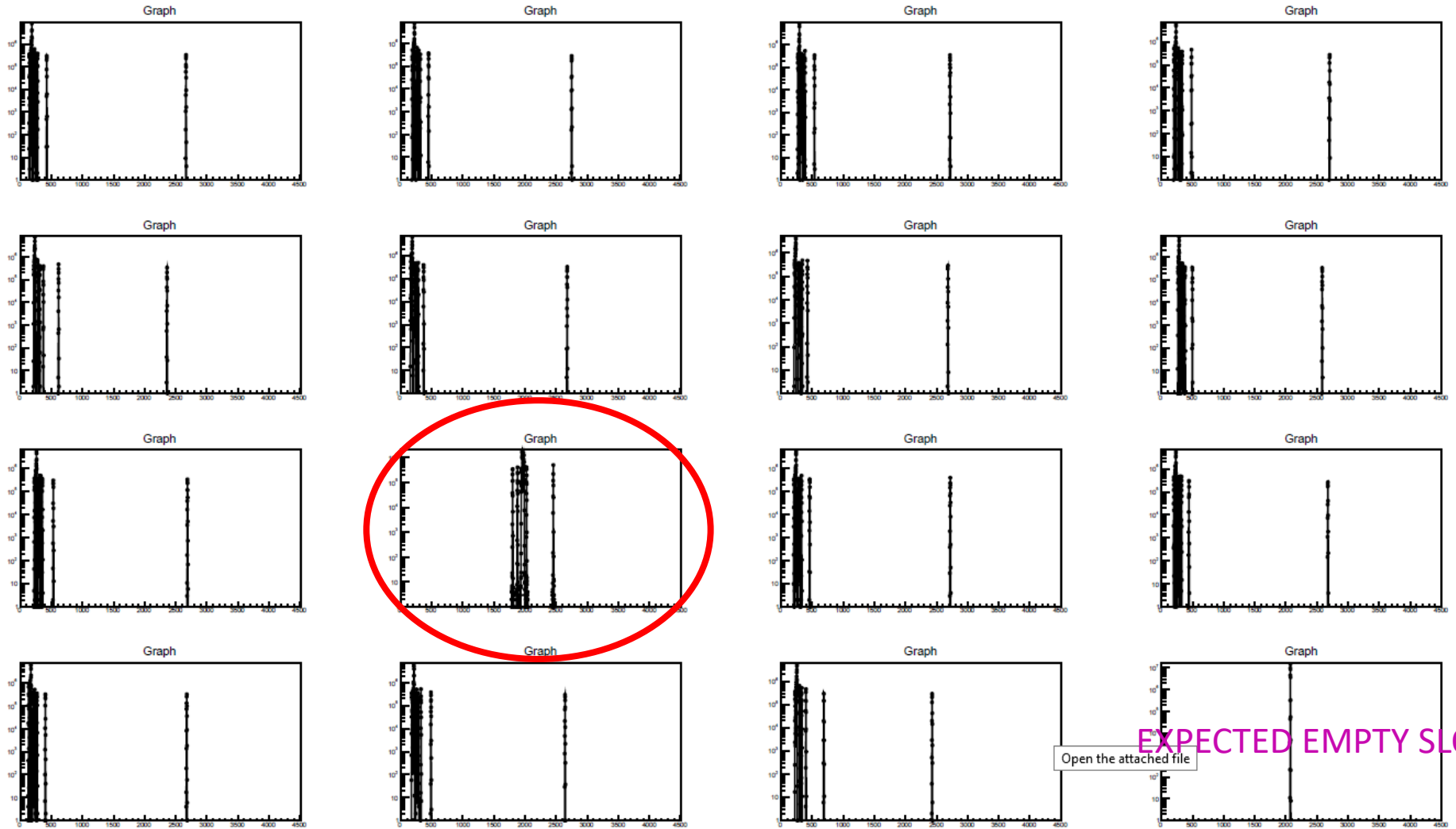
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MPD7 [3 × 4-slots BP short to long HDMI's via adapter]



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MPD8 [3 × 5-slots BP: short to long HDMIIs vi 3-to-4 Patch Panel]

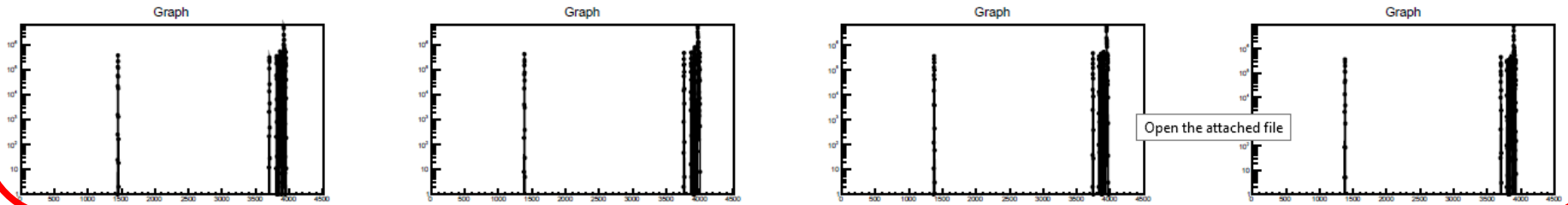
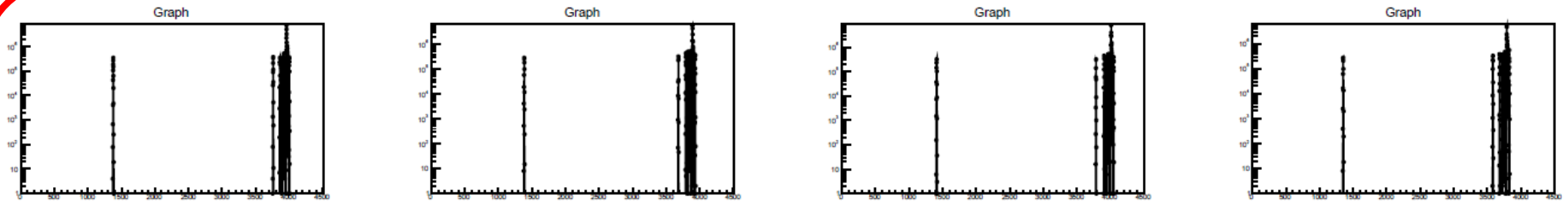
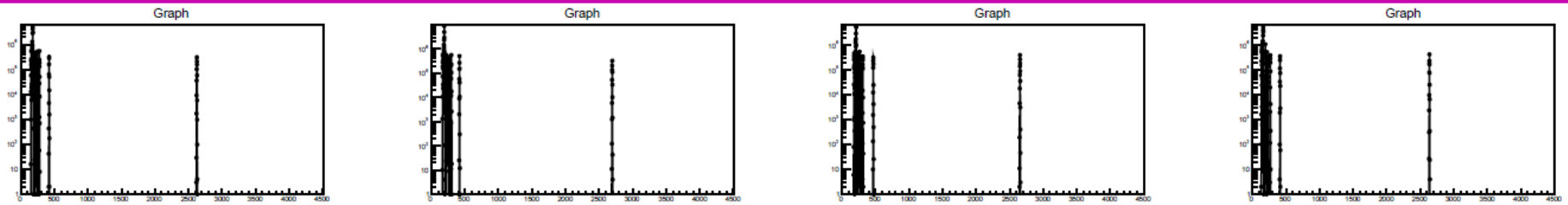
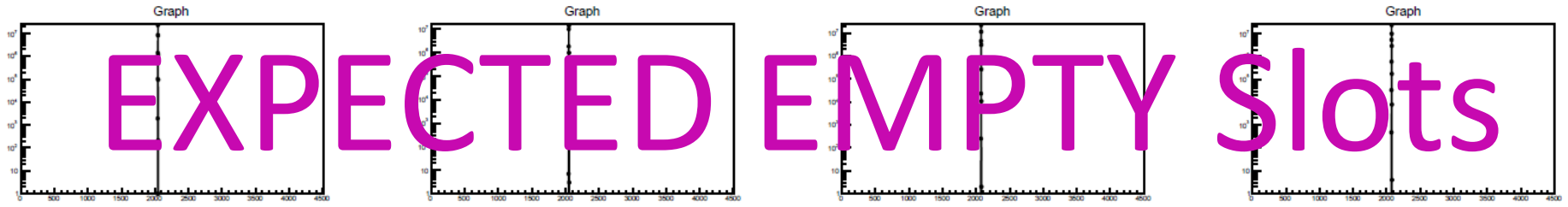


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EXPECTED EMPTY SLOT

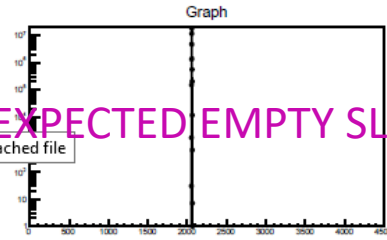
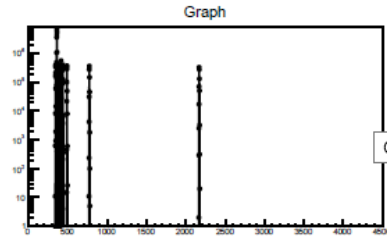
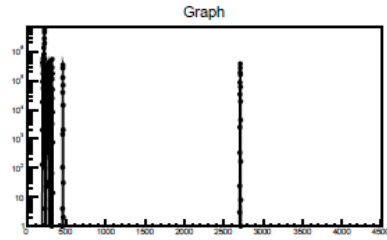
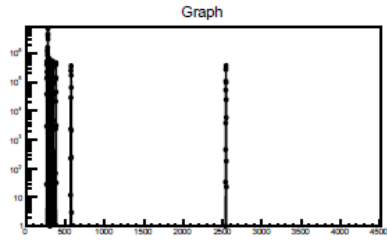
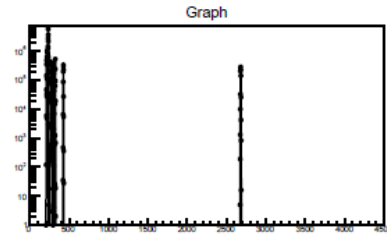
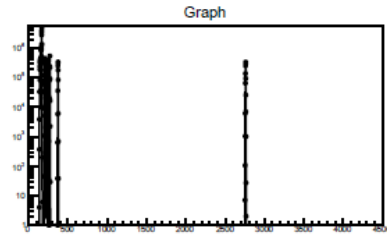
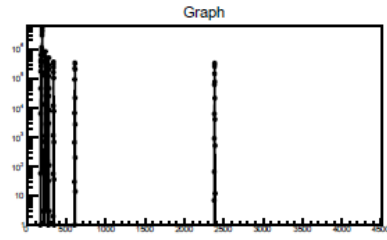
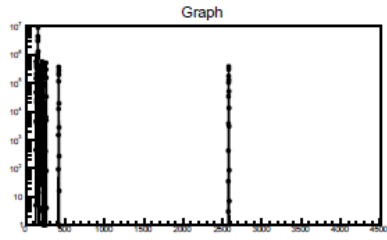
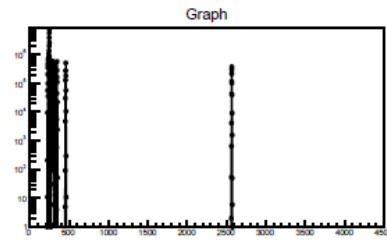
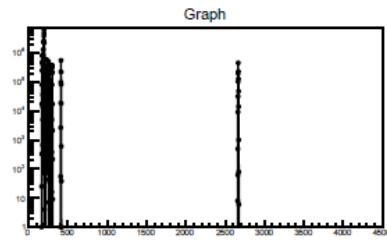
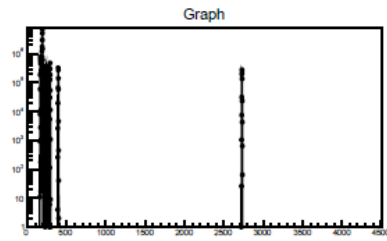
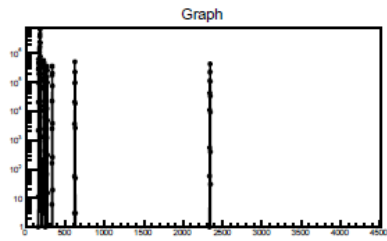
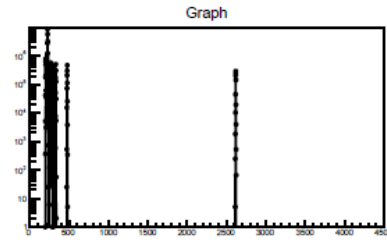
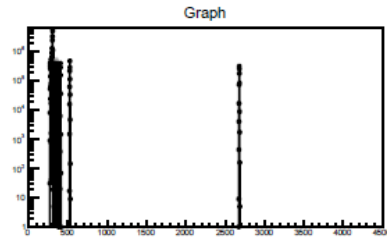
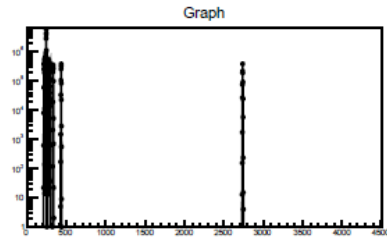
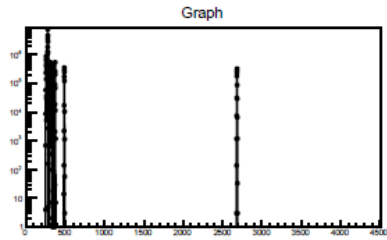
MPD9 [3 × 4-slots BP short to long HDMIIs via adapter]

EXPECTED EMPTY Slots



Good but signal inverted => probably just one bit in configuration / analysis to flip
Looks like expected value = 4096 (12 bits) – obtained value

MPD10 [3 × 5-slots BP: short to long HDMIIs vi 3-to-4 Patch Panel]

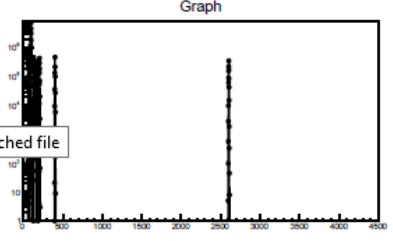
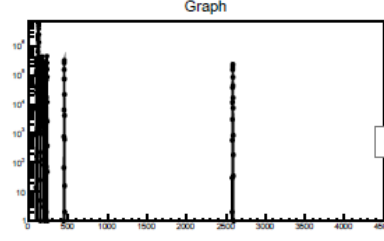
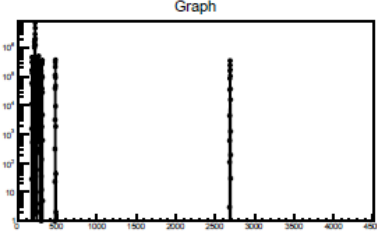
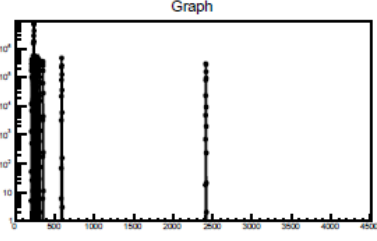
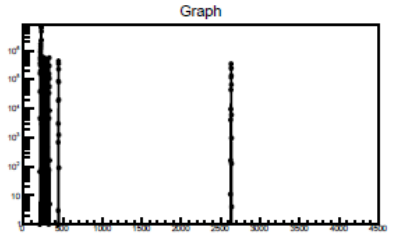
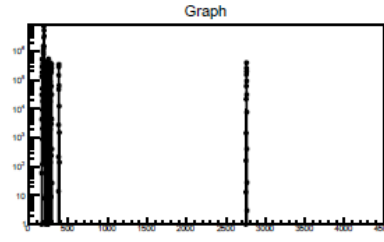
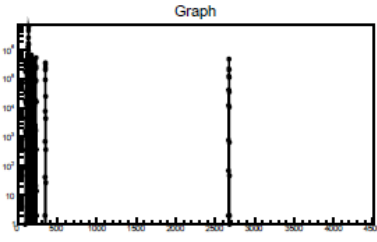
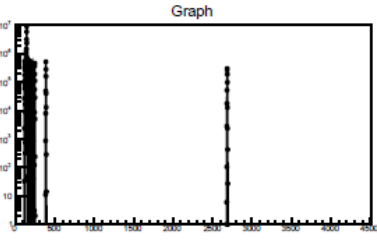
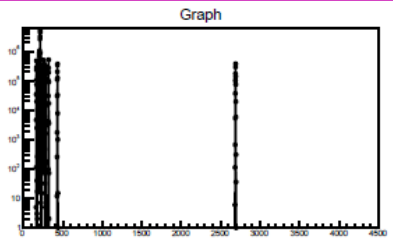
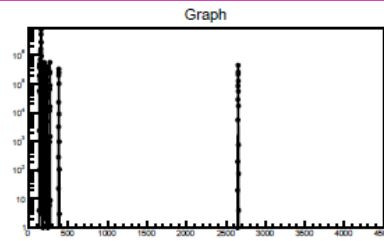
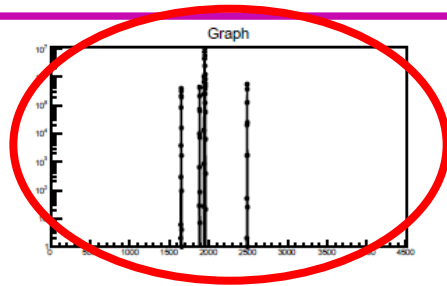
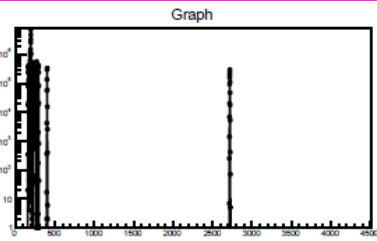
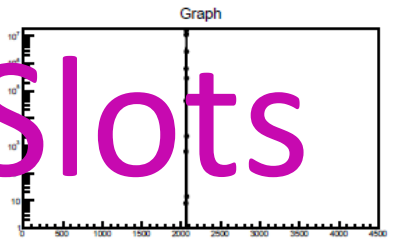
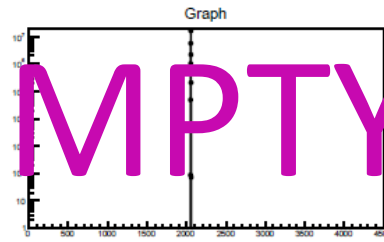
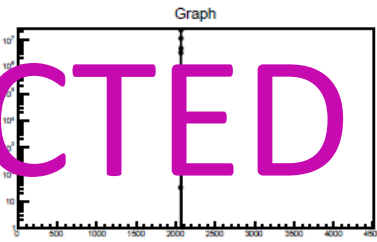
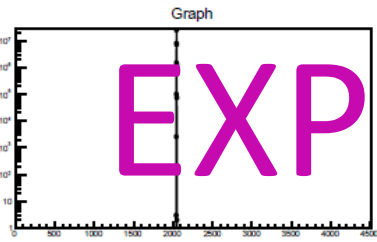


EXPECTED EMPTY SLOT

Open the attached file

MPD13 [3 × 4-slots BP short to long HDMI via adapter]

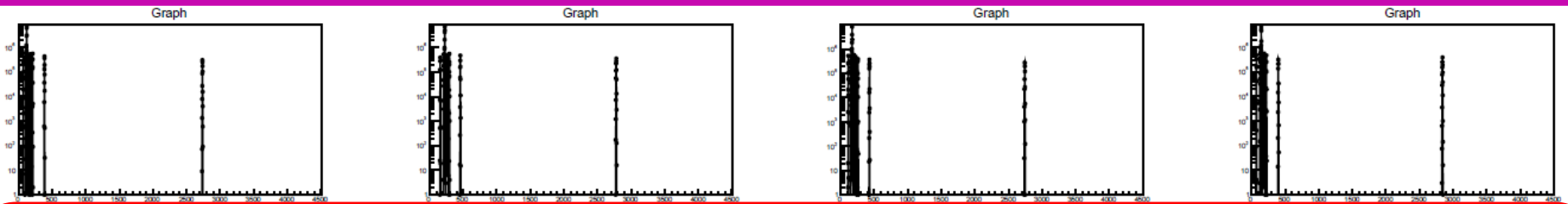
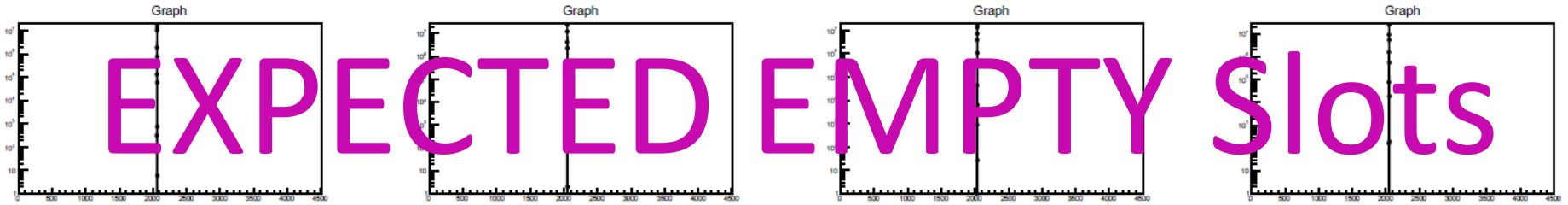
EXPECTED EMPTY Slots



Open the attached file

MPD15 [3 × 5-slots BP: short to long HDMIIs vi 3-to-4 Patch Panel]

EXPECTED EMPTY Slots



Problem with back plane?, HDMI cables? Connection?, configuration? To be checked

